[Released on the Web]

Parameter File

for the PG-FP5 Flash Memory Programmer

Outline

We will release a new parameter file for use with the PG-FP5 flash memory programmer.

This parameter file is a text file which contains the information required to use the PG-FP5 for writing to certain target MCUs.

Refer to the URL below for an overview of the PG-FP5.

http://www.renesas.com/pg_fp5

1. New Parameter File

RX family

PR5-R5F524T V1.00 (for the RX24T group)

Supported MCUs: R5F524T8 and R5F524TA

2. Obtaining the File

To obtain the parameter file, download it from the download site below.

The above information will be available from April 5.

http://www.renesas.com/pg_fp5_download

Refer to chapter 3 of the PG-FP5 User's Manual for details of how to install the file.

3. Required Firmware, FPGA Data, and Programming GUI

Obtain the release notes for the parameter file from the documentation page at the URL given below and consult them to confirm that you have the firmware, FPGA data, and programming GUI versions required to use the parameter file. This information will be available from April 5.

http://www.renesas.com/pg_fp5_documentation

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Apr. 01, 2016	-	First edition issued

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■Inquiry

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