[Released on the Web]

Parameter Files for the PG-FP5 Flash Memory Programmer

Outline

We are revising the parameter file for the PG-FP5 flash memory programmer.

A parameter file is a text file that contains information on the target controller required to use the PG-FP5 for programming. For an overview of the PG-FP5, see the following URL:
https://www.renesas.com/pg-fp5

1.   Parameter Files to Be Revised

1.1   Addition of Supported MCUs

➢   Parameter file PR5-R5F5130 for the RX130 group will be revised from V1.01 to V1.02.

MCUs to be added: R5F51305B, R5F51306B

2.   Obtaining the Program

Download a required parameter file from the following URL:
The parameter file will be available from July 20.
https://www.renesas.com/pg_fp5_download

For details of how to install the file, see chapter 3 of PG-FP5 User's Manual: Common, at the following URL:
https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut2922
PG-FP5 V2.15 Flash Memory Programmer User's Manual: Common

3.   Required Firmware, FPGA Data, and Programming GUI

To confirm that you have the firmware, FPGA data, and programming GUI versions required to use the parameter file, see the Release Note for the parameter file at the following URL:

https://www.renesas.com/pg_fp5_documentation
Parameter Files for PG-FP5 Release Note
This information will be available from July 20.
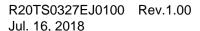
4.   Remarks

Production of PG-FP5 ended with acceptance of final orders in May 2018.

Addition of supported MCUs for the PG-FP5 is discontinued as of this announcement.

For details about PG-FP5 and its successor PG-FP6, refer to the following issue of Tool News.

https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ts0289
[Notification] Acceptance for Final Orders for PG-FP5 Flash Memory Programmer, Introducing PG-FP6 as Successor

Revision History

Rev.	Date	Description	
		Page	**Summary**
1.00	Jul. 16, 2018	-	First edition issued

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■Inquiry

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