

RENESAS TOOL NEWS on November 16, 2010: 101116/tn2

The Device File Updater Specific to the C Compiler Package for the M16C Series and R8C Family of MCUs Revised to V.1.07

We have revised the device file updater specific to the C compiler package for the M16C series and R8C family of MCUs (M3T-NC30WA) from V.1.06 to V.1.07.

For an overview of device file updaters, see: <https://www.renesas.com/dfu>

The above URL is one of our global sites.

1. Supporting C Compiler and Its Versions

The C compiler package for the M16C series and R8C family (M3T-NC30WA) V.5.42 Release 00 and later

Note, however, that to install the device file updater V.1.07 in your PC, you must install the V.5.45 Release 00 or later product of the above compiler package in advance.

2. Descriptions of Revision

2.1 CPU Groups Added to the Support Line

In this revision, no CPU groups have been added to the support line.

2.2 Problems Fixed

In the device file updater V.1.07, the following problems have been fixed, which arise in C startup files created by the supporting C compiler package:

(1) Problem that the standard I/O functions of the standard library cannot operate properly. For details, see FAQ 1000137 at:

http://www.renesas.com/support/faqs/faq_results/Q1000000-Q9999999/tools/coding_tools/compiler_100706a.jsp

(2) Problem with the definitions of interrupt vector functions of UART1
For details of the problem, see RENESAS TOOL NEWS Document No. 101116/tn1.

This item of news is also accessible at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=101116tn1>

The Web page will be opened on December 6.

- (3) Problem that if the M16C/63 group is selected in the CPU Groups list and 512 KB selected in the ROM Size list, startup files are created incorrectly.

3. How to Obtain the Revised Product

Download the device file updater V.1.07 specific to the C compiler package for the M16C series and R8C family (M3T-NC30WA) from:

https://www.renesas.com/dfu_download

Then install it. This Web site will be opened on December 6.

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