

A Note on Using the High-performance Embedded Workshop --On Customizing Linking Order--

Please take note of the following problem in using the High-performance Embedded Workshop, an integrated development workshop:

- On customizing linking order
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1. Products Concerned

The versions of the High-performance Embedded Workshop included in the following compiler packages are concerned:

- (1) C/C++ compiler package V.8.00 Release 02 through V.9.00 Release 03
for the SuperH RISC engine family
- (2) C/C++ compiler package V.6.00 Release 02 through V.6.01 Release 01
for the H8, H8S, and H8SX families

2. Description

If a build is performed under the following conditions: (1) a binary file is added to the linking objects as one of the options of the optimizing linker,*1 and (2) customization of linking order is made enabled using the Customize Linking Order function,*2 the contents of the binary file added is overwritten in error with the output of the optimizing linker.

NOTES:

- *1. To add a binary file, open the Build menu and select the Renesas XXX Standard Toolchain command to open the Renesas XXX Standard Toolchain dialog box. Next, click its Link/Library tab. Then select Input in the pull-down menu of the Category combo box and Binary Files in that of the Show entries for combo box to add a binary file.

- *2. To make customization of linking order enabled, open the Build menu and select Linkage Order command to open the Linkage Order dialog box. Then check the Use custom linkage order check box.

3. **Workaround**

Don't perform any build under the conditions described in Section 2. When a binary file is added, perform a build without enabling customization of linking order. If customization of linking order remains disabled, binary files cannot be overwritten.

4. **Schedule of Fixing the Problem**

We plan to fix this problem in the next versions of the product.

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