

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A029A/E	Rev.	1.00
Title	RZ/G1E User's Manual:Hardware Correction (PFC)		Information Category	Technical Notification		
Applicable Product	RZ/G Series RZ/G1E	Lot No.	Reference Document	RZ/G1E User's Manual: Hardware Rev.1.00 (R01UH0544EJ0100)		
		All lots				

This technical update describes document correction for RZ/G1E.

[Summary]

RZ/G1E User's Manual: Hardware Correction (manual errata)

Wrong AVB_Capture signal setting removed.

[Products]

RZ/G1E

[Note]

There is no specification change (document correction only).

[Correction]

(Gray highlighted parts (abcd) are corrected.)

1. Section 5.3.22 Peripheral Function Select Register 13 (IPSR13)

Table 5.2 Correspondence between Function Signals and Register Bit Settings

Current (from):

GPIO (GP-Set- Value==0)	Peripheral-Module-(GP-Set-Value==1) Function-Selected-by-IP-Bits								GPIO/ Peripheral- Function- Selecting- Bit	
	Function-1 (IP-Set- Value==0)	Function-2 (IP-Set- Value==1)	Function-3 (IP-Set- Value==2)	Function-4 (IP-Set- Value==3)	Function-5 (IP-Set- Value==4)	Function-6 (IP-Set- Value==5)	Function-7 (IP-Set- Value==6)	Function-8 (IP-Set- Value==7)	Peripheral- Function- Selecting- Bit	Peripheral- Function- Selecting- Bit
...
GP5[11]	SSI_SCK1	SCIF1_RXD_B	IIC0_SCL_C	VI1_CLK	CAN0_RX_D	AVB_AVTP_CAP TURE	ETH_CRS_DV _B	-	GP5[11]	IP12[20:18]
...

Correction (to):

GPIO (GP-Set- Value==0)	Peripheral-Module-(GP-Set-Value==1) Function-Selected-by-IP-Bits								GPIO/ Peripheral- Function- Selecting- Bit	
	Function-1 (IP-Set- Value==0)	Function-2 (IP-Set- Value==1)	Function-3 (IP-Set- Value==2)	Function-4 (IP-Set- Value==3)	Function-5 (IP-Set- Value==4)	Function-6 (IP-Set- Value==5)	Function-7 (IP-Set- Value==6)	Function-8 (IP-Set- Value==7)	Peripheral- Function- Selecting- Bit	Peripheral- Function- Selecting- Bit
...
GP5[11]	SSI_SCK1	SCIF1_RXD_B	IIC0_SCL_C	VI1_CLK	CAN0_RX_D	Reserved	ETH_CRS_DV _B	-	GP5[11]	IP12[20:18]
...

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