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## **RENESAS TECHNICAL UPDATE**

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Product Category	MPU/MCU		Document No.	TN-RZ*-A034A/E	Rev.	1.00
Title	RZ/G Series User's Manual: Hardware Correction (Electrical Characteristics)		Information Category	Technical Notification		
Applicable Product	RZ/G Series, RZ/G1N	Lot No.				
		All lots	Reference Document	RZ/G Series User's Manual: Hardware Rev.1.00 Sep. 30, 2016 (R01UH0543EJ0100)		

There is a following document correction about the RZ/G series products.

[Summary]

RZ/G Series User's Manual: Hardware Correction (manual errata)

[Products]

RZ/G1N

[Note]

This document includes user's manual correction of the RZ/G1N.

[Correction]

Table 63.21.2, QSPI Signal Timing (RZ/G1M, N and E) (page 63-68):

Data output hold time, tOH is corrected from -1.4 to -3.0.

(Gray highlighted parts (abcd) are corrected.)

Current (from):

Notes: Tcyc is a cycle time of QSPI clock (QSPIφ).

- 1. For RZ/G1M only.
- 2. For RZ/G1N and E.

Change (to):

Notes: Tcyc is a cycle time of QSPI clock (QSPIφ).

- 1. For RZ/G1M and N.
- 2. For RZ/G1E only.

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