**RENESAS TECHNICAL UPDATE**

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<tbody>
<tr>
<td>Title</td>
<td>Initialization setting issue of CRU MIPI CSI2 Link Registers</td>
<td>Information Category</td>
<td>Technical Notification</td>
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<tr>
<td>Applicable Product</td>
<td>RZ/G2L Group, RZ/G2LC Group, RZ/V2L Group</td>
<td>Lot No.</td>
<td>Reference Document</td>
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### [Phenomenon]

At the time of initial setting, the following MIPI-CSI2 Link register setting may not be reflected correctly.

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<tr>
<th>Register Name</th>
<th>Abbreviation</th>
<th>Address</th>
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<tr>
<td>Module Control Register 0 CSI2nMCT0</td>
<td>CSI2nMCT0</td>
<td>H’0_1083_0410</td>
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<tr>
<td>Module Control Register 2 CSI2nMCT2</td>
<td>CSI2nMCT2</td>
<td>H’0_1083_0418</td>
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<td>EPD Option Control Register</td>
<td>CSI2nEPCT</td>
<td>H’0_1083_0440</td>
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<tr>
<td>Receive Data Type Enable Low Register</td>
<td>CSI2nDTEL</td>
<td>H’0_1083_0460</td>
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<tr>
<td>Receive Data Type Enable High Register</td>
<td>CSI2nDTEH</td>
<td>H’0_1083_0464</td>
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<tr>
<td>Generic Short Packet Control Register</td>
<td>CSI2nGSCT</td>
<td>H’0_1083_0680</td>
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### [User's Manual Update]

Additional operations are added to **"35.3.1, Figure 35.33 Reception Start Flow for the MIPI CSI-2 Input"** to avoid this issue.

<Before>

```plaintext
Initializing the LINK (MIPI CSI-2 input)

Enable LINK reception
CSI2nMCT3 = 00001_00001 (Set the [0]RXEN bit to 1)

Release the CRU from the reset state for the third time (O-PHY)
CPG_RST_CRU.CRU_CMN_RSTB = 1

≥1 ms wait

Start receiving data from the sensor

End of reception
```
<After>

1. Initializing image processing (MIPI CSI-2 input)

2. Enable image processing interrupt
   CRUnIE

3. Enable image processing reception
   ICnEN.ICnEN = 0x1

4. Initializing the LINK (MIPI CSI-2 input)

5. VCLK OFF:
   CPG_CLKON_CRU.CRU.VCLK = 0  \textbf{Note 1}

6. Enable links reception
   CSI2#MCT3 = 0x0000_0001  (Set the [0]RXEN bit to 1)

7. VCLK ON:
   CPG_CLKON_CRU.CRU.VCLK = 1  \textbf{Note 2}

8. Release the CRU from the reset state for the third time (D-PHY)
   CPG_RST_CRU.CRU.CMN_RSTB = 1

9. \geq 1 ms wait

10. Start receiving data from the sensor

11. End of reception

\textbf{Note 1}: After VCLK OFF setting, confirm whether VCLK is off by reading CPG_CLKMON_CRU.CLU1.ON=0.

\textbf{Note 2}: After VCLK ON setting, confirm whether VCLK is on by reading CPG_CLKMON_CRU.CLU1.ON=1.