

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

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RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU		Document No.	TN-H8*-A389A/E	Rev.	1.00
Title	H8S/2112 Group: Modification regarding the power-on reset (POR) in the hardware manual		Information Category	Technical Notification		
Applicable Product	H8S/2112 Group	Lot No.	Reference Document	H8S/2112 Group Hardware Manual Rev. 1.00 (REJ09B0451-0100)		
		All lots				

Thank you for your consistent patronage of Renesas semiconductor products.

We would like to inform you of modification regarding the power-on reset (POR) in the H8S/2112 Group Hardware Manual.

H8S/2112 Group: Section 4 Resets
 Section 25 List of Registers
 Section 26 Electrical Characteristics

Table 1 Modification in the H8S/2112 Group Hardware Manual

Item	Page	Revision
4.5 Power-on Reset	85	[Before change] After the VCC is turned on with the RES pin held low, namely in the state of pin reset, if the RES pin is driven high in the state that the VCC stays higher than the level of Vpor, the power-on reset function is disabled and a reset exception handling starts before entering the stabilization time. [After change] After the VCC is turned on with the RES pin held low, namely in the state of pin reset, if the RES pin is driven high in the state that the VCC stays higher than the level of Vpor, the power-on reset function is disabled and a reset exception handling starts before entering the power-on reset time.
4.5 Power-on Reset	85	[Before change] In this case, when the VCC reaches or exceeds the level of Vpor and stays at the level after the elapse of the stabilization time, the power-on reset is canceled and a reset exception handling starts. [After change] In this case, when the VCC reaches or exceeds the level of Vpor and stays at the level after the elapse of the power-on reset time, the power-on reset is canceled and a reset exception handling starts.
25.3 Register States in Each Operating Mode RSTSR in the RESET column	784	[Before change] Initialized [After change] Initialized* Note: * The PORF bit in RSTSR is only initialized by a pin reset.
Table 26.14 Electrical Characteristics of the Power-on Reset Circuit Power-on reset time in the Max. column	854	[Before change] 80 [After change] 60