Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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Date: Oct.02, 2009

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU	Document No.	TN-R8C-A008A/E	1.00		
Title	Correction of Notes on Power-on Reset Circuit Characteristics		Information Category	Technical Notification		
Applicable Products	R8C/32A Group, R8C/33A Group, R8C/35A Group, R8C/36A Group, R8C/38A Group, R8C/3GA Group R8C/3JA Group	Lot No.	Reference Document			

1. Information

Notes on power-on reset circuit characteristics in the hardware manuals for the R8C/32A, 33A, 35A, 36A, 38A, 3GA, and 3JA Groups have been partially deleted.

2. Reference Documents

• R8C/32A Group Hardware Manual Rev.0.20 (REJ09B0458-0020)

• R8C/33A Group Hardware Manual Rev.0.20 (REJ09B0455-0020)

• R8C/35A Group Hardware Manual Rev.0.40 (REJ09B0407-0040)

• R8C/36A Group Hardware Manual Rev.0.20 (REJ09B0480-0020)

• R8C/38A Group Hardware Manual Rev.0.10 (REJ09B0485-0010)

• R8C/3GA Group Hardware Manual Rev.0.20 (REJ09B0472-0020)

• R8C/3JA Group Hardware Manual Rev.0.10 (REJ09B0508-0010)

3. Description

The note below has been deleted from the trth symbol in the Power-on Reset Circuit table found in the Electrical Characteristics chapter. Adhere to the trth standard regardless of the external power Vcc voltage.

• Deleted note

This condition (external power VCC rise gradient) does not apply if VCC ≥ 1.0 V.

Table. Power-on Reset Circuit (R8C/3x Series)

Symbol	Parameter	Condition	Standard			Unit
			Min.	Тур.	Max.	Uill
trth	External power Vcc rise gradient (Note)		0	-	50000	mV/msec

Note:

This condition (external power Vcc rise gradient) does not apply if Vcc \geq 1.0 V.

