
ISL70218SEHTotal Dose Testing

Introduction

This document reports the results of low and high dose rate total dose testing of the [ISL70218SEH](#) dual operational amplifier (op amp). The test was conducted to provide an assessment of the total dose hardness of the ISL70218SEH. Samples were irradiated under bias and with all pins grounded.

The ISL70218SEH is available in two versions differing in total ionizing dose acceptance testing. The ISL70218SEH is acceptance tested on a wafer-by-wafer basis to 50krad(Si) at Low Dose Rate (LDR) (0.01rad(Si)/s) and to 100krad(Si) at High Dose Rate (HDR) (50 – 300rad(Si)/s). The ISL70218SRH is acceptance tested on a wafer by wafer basis to 100krad(Si) at HDR (50 – 300rad(Si)/s) only. The “EH” and “RH” devices use the same design and silicon and differ in screening flows only.

Related Literature

For a full list of related documents, visit our website:

- [ISL70218SEH](#) device page
- MIL-STD-883 Test Method 1019

Part Description

The ISL70218SEH is a low-power precision dual op amp optimized for single-supply applications. The part features a common mode input voltage range extending to 0.5V below the negative supply rail, rail-to-rail differential input voltage range, and rail-to-rail output voltage swing, which makes it ideal for single-supply applications where input operation at ground is important. The ISL70218SEH features low power, low offset voltage, and low temperature drift, making it ideal for applications requiring both high DC accuracy and AC performance.

The ISL70218SEH is designed to operate over a single supply range of 3V to 36V or a split supply voltage range of +1.8V/-1.2V to ± 18 V. Applications include precision instrumentation, data acquisition, and precision power supply controls. The ISL70218SEH is available in a 10 Ld hermetic ceramic flatpack and operates across the extended temperature range of -55°C to +125°C.

1. Test Description

1.1 Irradiation Facilities

LDR gamma ray testing was performed at 0.01rad(Si)/s using the Renesas Electronics America Palm Bay panoramic irradiator. HDR gamma ray testing was performed at 65rad(Si)/s using a Gammacell 220 irradiator located in the Renesas Palm Bay, Florida facility.

1.2 Test Fixturing

[Figure 1](#) shows the configuration used for biased irradiation.

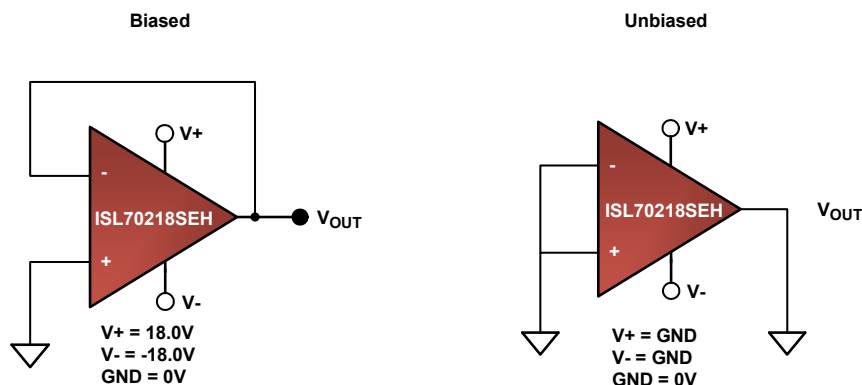


Figure 1. Irradiation Bias Configuration for the ISL70218SEH per SMD 5962-12222.

1.3 Characterization Equipment and Procedures

All electrical testing was performed outside the irradiator using production Automated Test Equipment (ATE) with datalogging at each downpoint. Downpoint electrical testing was performed at room temperature.

1.4 Experimental Matrix

Total dose irradiation proceeded in accordance with the guidelines of MIL-STD-883 Test Method 1019. The experimental matrix consisted of four samples irradiated at LDR under bias, four samples irradiated at LDR with all pins grounded, and four samples irradiated at HDR under bias.

Samples of the ISL70218SEH were drawn from preproduction lots WLH4WAAAA and WLH4WAAB and were packaged in the production 10 Ld solder-sealed flatpack (CDFP3-F10). Samples were processed through the standard burnin cycle before irradiation, as required by MIL-STD-883, and were screened to the ATE limits at room temperature prior to the test.

1.5 Downpoints

Downpoints for the LDR tests were 0, 50, 100, and 150krad(Si). Downpoints for the HDR test were 0 and 100krad(Si).

2. Attributes Data

Table 1. ISL70218SEH Total Dose Test Attributes Data

| Part | Dose Rate (Note 1) | Bias | Sample Size | Downpoint | Pass (Note 2) | Rejects |
|-------------|---|--------|-------------|-----------------|------------------------------------|---------|
| ISL70218SEH | LDR | Biased | 4 | Pre-irradiation | 4 | 0 |
| | | | | 50krad(Si) | 4 | 0 |
| | | | | 100krad(Si) | 4 | 0 |
| | | | | 150krad(Si) | 4 | 0 |

Table 1. ISL70218SEH Total Dose Test Attributes Data (Continued)

| Part | Dose Rate (Note 1) | Bias | Sample Size | Downpoint | Pass (Note 2) | Rejects |
|-------------|---|----------|-------------|-----------------|------------------------------------|---------|
| ISL70218SEH | LDR | Grounded | 4 | Pre-irradiation | 4 | 0 |
| | | | | 50krad(Si) | 4 | 0 |
| | | | | 100krad(Si) | 4 | 0 |
| | | | | 150krad(Si) | 4 | 0 |
| ISL70218SEH | HDR | Biased | 4 | Pre-irradiation | 4 | 0 |
| | | | | 100krad(Si) | 4 | 0 |

Notes:

- 'HDR' indicates high dose rate and 'LDR' indicates low dose rate.
- 'Pass' indicates a sample that passes all post-irradiation SMD limits.

2.1 Variables Data

The plots in [Figures 2](#) through [20](#) show variables data. The plots show the median and minimum/maximum error bars of key parameters at each downpoint. We chose to plot the median for these parameters due to the small sample sizes of four per experimental cell. A discussion of each parameter's total dose response is presented in ["Discussion and Conclusion" on page 13](#).

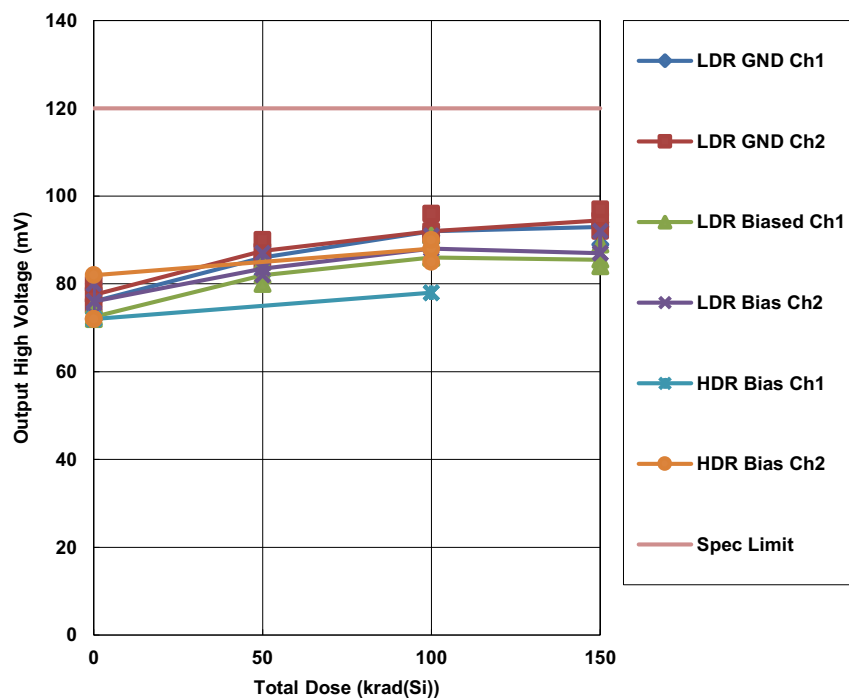


Figure 2. ISL70218SEH output high voltage (see ["Discussion and Conclusion" on page 13](#) for a definition), Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 120mV maximum.

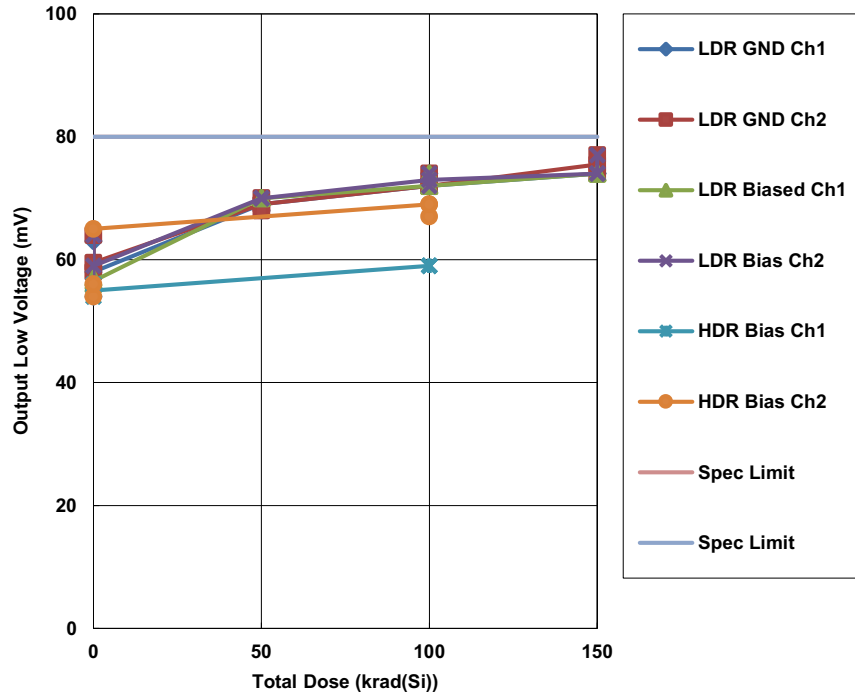


Figure 3. ISL70218SEH output low voltage (see [“Discussion and Conclusion” on page 13](#)), Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 80mV maximum.

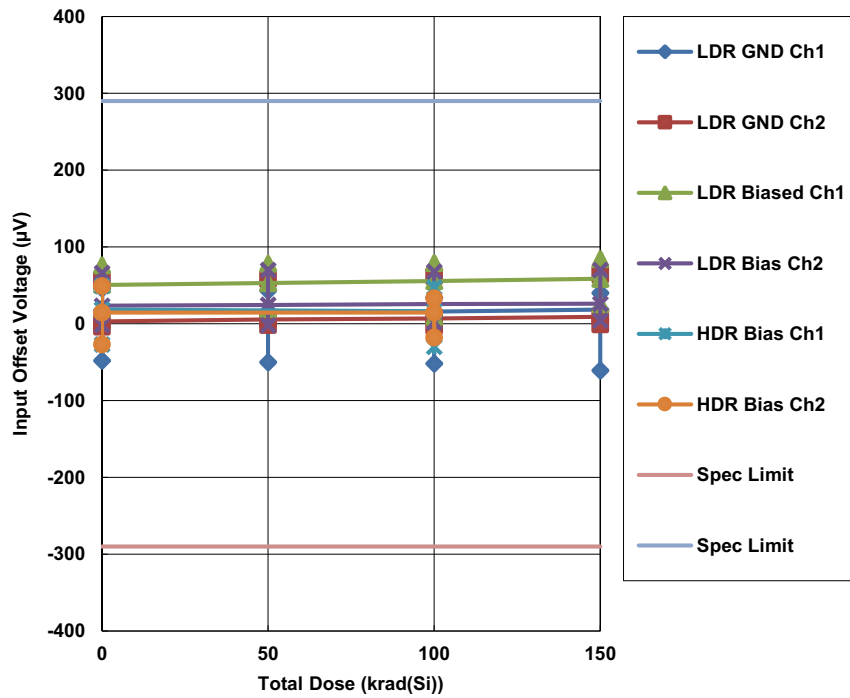


Figure 4. ISL70218SEH input offset voltage, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limits are -290µV to 290µV.

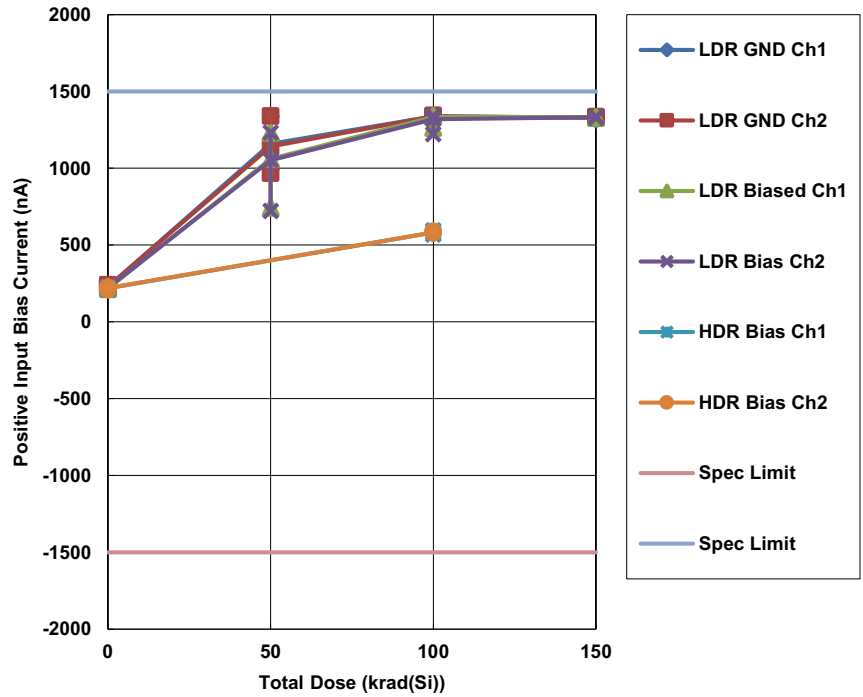


Figure 5. ISL70218SEH positive input bias current, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limits are -1500nA to 1500nA.

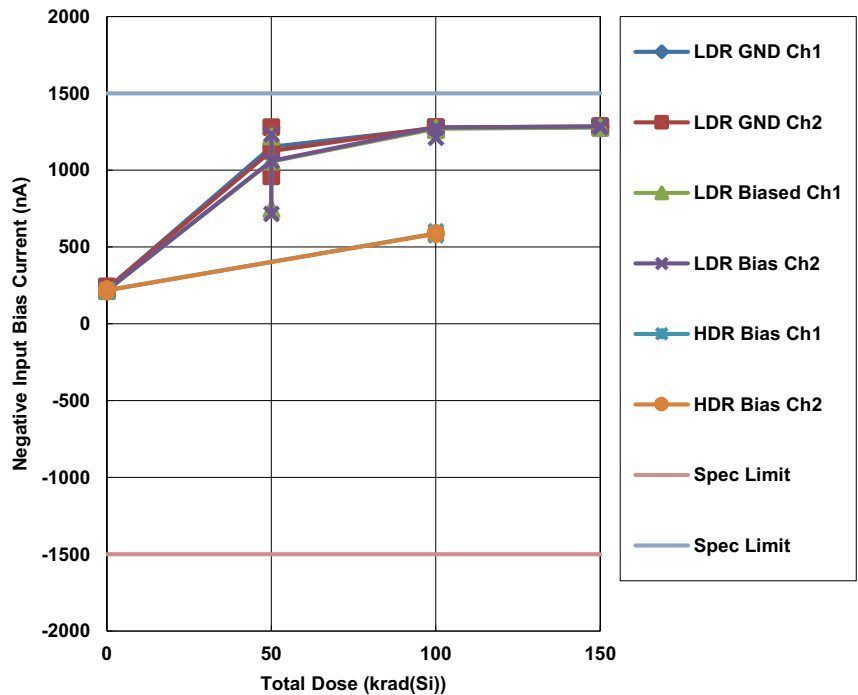


Figure 6. ISL70218SEH negative input bias current, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limits are -1500nA to 1500nA.

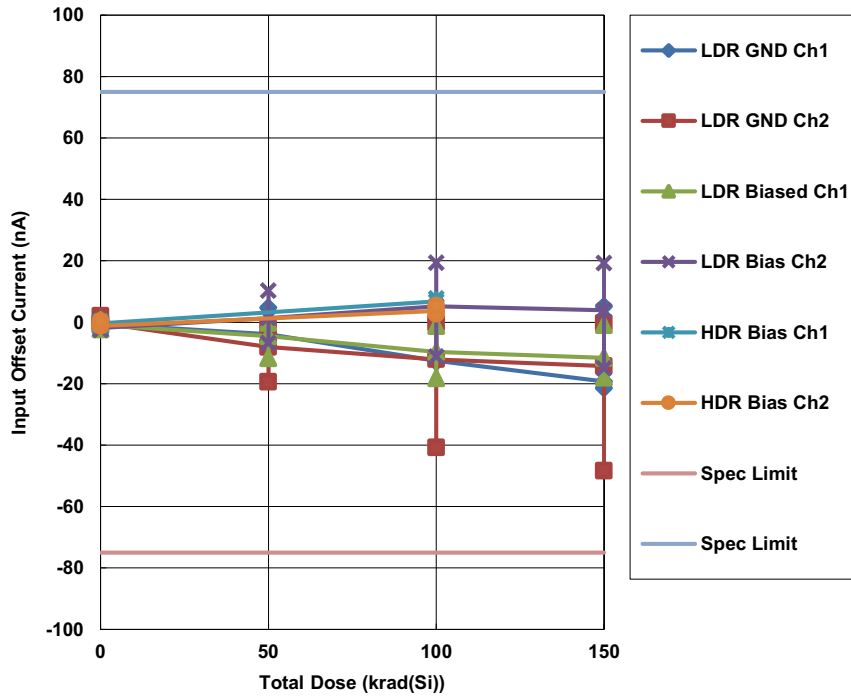


Figure 7. ISL70218SEH input offset current, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limits are -75nA to 75nA.

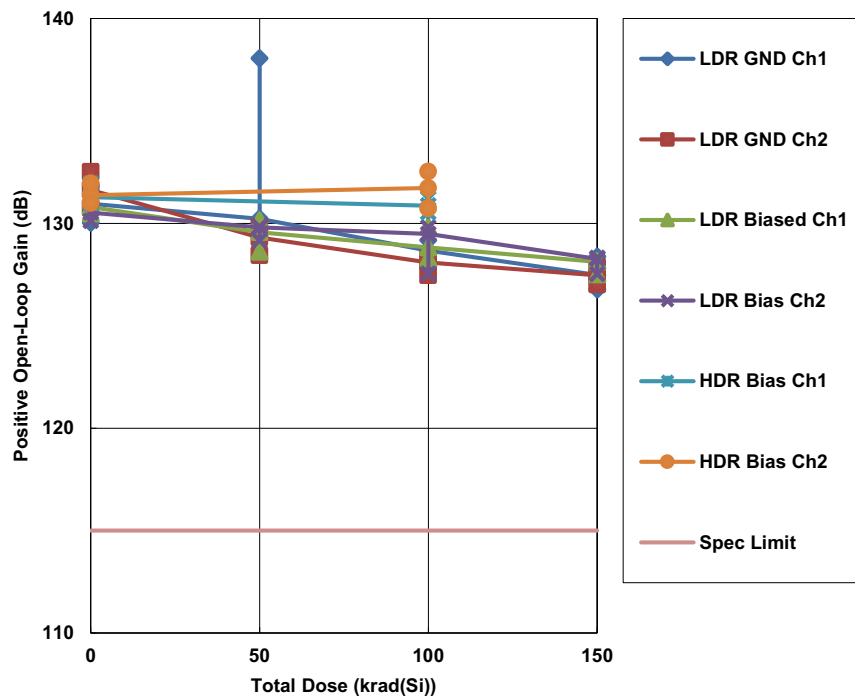


Figure 8. ISL70218SEH positive open-loop gain, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 115dB minimum.

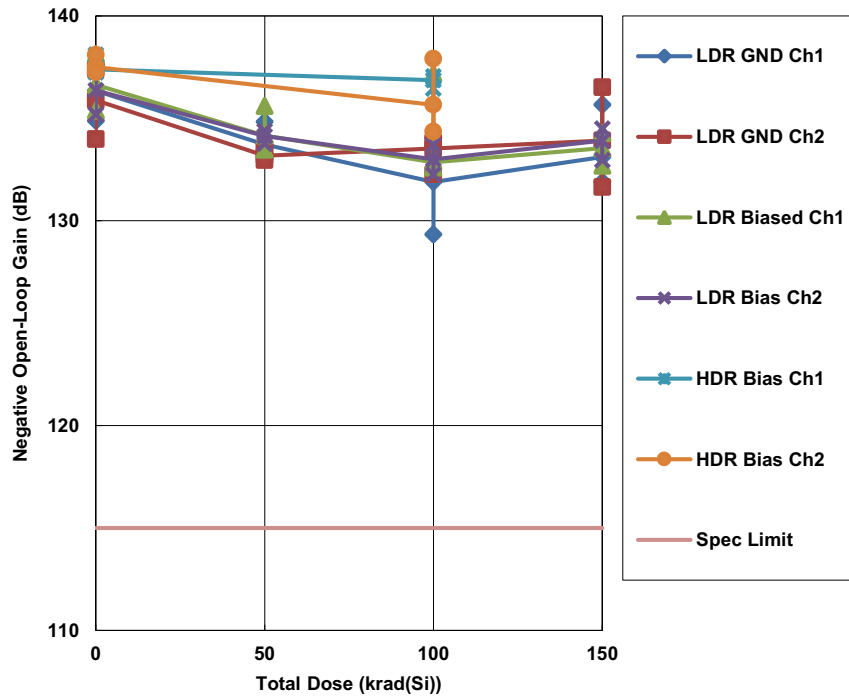


Figure 9. ISL70218SEH negative open-loop gain, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 115dB minimum.

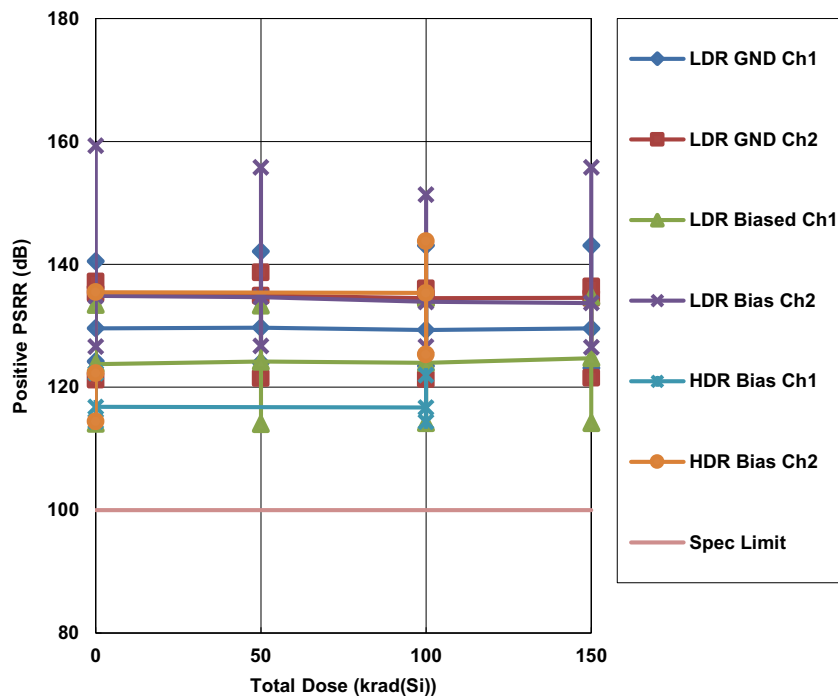


Figure 10. ISL70218SEH positive power supply rejection ratio, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 100dB minimum.

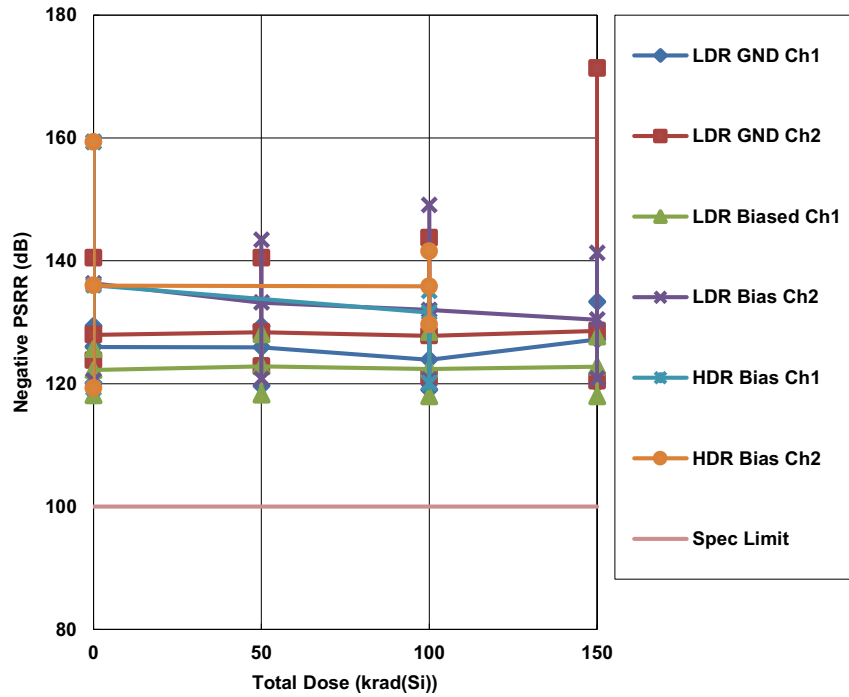


Figure 11. ISL70218SEH negative power supply rejection ratio, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 100dB minimum.

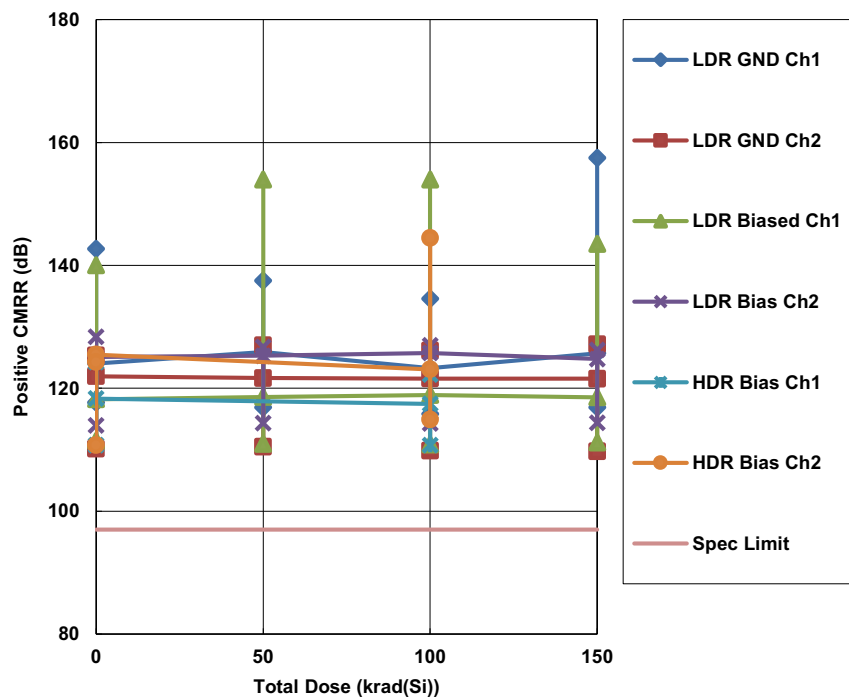


Figure 12. ISL70218SEH positive common-mode rejection ratio, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 97dB minimum.

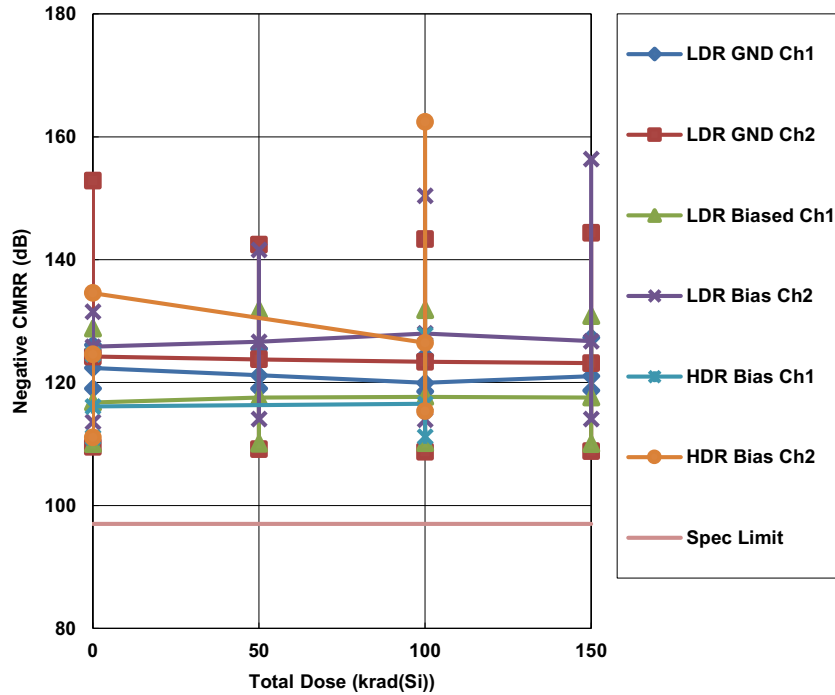


Figure 13. ISL70218SEH negative common-mode rejection ratio, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 97dB minimum.

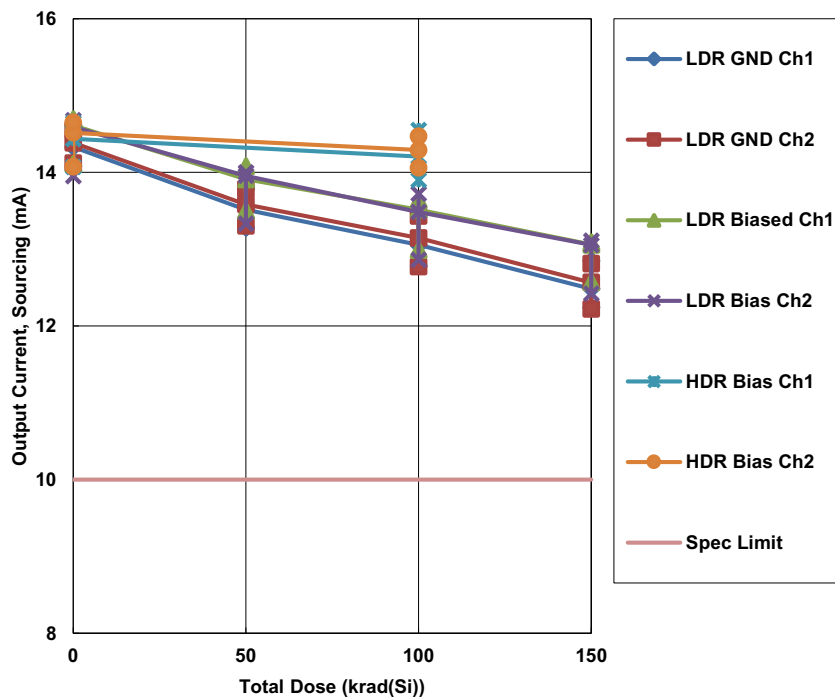


Figure 14. ISL70218SEH output current, sourcing, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 10mA minimum.

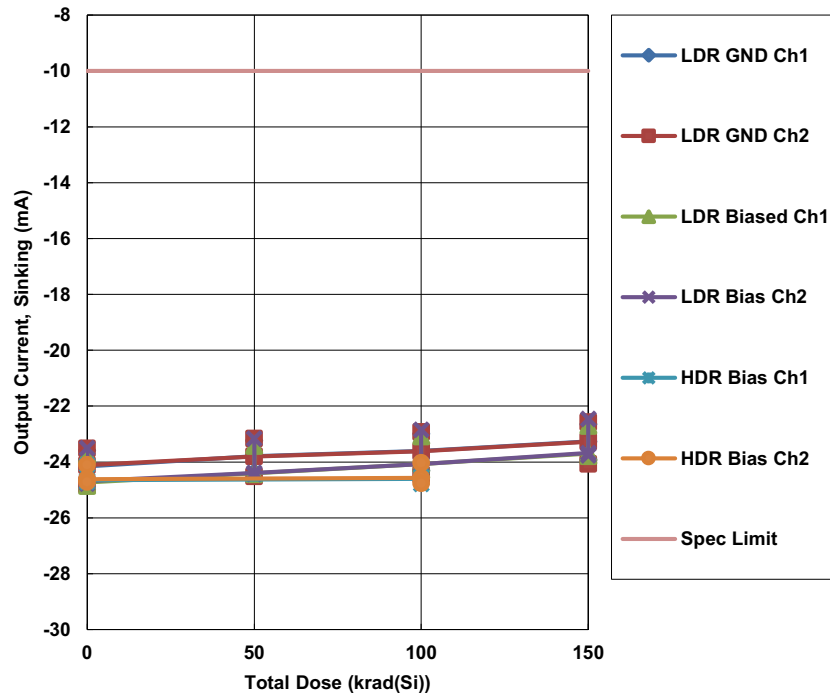


Figure 15. ISL70218SEH output current, sinking, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is -10mA maximum.

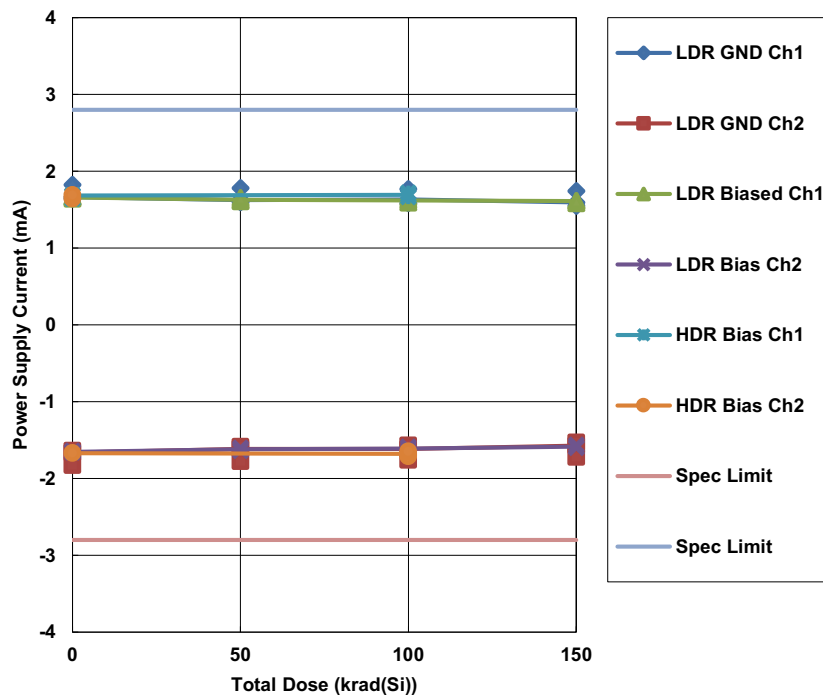


Figure 16. ISL70218SEH positive and negative supply current, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limits are 2.8mA maximum (positive supply current) and -2.8mA maximum (negative supply current).

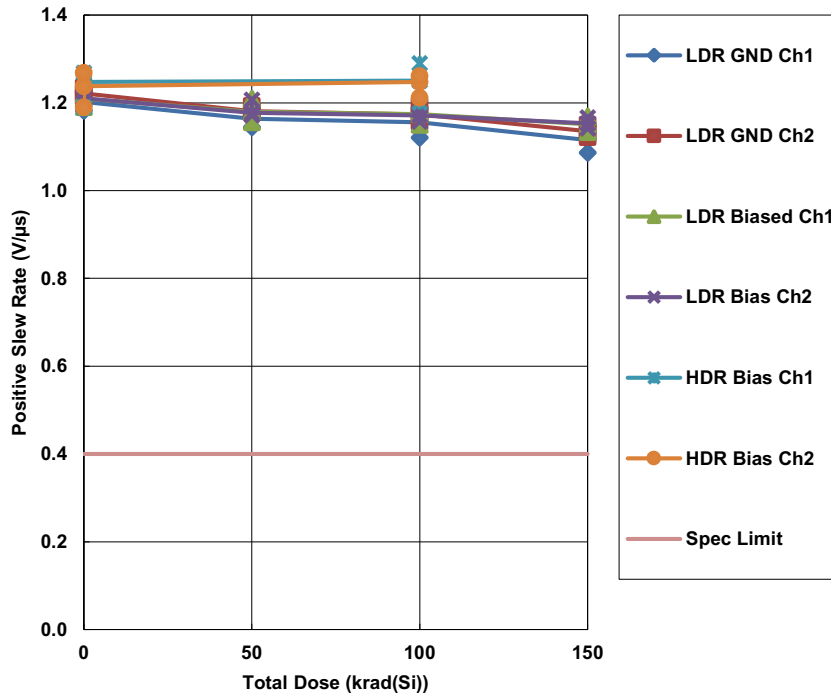


Figure 17. ISL70218SEH positive slew rate, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 0.4V/μs minimum.

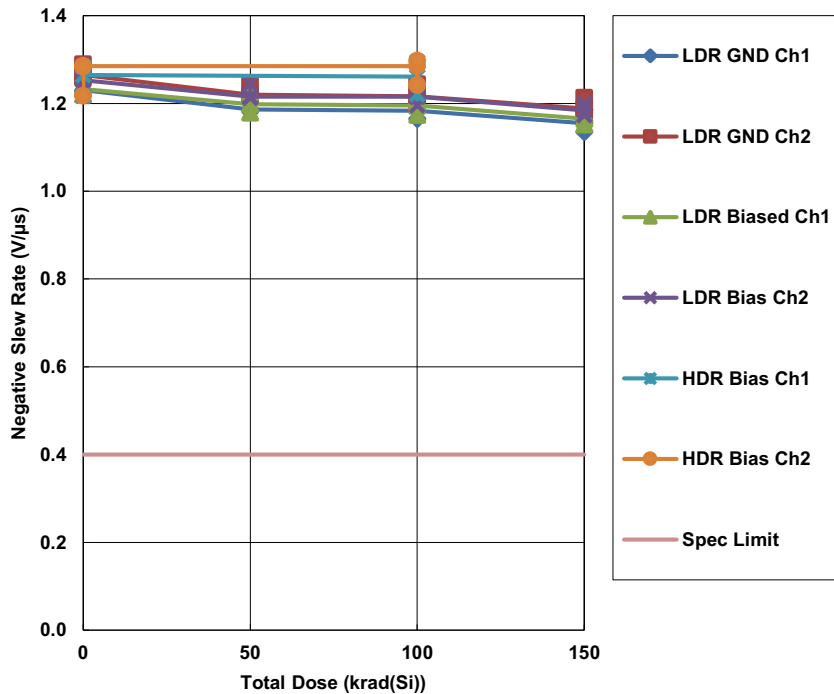


Figure 18. ISL70218SEH negative slew rate, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 0.4V/μs minimum.

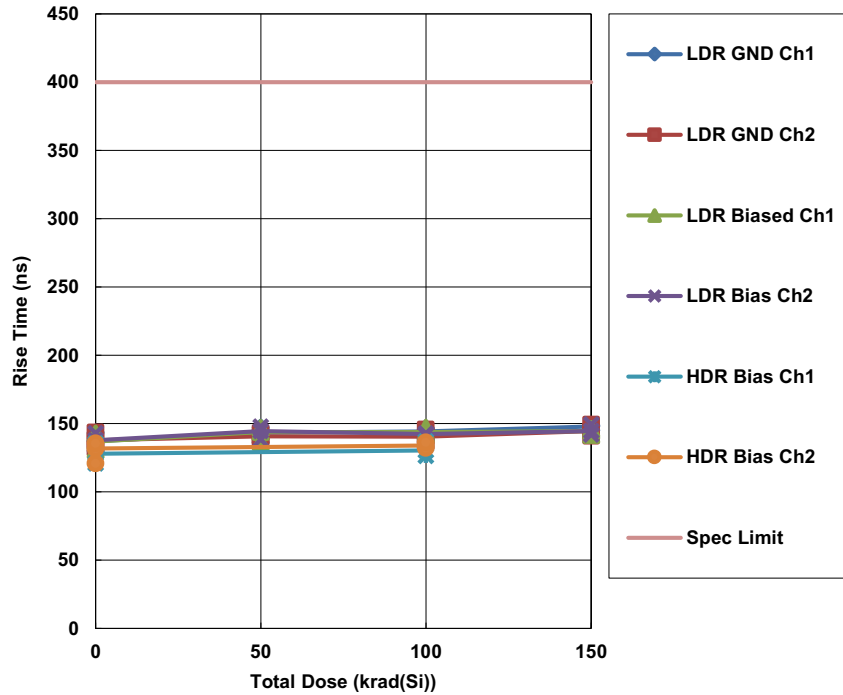


Figure 19. ISL70218SEH rise time, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 400ns maximum.

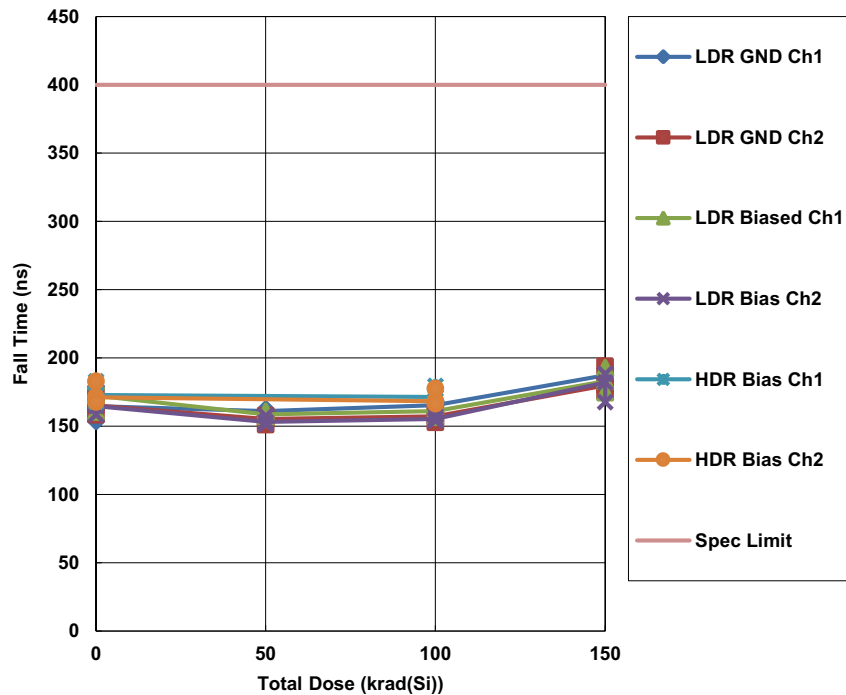


Figure 20. ISL70218SEH fall time, Channels 1 and 2, as a function of total dose irradiation at LDR (biased and unbiased) and at HDR (biased only). The dose rate was 0.01rad(Si)/s for LDR irradiation and 65rad(Si)/s for HDR irradiation. Sample sizes for all three cells were four. The post-irradiation SMD limit is 400ns maximum.

3. Discussion and Conclusion

This document reports results for the Low Dose Rate (LDR) and High Dose Rate (HDR) total dose testing of the ISL70218SEH dual operational amplifier. Parts were tested at LDR under biased and unbiased conditions and at HDR under biased conditions only at 0.01rad(Si)/s and 65rad(Si)/s, respectively. The LDR tests were run to 150krad(Si) and the HDR test was run to 100krad(Si). All parameters showed good stability out to the maximum total dose for each test.

The output HIGH and LOW voltages ([Figures 2](#) and [3](#)) were stable over irradiation and remained within the SMD post-irradiation limits at both dose rates. Note that this parameter provides a measure of the amplifier's ability to swing close to the rail and is not the 'LOW' and 'HIGH' voltage parameter as seen in digital parts.

The input offset voltage, positive and negative input bias current, and input offset current were stable over irradiation and remained within the SMD post-irradiation limits at both dose rates. The input offset voltage ([Figure 4](#)) and input offset current ([Figure 7](#)) were stable over LDR and HDR irradiation. The positive and negative bias current ([Figures 5](#) and [6](#)) showed an increase at both dose rates but remained well within the $\pm 75\text{nA}$ SMD post-irradiation limit. The part is not considered LDR sensitive.

The positive and negative open-loop gain ([Figures 8](#) and [9](#)) showed some variation but remained well within the SMD limits.

The positive and negative power supply rejection ratio ([Figures 10](#) and [11](#)) and the common-mode rejection ratio ([Figures 12](#) and [13](#)) showed some variation but remained well within the SMD limits.

The sourcing and sinking output short circuit current ([Figures 14](#) and [15](#)) and the positive and negative supply currents ([Figure 16](#)) were stable over irradiation and remained within the SMD post-irradiation limits at both dose rates.

The positive and negative slew rates ([Figures 17](#) and [18](#)) were stable over irradiation and remained within the SMD post-irradiation limits at both dose rates, as were the rise time and fall time ([Figures 19](#) and [20](#)).

In conclusion, the ISL70218SEH showed good performance to the SMD low and high dose rate limits of 150krad(Si) at LDR and 100krad(Si) at HDR. The part showed no LDR sensitivity. No differences in total dose response were noted between biased and grounded LDR irradiation for any parameters. Additionally, no channel-to-channel differences were noted, either in the pre-irradiation data or in the total dose response of the parts.

4. Appendices

4.1 Reported parameters.

Table 2. ISL70218SEH Reported Parameters

| Figure | Parameter | Low Limit | High Limit | Units | Notes |
|--------------------|---------------------------------------|-----------|------------|------------|----------------------|
| 2 | Output High Voltage | - | 120 | mV | Channels 1 and 2 |
| 3 | Output Low Voltage | - | 80 | mV | Channels 1 and 2 |
| 4 | Input Offset Voltage | -290 | 290 | μ V | Channels 1 and 2 |
| 5 | Positive Input Bias Current | -1500 | 1500 | nA | Channels 1 and 2 |
| 6 | Negative Input Bias Current | -1500 | 1500 | nA | Channels 1 and 2 |
| 7 | Input Offset Current | -75 | 75 | nA | Channels 1 and 2 |
| 8 | Positive Open Loop Gain | 115 | - | dB | Channels 1 and 2 |
| 9 | Negative Open Loop Gain | 115 | - | dB | Channels 1 and 2 |
| 10 | Positive Power Supply Rejection Ratio | 100 | - | dB | Channels 1 and 2 |
| 11 | Negative Power Supply Rejection Ratio | 100 | - | dB | Channels 1 and 2 |
| 12 | Positive Common-Mode Rejection Ratio | 97 | - | dB | Channels 1 and 2 |
| 13 | Negative Common-Mode Rejection Ratio | 97 | - | dB | Channels 1 and 2 |
| 14 | Output Current, Sourcing | 10 | - | mA | Channels 1 and 2 |
| 15 | Output Current, Sinking | -10 | - | mA | Channels 1 and 2 |
| 16 | Positive Supply Current | - | 2.8 | mA | Sum of both channels |
| | Negative Supply Current | - | -2.8 | mA | Sum of both channels |
| 17 | Positive Slew Rate | 0.4 | - | V/ μ s | Channels 1 and 2 |
| 18 | Negative Slew Rate | 0.4 | - | V/ μ s | Channels 1 and 2 |
| 19 | Positive Rise Time | - | 400 | ns | Channels 1 and 2 |
| 20 | Negative Rise Time | - | 400 | ns | Channels 1 and 2 |

5. Revision History

| Rev. | Date | Description |
|------|-----------|---|
| 1.00 | May.14.19 | Applied new formatting. Added 100krad(Si) HDR and 150krad(Si) LDR information throughout document. Updated disclaimer |
| 0.00 | Jan.10.17 | Initial release |

Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
 - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.
6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.4.0-1 November 2017)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:
www.renesas.com/contact/

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.