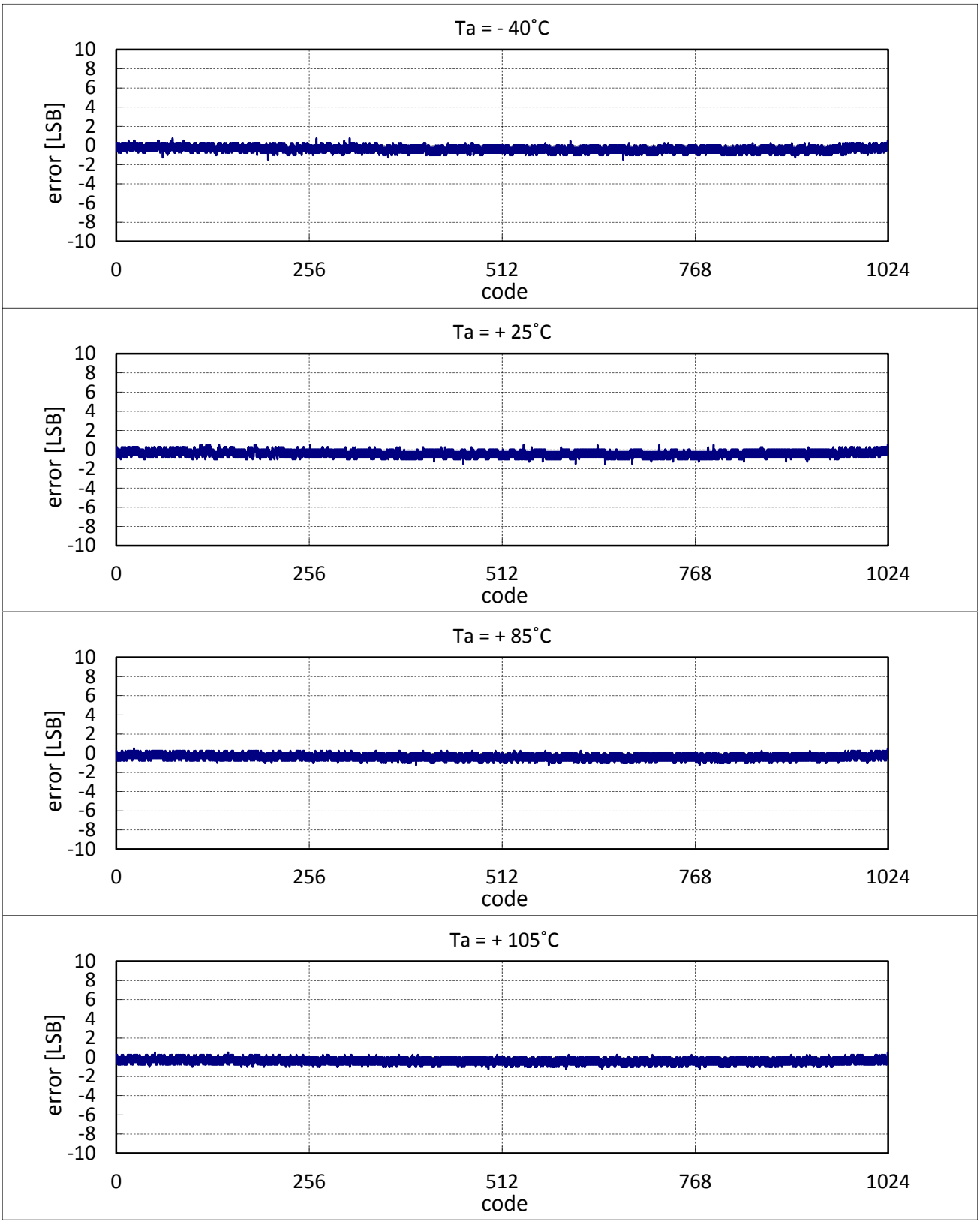
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 5.0 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : HS mode, RUN
fCLK = 32 MHz (High-speed OCO)

VREF(+) = AVREFP = 5.0 V, VREF(-) = AVREFM = GND
fAD = 8 MHz
conversion time = 2.375 µs
mode : Normal1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

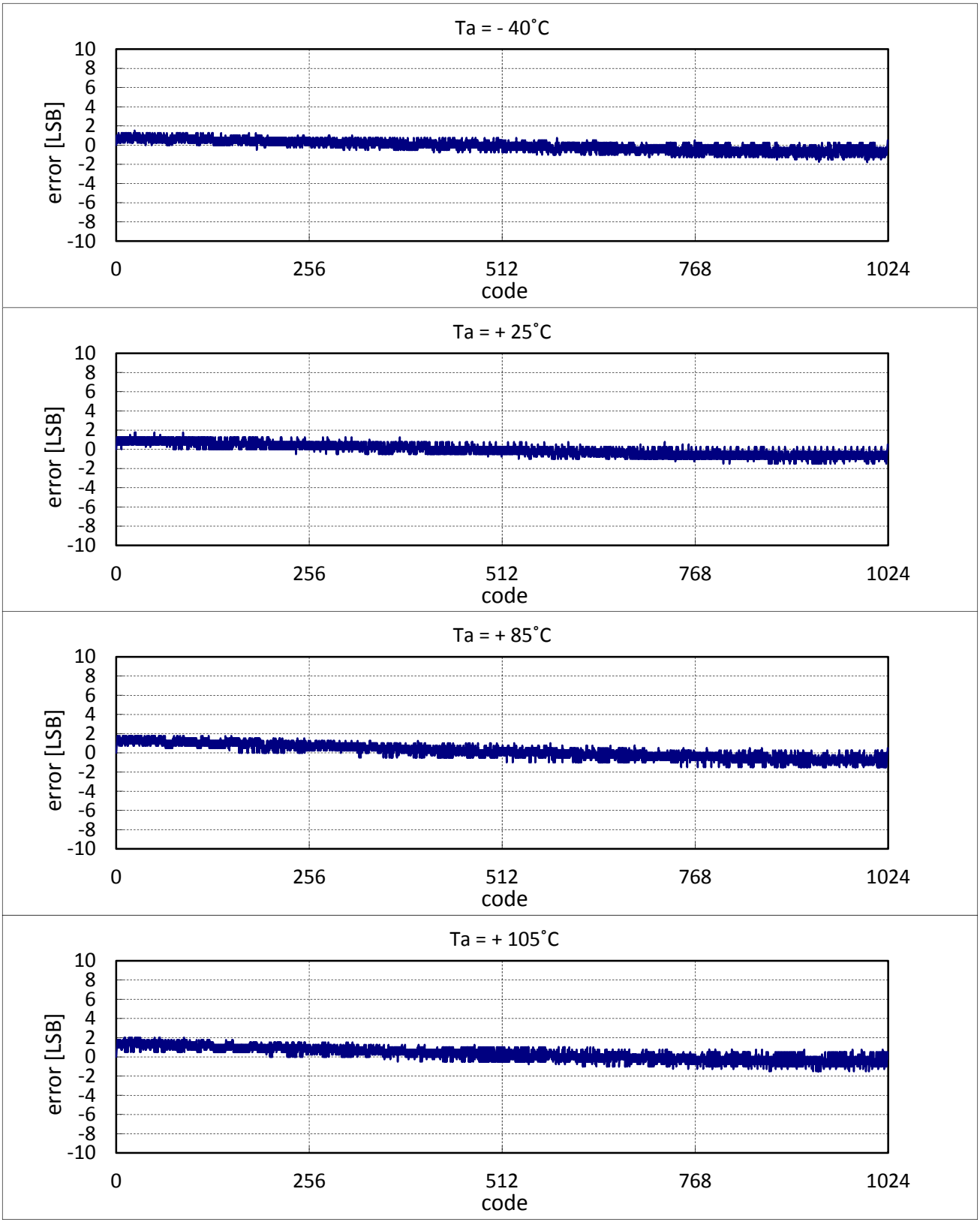
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 5.0 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : HS mode, RUN
fCLK = 32 MHz (High-speed OCO)

VREF(+) = VDD = 5.0 V, VREF(-) = VSS = GND
fAD = 8 MHz
conversion time = 2.375 µs
mode : Normal1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



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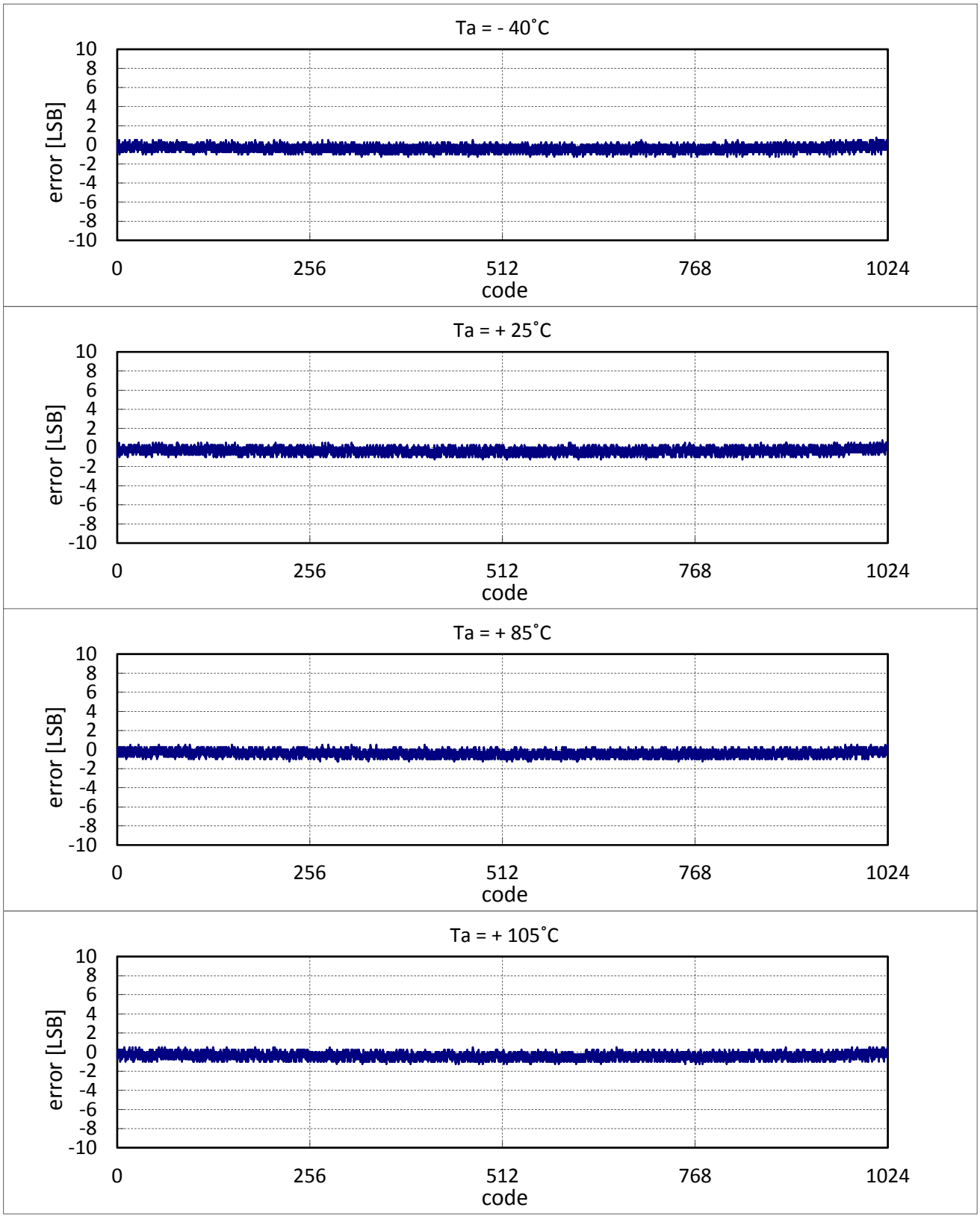
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 3.0 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : HS mode, RUN
fCLK = 32 MHz (High-speed OCO)

VREF(+) = AVREFP = 3.0 V, VREF(-) = AVREFM = GND
fAD = 5.33 MHz
conversion time = 3.5625 µs
mode : Normal1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



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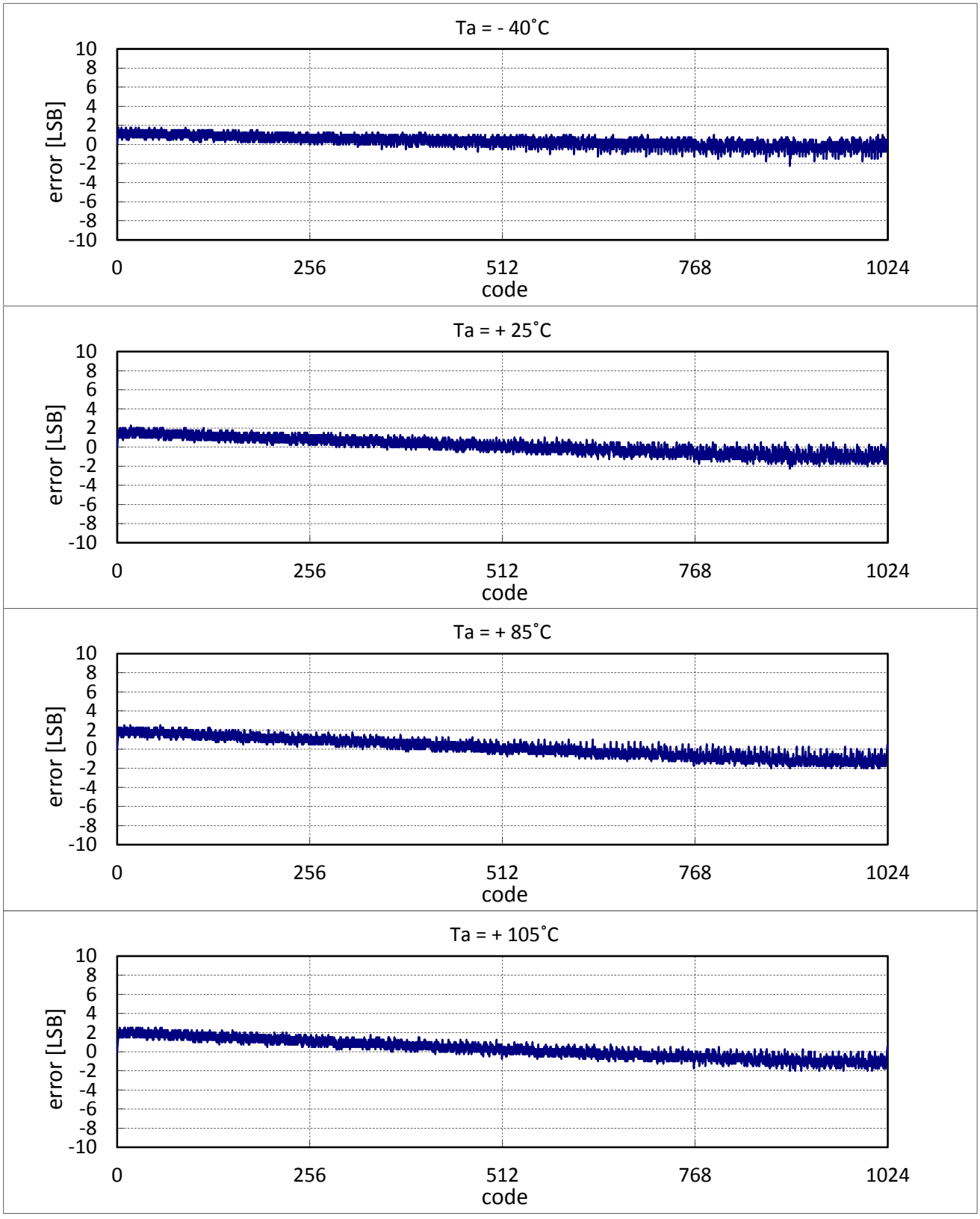
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 3.0 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : HS mode, RUN
fCLK = 32 MHz (High-speed OCO)

VREF(+) = VDD = 3.0 V, VREF(-) = VSS = GND
fAD = 5.33 MHz
conversion time = 3.5625 µs
mode : Normal1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



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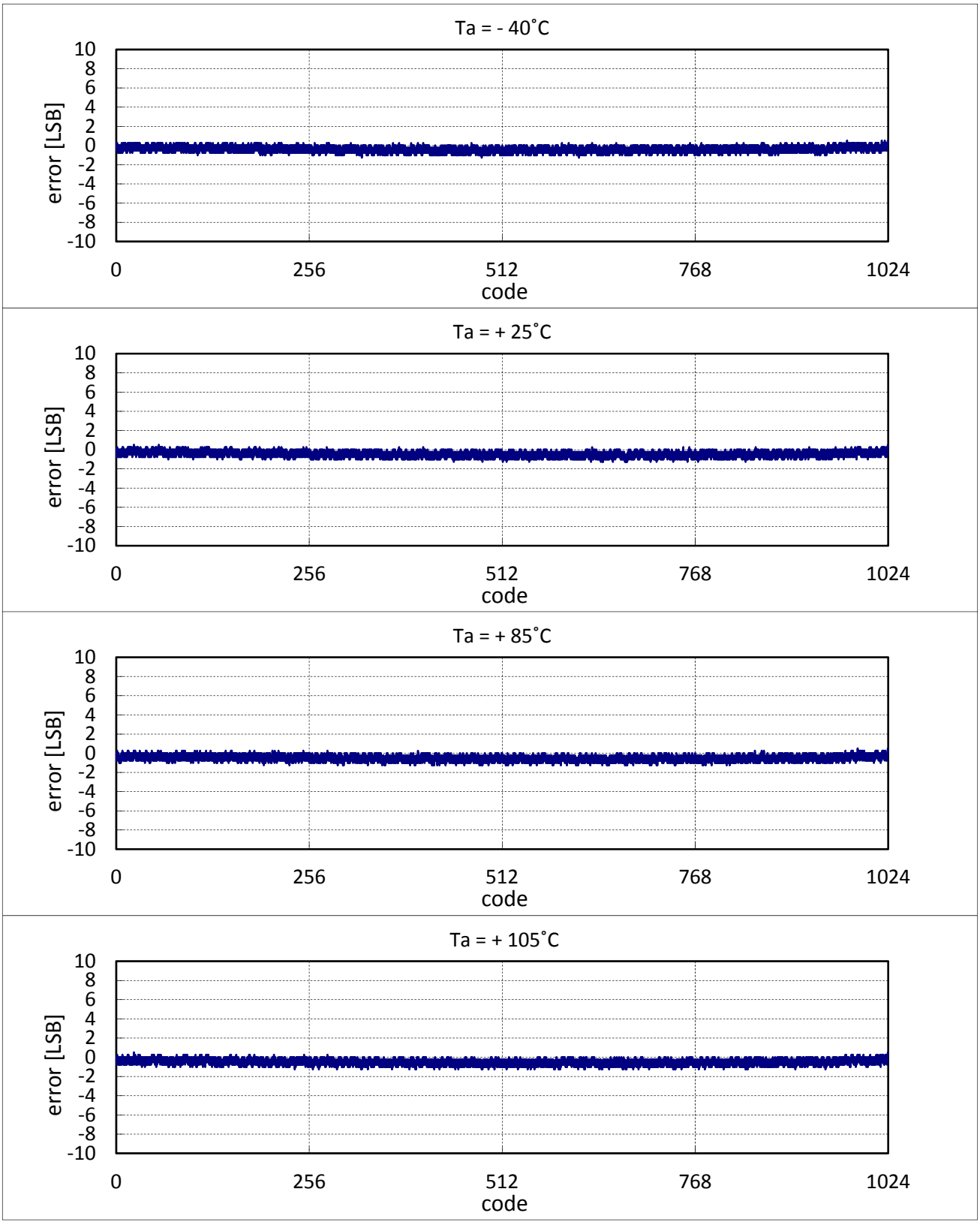
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 3.0 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : LS mode, RUN
fCLK = 8 MHz (High-speed OCO)

VREF(+) = AVREFP = 3.0 V, VREF(-) = AVREFM = GND
fAD = 4 MHz
conversion time = 4.75 µs
mode : Normal1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



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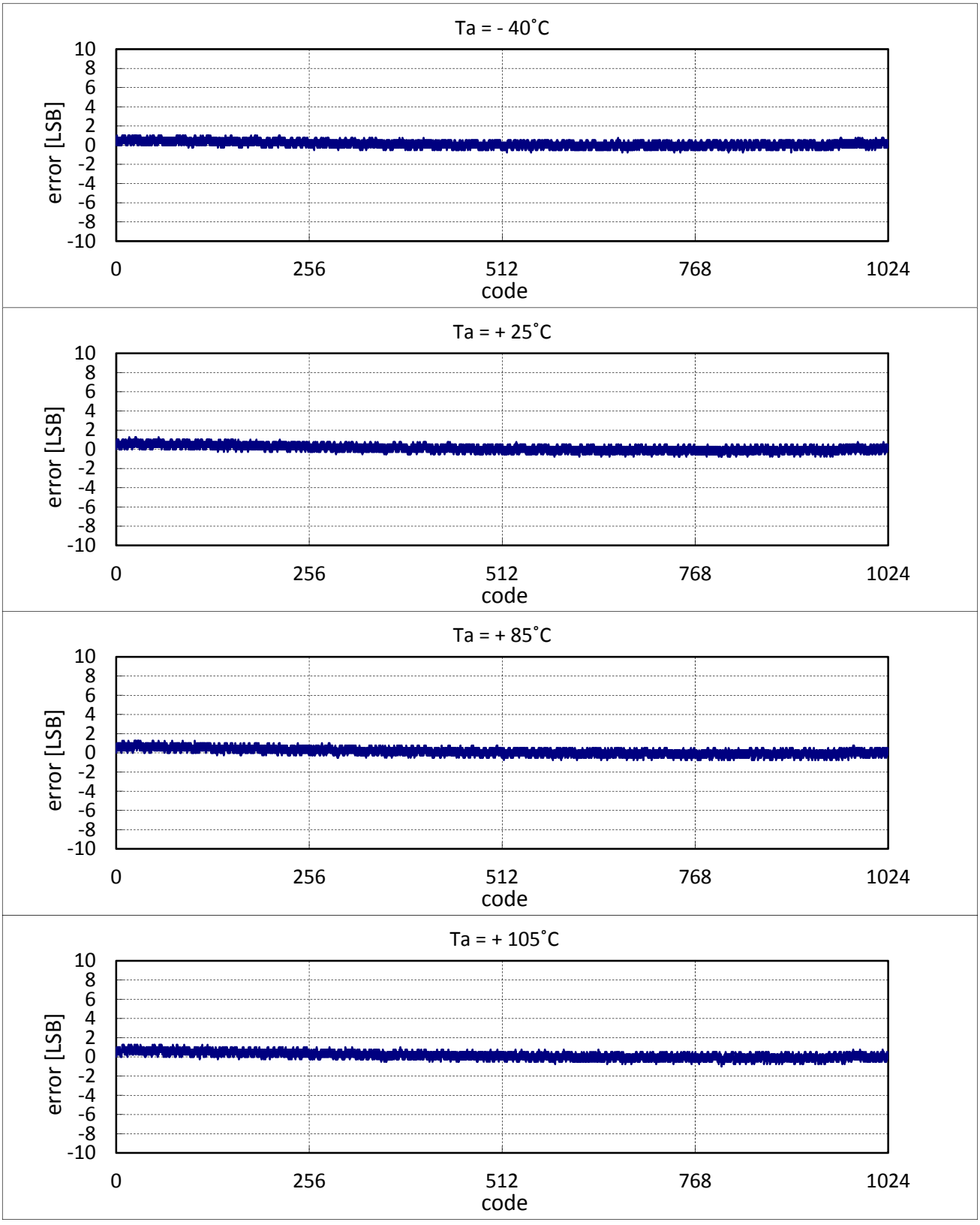
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 3.0 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : LS mode, RUN
fCLK = 8 MHz (High-speed OCO)

VREF(+) = VDD = 3.0 V, VREF(-) = VSS = GND
fAD = 4 MHz
conversion time = 4.75 µs
mode : Normal1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



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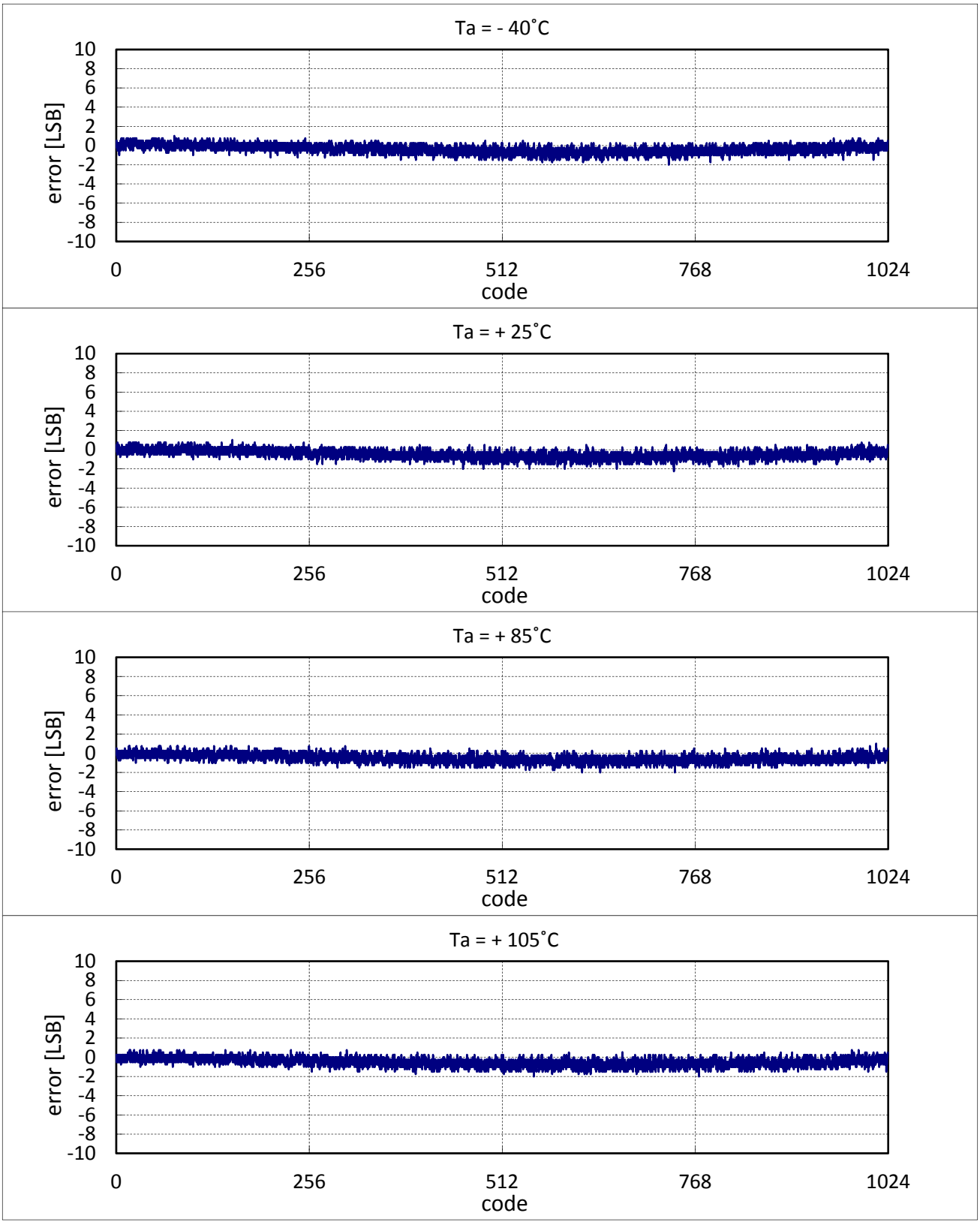
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 1.6 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : LV mode, RUN
fCLK = 4 MHz (High-speed OCO)

VREF(+) = AVREFP = 1.6 V, VREF(-) = AVREFM = GND
fAD = 0.25 MHz
conversion time = 76 µs
mode : Low voltage 1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

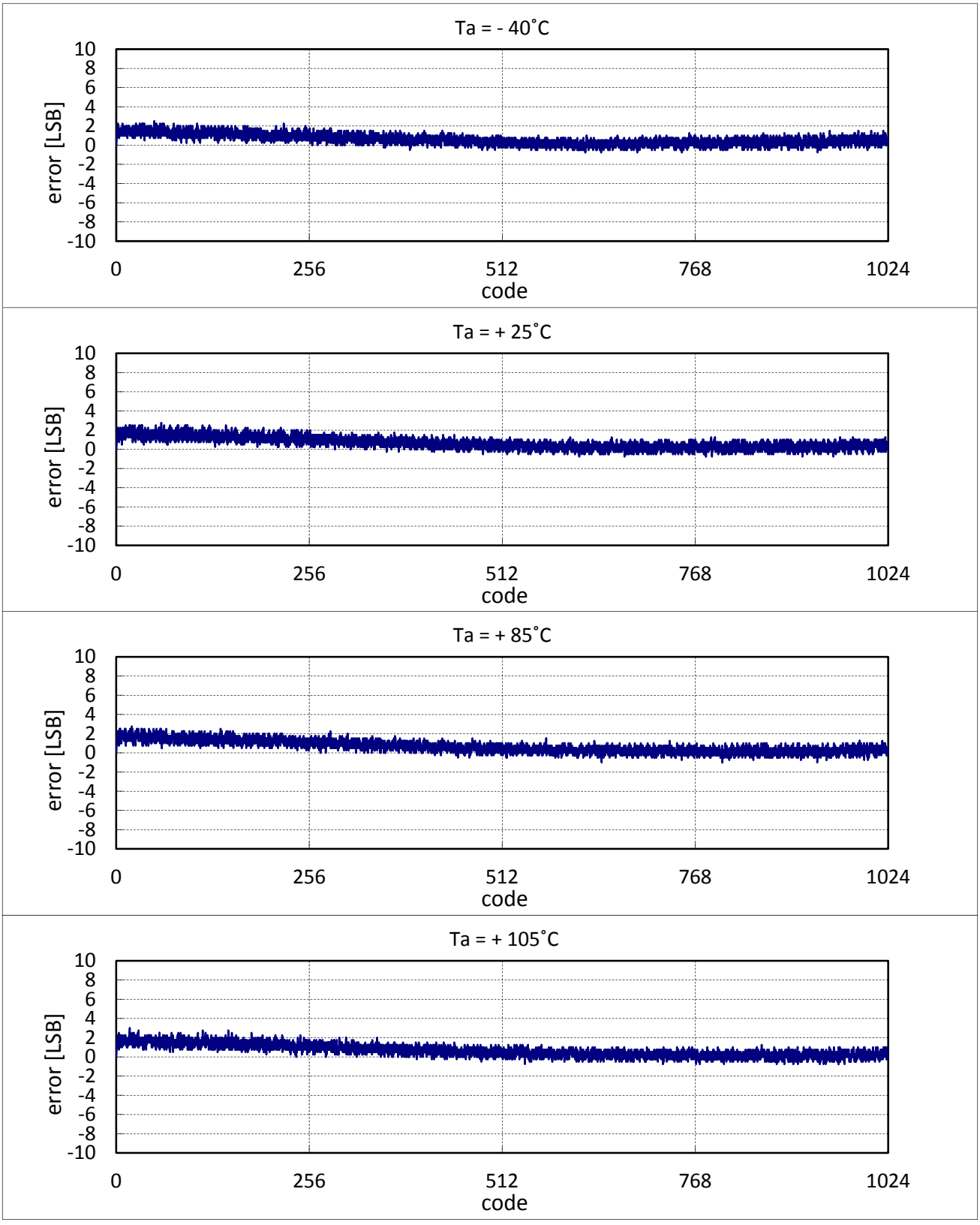
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 1.6 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : LV mode, RUN
fCLK = 4 MHz (High-speed OCO)

VREF(+) = VDD = 1.6 V, VREF(-) = VSS = GND
fAD = 0.25 MHz
conversion time = 76 µs
mode : Low voltage 1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



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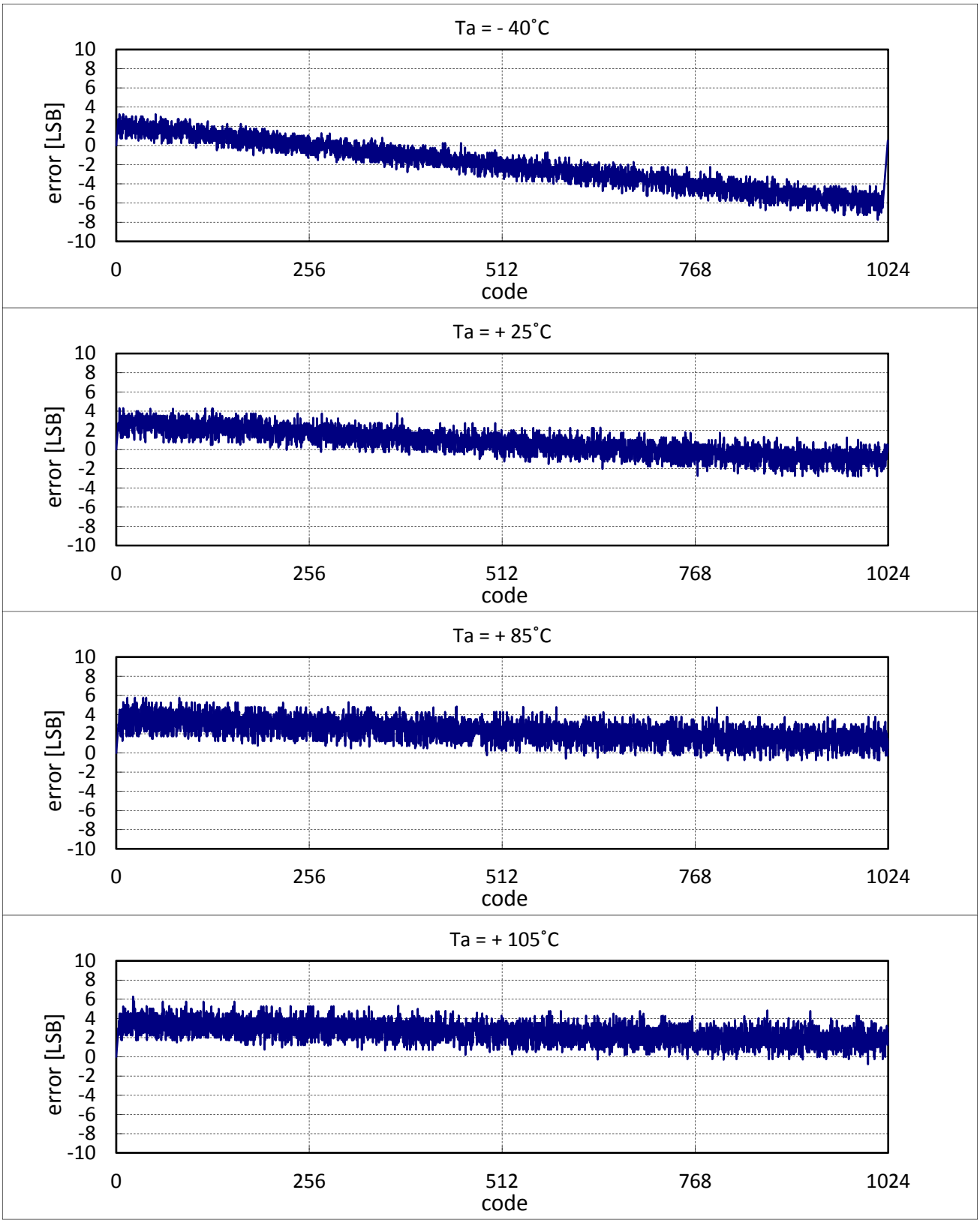
RL78 series
10-bit A/D converter overall error

VDD = EVDD0 = EVDD1 = 3.0 V
Ta = - 40 ˚C, +25 ˚C, + 85 ˚C, + 105 ˚C
CPU : HS mode, RUN
fCLK = 32 MHz (High-speed OCO)

VREF(+) = VBGR = 1.45V(typ.), VREF(-) = VSS = GND
fAD = 1 MHz
conversion time = 19 µs
mode : Normal1

Prepared on May 25th, 2015

*Applicable devices of this report as of May 2015.
RL78/G12, RL78/G13, RL78/G14, RL78/G1C, RL78/G1D, RL78/G1F, RL78/G1G, RL78/L12, RL78/L13, RL78/I1A, RL78/I1B



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