

RL78/I1D The Smart choice for detector & sensor





2015.01



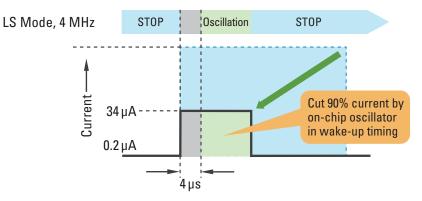
RL78/I1D – The Smart choice for detector & sensor

Detectors and sensors are surely one of the fast growing market as part of the change happening with the development of the Internet of Things. More and more detectors & sensors are used, especially in building automation systems, like access control system & security system or fire protection system. The latest microcontroller of the RL78 platform RL78/I1D is specifically designed for such type of applications.

RL78/I1D Benefits and Key Features

- Single chip solution ideal for all types of detectors & sensors applications
- High integration of analog features needed for analog processing: 12-bit ADC, up to 4 units of Operational Amplifiers, 2 window comparators, internal reference voltage and temperature sensor

 All components available on-chip for sensor processing
- Less external components thanks to internal peripherals: LVD, POR, Data Flash – Reduce BOM list with more integration for cost effective solution
- Lowest power consumption value with highly optimised peripherals and modes – Achieve extended battery life
- Wake up from lowest power mode in 4 µs
 - Fast wake up helps to reduce the power consumption even more, and also to react more quickly to event for accurate and real time control
- 4 channels of new 8-bit interval timer, and still 12-bit interval timer for longer interval timer
 - Best timer for stop mode because of power consumption < 100 nA, and ideal to achieve even longer wake up time in order to increase even more the lifetime of the battery
- Enhanced functionality with intelligent peripherals connection
 - Data Transfer Controller (DTC), Data Operation Circuit (DOC), Event Link Controller (ELC) will help to reduce the workload
 and the program code and also helps to reduce the overall current consumption, as these are available in stop mode
- Easiest development thanks to 'Detect it!' solution kit and code generation with Applilet tool



Middle speed on-chip oscillator current image (RL78/I1D

RL78/I1D Applications

- Detectors for security & access control applications: glass break detector, open/close detector, motion or presence detector, ...
- Detectors for fire protection: smoke detector, CO detector, ...
- Industrial sensors: flow, temperature, pressure, ...
- Electrical components: circuit breakers, remote control, ...





RL78/I1D Block Diagram

- RL78 CPU Core
 - 16-bit CISC CPU, 33 DMIPS @ 24 MHz
- Flash Memory
 - 1.8 V Reprogramming
 - ROM: up to 32 KB
 - SRAM: up to 3 KB
 - Data Flash: 2 KB
 - Error Code Correction (ECC)
- System
 - ± 1% Internal High-Speed Oscillator (24 MHz)
 - Low Power Internal Medium-Speed Oscillator (4 MHz): 34 µA
 - ± 15% Internal Low-Speed Low Power oscillator (15 kHz)
 - 16 x 16 Multiplier, 32/32 Divider, Multiply-Instruction
 - 1.6 to 3.6 V operation, External Clock (20 MHz max.)
- Power Management
 - Operating: <150 µA @ 1 MHz
 - STOP: 0.2 µA (RAM retained)
 - MOCO frequency stabilization time: <4 μs
- Safety Features
 - IEC/UL 60730 Support, Illegal Memory Access,
- Timers
 - Multi-function Timer Array Unit (TAU0)
 - Watchdog (window function)
 - 12-bit Interval Timer: 1 ch
 - 8-bit Interval Timers: 4 ch
 - Real Time Clock
- Analog
 - ADC: Up to 17 channels 12-bit
 - Op-Amp: Up to 4 ch
 - Comparators: 2 ch
 - 1.6 V (Vcc) operation,
 - Internal Voltage Reference (1.45 V)

- RL78 16-bit CPU Memory 24 MHZ 33 DMIPS Program Flash up to 32 KB MUL/DIV/MAC Instructions SRAM up to 3 KB Four Register Banks Data Flash 2 KB 16-bit Barrel Shifter Analog Safety System ADC 12-bit, 17 ch RAM Parity Check DTC ADC Self-diagnostic Internal VREF DOC Interrupt Controller 4 Levels Clock Monitoring Temp. Sensor Op-Amp Up to 4 ch Clock Generation Internal, External Memory Comparator I/O Port Read Back POR, LVD ELC 20 Events Power Management Timers Debug Single-Wire Timer Array Unit 16-bit 4 ch (1 ch: 2 x 8-bit HALT RTC, DTC enabled Interval Timer 8-bit 4 ch, 12-bit 1 ch Active in STOP Mode SNOOZE Serial, ADC enabled Communication STOP SRAM On WDT 2 x 12C 17-bit, 1 ch 1 x UART RTC Calendar Fast Wake-up 2 x CSI
- Temperature range
 - -40 to +105° C operation
- High-end peripherals
 - Data Transfer Controller (DTC)
 - Event Link Controller (ELC)
 - Data Operation Circuit (DOC)
 - POR, LVD
- Communication
 - CSI, UART, I²C
- Packages: 20-pin to 48-pin

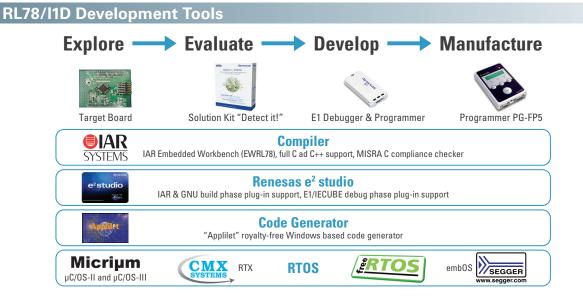
RL78/I1D Line-up

RL78/I1D line-up is optimised in terms of memory/package for detector & sensor applications:

- Small memory for good fit with applications
- Cost effective and compact packages







RL78/I1D Tool Ordering Information

Hardware Tool	Part Number					
RL78/I1D Target Board	RTE5117GC0TGB00000R					
RL78/I1D Solution kit "Detect It!"	YDETECT-IT-RL78					
E1 Debugger & Programmer	YR0E000010KCE00-EE					
PG-FP5 Flash Programmer	Y-PG-FP5-EE					
IAR Systems IDE & Compiler (free up to 16 kB Flash)	Y-IAR-EWRL78-FULL					

RL78/I1D Devices Ordering Information

Pin Count	Package	Memory [KB]	RAM [KB]	Data Flash [KB]	16-bit Timers	12-bit int. Timer	8-bit Int. Timers	Watchfog	RTC	CPU Frequency	ADC	Operational Amplifier	Comparator	CSI	UART	Simplified I ² C	GPIO Ports	Part Number
20	20-pin plastic LSSOP (4,4 x 6,5 mm)	8	0.7	2	4	1	4	1		24	6	2		1		1	14	R5F11768GSP
20		16	2						1 (No									R5F1176AGSP
24	24-pin plastic HWQFN (4 x 4 mm)	8	0.7						output)					2		2	18	R5F1178GNA
24		16	2															R5F1177AGNA
	30-pin plastic LSSOP (7,62 mm (300))	8	0.7						1 with output		12	4	2]	1	24	R5F117A8GSP
30		16	2											1				R5F117AAGSP
		32	3*												1			R5F117ACGSP
	32-pin plastic HVQFN (5 x 5 mm)	16	2											2		2		R5F117BAGNA
32		32	3*	-													26	R5F117BCGNA
	32-pin plastic LQFP (7 x 7 mm)	16	2															R5F117BAGFP
		32	3*															R5F117BCGFP
4.0	48-pin plastic LFQFP (7 x 7 mm)	16	2								17							R5F117GAGFB
48		32 3*								17						42	R5F117GCGFB	

*Only 2 KB when self-programming function and data flash are used.

Before purchasing or using any Renesas Electronics products listed herein, please refer to the latest product manual and/or data sheet in advance.



