

RX63N Group

Audio Player Sample Application: MP3, WAV, ADPCM

R01AN1707EU0100 Rev.1.00 Dec 2, 2013

Introduction

This document describes a sample application that implements an audio player capable of rendering PCM audio from MP3¹, WAV - PCM, and WAV- IMA ADPCM format files stored on an SD-Card.

Target Device

RX63N Group

This sample application is designed to operate on the following hardware:

• YRDKRX63N demonstration board.

Related Documents

- Renesas MP3 Decoder Middleware Terms and Conditions User Agreement
- MP3 Decoder for RX600 Ver.2.00 User's Manual. Describes the Renesas Micro Computer Middleware R0MRX60SM0010RRC MP3 Decoder.
- FAT FS; FAT files system module. Open-source software available from http://elm-chan.org/fsw/ff/00index_e.html

Contents

1.	Overview	. 2
2.	Importing the project	. 3
3.	Adding MP3 playback support	. 5
4.	Supported Audio File Formats	. 7
5.	Using the sample application	. 7
6.	Audio player principle of operation	. 9

Note 1:

MP3 file decode capability is licensed technology that requires a license from the technology owner for its commercial use. Therefore, the default build configuration of this software does not include the MP3 playback, and the MP3 decoder library file is not included in the default project. Please contact your Renesas Sales representative to obtain the full version of this project that supports MP3, and for information on how to acquire the MP3 usage rights.



1. Overview

This document describes a sample application that implements an audio player capable of rendering PCM audio from MP3, WAV - PCM, and WAV- IMA ADPCM format files stored on an SD-Card on the YRDKRX63N development board platform. The sample software provides a rudimentary file browser for viewing the contents of an SD-Card storage device. Audio files in the MP3 format (MPEG-1 layer III, and MPEG-2 layer III) can be selected from the browser for playback. In addition, 16-bit PCM encoded, or 16-bit IMA ADPCM .wav format files can be played. Audio is output as a PWM signal from the RX63N MCU, then filtered and amplified by the external audio amplifier for listening with stereo headphones or external speaker. With appropriate modifications, the audio and file system capabilities demonstrated in this sample application can be ported to other Renesas RX family MCUs. This sample software is intended for reference purposes only to demonstrate the concepts and capabilities of audio reproduction using the Renesas RX family MCUs.

1.1 Hardware Requirements

- YRDKRX63N demonstration board.
- Micro SD memory card.
- External stereo speaker or headphones with 1/8" stereo jack (recommended).

1.2 MCU resources used by this application

- 1. This application requires use of a few of the RX6xx microcontroller's peripheral modules to communicate with the SDCard memory and to run the demo. The following hardware resources are required:
- 2. One "Serial Peripheral Interface (RSPI)" channel. (Required for SDCard communication.)
- 3. One "Compare Match Timer (CMT)" channel. (Used for timed delays in SDCard driver code.)
- 4. The "Data Transfer Controller (DTC)" for streaming PWM timer data to the TPU timers.
- 5. Three "TPU" timer channels.
- 6. "Real-time clock (RTC)". Not essential for audio playback, but it is used for file data and time stamping by the application.
- 7. 12-bit ADC. The "ADC12" is used for measuring the audio playback volume setting by potentiometer VR1.
- 8. One serial "SCI" channel, used as a UART for optional RS232 communications to support an external player control UI.

1.3 Supported Toolchains

This sample software is tested and working with the following toolchains:

• Renesas RX Toolchain v1.02

1.4 Integer Types

This project uses ANSI C99 "Exact width integer types" in order to make the code clearer and more portable. These types are defined in stdint.h.



2. Importing the project

The application is distributed as an archived e^2 studio project. The project archive appears as a ".zip" file but it is not to be manually unzipped. Instead, the project archive is to be "imported" into an existing e^2 studio workspace. During import e^2 studio will take care of extracting the project files into the correct location.

Depending on how the application was obtained, for example download from website, it might be contained in a .zip file that also includes this application note. If this is the case then there will be another .zip inside the first that contains the project itself. The outer .zip file should be unzipped, but do not manually unzip the inner project archive.

2.1 Steps to import and build the audio player application

- 1. With e^2 studio open to the workbench view, select File \rightarrow Import and the Import tool will open.
- 2. Under the "General" category, select "Existing Projects into Workspace", then click Next >

e ² Import				
Select		e2 Import		
Create new projects from an archive file or directory.		Import Projects		
Select an import source:		Select a directory to sear	ch for existing Eclipse projects.	
📔 🖌 General		Select root directory:		B <u>r</u> owse
👰 Archive File		Select <u>a</u> rchive file:	C:\Downloads\RDKRX63N_Audio_Player.zip	B <u>r</u> owse
😢 Existing Projects into Workspace		Projects:		
HEW Project , Preferences		RX_Audio_Player	(RX_Audio_Player)	Select All
箳 Renesas Common Project File				Deselect All
▷ ▷ C/C++ ▷ ▷ CVS				R <u>e</u> fresh
 				
▷				
		✓ <u>C</u> opy projects into w	orkspace	
		Add project to work	ting sets	
		Working sets:	·	S <u>e</u> lect
(<u>Back</u>) <u> (Einish</u> (Einish) (Einish)	Cancel	· · · · · · · · · · · · · · · · · · ·		
		?	< Back Next > Einish	Cancel
				Cancel

- 3. Click the "Select archive file" option, and then click the Browse button. Using the file browser window that opens, navigate to the location that contains the RX_Audio_Player archive, and select the file. Click "Open", and then the Audio player project will appear in the Projects pane of the import tool.
- 4. Make sure that the RX_Audio_Player project checkbox is checked, and then click "Finish".
- 5. A new copy of the RX_Audio_Player project will now be created within your workspace with all source files and settings.
- 6. If you wish to build this software to include support for MP3 playback, and you have obtained the MP3decoder library from Renesas, then see the instructions in chapter 4 for adding the required MP3 files to the project.
- 7. You may now select the project from within the Project Explorer view and build the project as normal.
- 8. Connect to the board using the J-Link USB connector. Under the e^2 studio debug configurations menu, set the appropriate J-Link debug settings and download the code to the MCU.



The list in Table 1 shows the files that are contained in the RX_Audio_File_Player project workspace.

Table 1

Purpose	C source files (or lib)	C header files	Location
Sample Application	main.c	file_time_date.h	RX_Audio_Player
	browse_files.c		src
	led_meter.c		application
Audio Playback	audio_player.c	audio_player.h	RX_Audio_Player
	audio_player_file_ops.c	audio_player_file_ops.h	src
	audio_hardware.c	audio_hardware.h	audio
	adpcm_decoder.c	adpcm_decoder.h	
		RIFF.h	
MP3 Decoder ¹	libMP3Frxlittle.lib	mp3f.h	RX_Audio_Player
			MP3
			inc
			lib
FAT FS file system	ff.c	ff.h	RX_Audio_Player
		ffconf.h	src
		diskio.h	FAT
		integer.h	
File I/O streams	lowsrc.c	lowsrc.h (custom)	RX_Audio_Player
	disk_iostreams.c	disk_iostreams.h	src
	mmc_disk.c	mmc_disk.h	iostreams
	r_diskio.c	r_diskio.h	
		diskio_drivers.h	
SD Card driver	mmc_protocol.c	mmc_protocol.h	RX_Audio_Player
	cmt_oneshot.c	sdcard.h	src
		cmt_oneshot.h	mmc
			cmt
Board support	dbsct.c	platform.h	RX_Audio_Player
	hwsetup.c	hwsetup.h	r_bsp
	lcd.c	lcd.h	board
	resetprg.c	mcu_info.h	rdkrx63n
	sbrk.c	r_bsp.h	mcu
	vecttbl.c	sbrk.h	rx63n
		stacksct.h	
		yrdkrx63n.h	
		iodefine.h	
		r_bsp_config.h	
LCD display	r_glyph_register.c	r_glyph_config.h	RX_Audio_Player
	r_glyph.c	r_glyph_if.h	r_glyph
	glyph.c	glyph.h	src
	st7579_lcd.c	preamble.h	glyph
	font_8x8.c	config.h	drivers
		st7579_lcd.h	fonts
RSPI Driver	r_rspi_rx.c	r_rspi_rx_if.h	RX_Audio_Player
		r_rspi_rx_config.h	r_rspi_rx
		5	src
Real-time Clock	rto o	rtab	
Real-time Clock	rtc.c	rtc.h	RX_Audio_Player
			src
On board Switchas	r owitchoo o	r owitchoo coofig b	rtc
On-board Switches	r_switches.c	r_switches_config.h	RX_Audio_Player
		r_switches.h	r_switches
			src
Audio volume control	S12ADC.c	S12ADC.h	RX_Audio_Player
Audio volume control	012/100.0		
Audio volume control	012/100.0		src

Note 1: Contact a Renesas Sales representative to obtain the MP3 decoder supports files.



3. Adding MP3 playback support

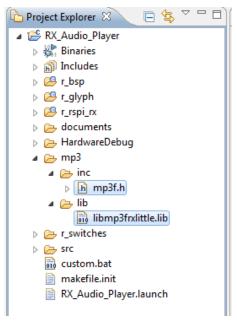
If you have obtained the MP3 decoder library from Renesas, you may build this software to support playback of MP3 encoded audio files. The MP3 library is distributed in a self extracting archive. After extracting the MP3 archive, locate the release note document that lists the contents of the archive. The locations for the required files are listed in the release note, and should be found as shown in Table 2. The other files in the MP3 archive will not be used.

Table 2

Purpose	File name	Location
MP3 Decoder	libMP3Frxlittle.lib	rx600_mp3decoder
Library		mp3
		lib
MP3 Header	mp3f.h	rx600_mp3decoder
		mp3
		inc

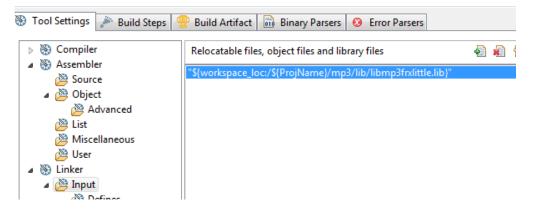
3.1 Copy the MP3 files into the project

In **e² studio**, Copy the "libMP3Frxlittle.lib" and the "mp3f.h" files into their corresponding folders in the RX_Audio_Player project as shown:



3.2 Configure the project to include the MP3 .lib

In **e² studio**, navigate to the **RX_Audio_Player->Properties->C/C++Build->Settings->Linker->Input** view and click the + icon (Add...) to add the file path to the "libMP3Frxlittle.lib" file.





3.3 Add Linker Sections

Add the following sections; BMP3F, CMP3F, PMP3F to the Linker section table as shown:

Source	Sections viewer:		
Source file			
🖄 Object	Address	Section Name	
🖉 List	0x0000000	SI	
a 🖉 Optimize		SU	
🖄 Advanced		B_1	
🖄 Miscellaneous		R_1	
🖄 User		B_2	
a 🚵 CPU		R_2	
🖄 Advanced		В	Add Section
🛛 🛞 Assembler		R	
🖄 Source		BMP3F	Remove Sectio
a 🖄 Object	0xFFF80000	C_1	
🖄 Advanced		C_2	Move Up
🖉 List		C	P
🖉 Miscellaneous		C\$*	Move Down
🖄 User		CMP3F	WOVE DOWN
🛛 🛞 Linker		L*	
a 🖄 Input		D*	
Defines		Р	
Advanced		PMP3F	
🖉 List	L	W*	
Optimize Advanced	0xFFFFFF80	FIXEDVECT	

Click "Apply", then "OK"

3.4 Set the MP3 configuration option

The last step before building the project is to enable the MP3 source. Open the "RX_Audio_Player\src\audio_ audio_player_config.h" file and edit the "MP3_ENABLED" option as below:

#define MP3_ENABLED (1)

This will cause the MP3 portions of the C source code to be included in the build.



4. Supported Audio File Formats

Audio files in the MP3 format (MPEG-1 layer III, and MPEG-2 layer III), 16-bit PCM encoded, or 16-bit IMA ADPCM .wav format files can be selected from the browser for playback.

For ADPCM, only monaural format file playback is supported. ADPCM files need to be in the IMA ADPCM standard format, which includes a RIFF type file header and correct data frame headers. Metadata file tags are not presently supported. IMA ADPCM is considered to be a sub-type of the .wav file format, and so must use the .wav file extension.

Audio file type	File extension	Decoded Sample size	44.1 kHz	33.075 kHz	32.0 kHz		11.025 kHz.	Stereo	Mono
Uncompressed PCM	.wav	16-bits	0	0	0	0	0	0	0
IMA ADPCM	.wav	16-bits	0	0	0	0	0	х	0
MPEG-1 layer III	.mp3	16-bits	0	х	0	х	х	0	0
MPEG-2 layer III	.mp3	16-bits	Х	х	Х	0	х	0	0
MPEG2.5	.mp3	16-bits	х	х	Х	Х	0	0	0

Table 3 Supported Audio File Formats

5. Using the sample application

To run the program in the e^2 studio development environment, first build the "RX_Audio_Player" project. Then download the code to the YRDKRX63N using the GDB debugger.

Alternatively, with the sample application software already loaded in the MCU flash, the audio player demo may be run in a stand-alone manner. In this case the board will need to be powered by either a USB cable plugged into the J-Link debugger port, or by providing regulated 5-volt power to one of the board power connectors. If using the J-Link USB port just for power, it is recommended to disable the debugger function by placing a shorting plug on the "J-Link disable" two-pin header located near the USB connector.

When the board is powered on, it will reset, and the audio player application will start. Control of the application is done with the pushbuttons on the YRDKRX63N, and information is displayed on the on-board LCD.

Before starting the application, a micro SD-Card containing the audio files should be inserted in its slot on the YRDKRX63N. If the card is not present when the application starts, the application will wait until the SD-Card is inserted, with a red LED illuminated to indicate the waiting status.

After the SD-Card is successfully recognized, the application immediately proceeds to the file browse function with user control information displayed on the LCD.

While browsing the files it may be noticed that some file names are displayed as shortened versions. The file-system in this application is limited to showing file names in the 8.3 format, due to patent restrictions on long filenames. Only the file name displayed has been changed; the actual file name stored on the media remains unchanged with its original long filename.

Audio files may be copied to the micro SD-Card by any PC with a SD-Card slot. They can be located in any directory with the limitation that the fully qualified path string to the file must be 64 characters or less. The fully-qualified path string includes the drive letter or number, colon, backslash, all subdirectories leading to the file, and the filename itself. For example: "E:\audio\music\somefile.mp3" (without quotes) is a valid path, containing 27 characters. (In the audio player demo application, the drive letter "E:" will be represented as a number; "1:").

Note: Take care inserting or removing the micro SD Card from the slot on the YRDKRX63N. It should go in or out in a straight direction aligned with the slot. The micro SD Card is fragile and can be damaged or cracked by excessive off-center pressure.

Once an audio file of the .mp3 or .wav type has been found and playback has been started, the volume control becomes active. LEDs on the board will illuminate to provide a visual indicator of the current volume setting. Volume control is provided by the potentiometer "VR1" on the YRDK board. Turn VR1 clockwise to increase the volume or counter-clockwise to decrease it.



5.1 Using the file browse function to find and play audio files.

On startup, the sample software proceeds to the file browsing function. Files and directories can be displayed on the LCD one at a time by pressing switches on the YRDKRX6xN board. On entry, the root directory is read and the first file or directory name will be displayed. The switch usage instructions are displayed on the LCD as follows:

Example display:

Browse files	
SW1 -> Back	
SW2 -> Next	Press switch 2 to view the next file or directory entry.
SW3 -> Open	Press switch 3 to open the currently displayed file or directory.
1:\	The root (top) directory name is always displayed as 1:\ (drive 1:).
\AUDIO	Below that, the first file or directory name in this folder is displayed.

Directory names start with a backslash character '\'. If the currently displayed entry is a directory, then pressing switch 2 opens that directory for reading, and the first entry in that directory will displayed. Also, the current directory name will remain visible on the line above.

Browse	files	
SW1 ->	Back	Press switch 1 to go back up one directory level.
SW2 ->	Next	Press switch 2 to view the next file or directory entry.
SW3 ->	Open	Press switch 3 to open the currently displayed file or directory.
\AUDIO		Current directory name remains displayed.
SOMEFII	LE.WAV	Current file entry in the directory.

If the currently displayed entry is a file, then pressing switch 3 reads the file statistics and displays them on the LCD. The contents of the file are not examined at this point. When finished viewing the information, press SW3 to proceed. If the file is a supported audio file type (name ends with .wav or .mp3), then the audio player will open the file and begin playing it. If it is any other file type then the browser menu will return.

Basic file statistics known to the file system are displayed.
Press switch 3 to proceed.
Current file entry is displayed. Long file names are displayed truncated to 8.3 format.

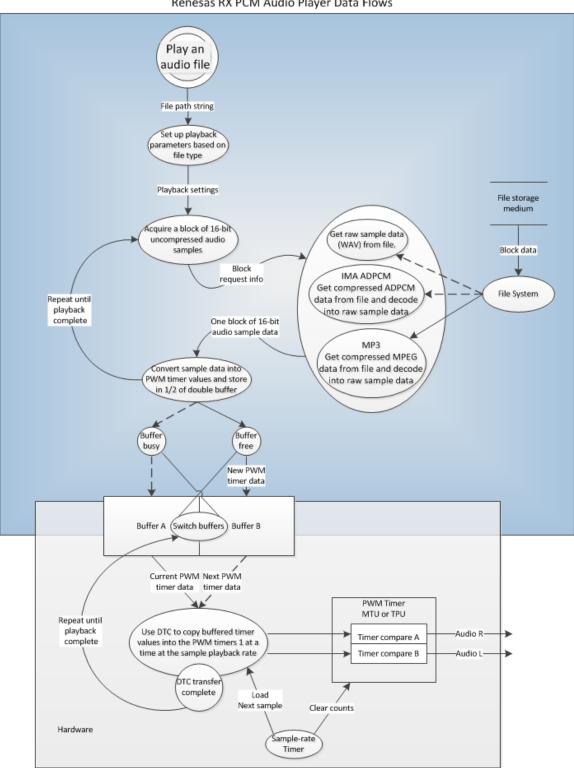
After pressing switch 3 in the above example, the audio controls are displayed and the player will start playing the file. Once playback begins, the choices to quit or advance to the next audio selection in the current directory are available. If "Next" is selected, the currently playing audio stops immediately, and the next audio file found will begin playing. Otherwise, playback will continue until the selection has completed, and then the next audio file found will begin playing. Playback continues in an endless loop, cycling through all supported audio files in the current directory. At any time the "Quit" button may be pressed which will stop playback and return to the file browser.

RENESAS YRDKRX63N PCM Audio	
NOW PLAYING BEETHO~1.WAV SW1 -> Quit SW3 -> Next	The currently playing file is displayed. Long file names are displayed truncated to 8.3 format. Press switch 1 to quit playback and return to the file browser. Press switch 3 to skip to the next audio file.



Audio player principle of operation 6.

This project renders audio from the RX6xx MCU as a PWM signal. PCM audio data is acquired from either .wav format (raw PCM or IMA ADPCM) or .mp3 format files stored on a micro-SD card memory in a FAT file-system. The basic scheme of operation is illustrated in Figure 1



Renesas RX PCM Audio Player Data Flows



Website and Support

Renesas Electronics Website <u>http://www.renesas.com/</u>

Inquiries

http://www.renesas.com/contact/

All trademarks and registered trademarks are the property of their respective owners.



Revision Record

		Description		
Rev.	Date	Page	Summary	
Rev.1.00	Dec 2, 2013	-	First edition issued	

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.
- 2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
 In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.
 In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at
 - which resetting has been specified.
- 3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access
 these addresses; the correct operation of LSI is not guaranteed if they are accessed.
- 4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal.
 Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.
- 5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

— The characteristics of an MPU or MCU in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

Notice

- 1. Descriptions of circuits. software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
- 2. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herei
- 3. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or
- technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others
- 4. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics product.
- 5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
- "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; and safety equipment etc

Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (nuclear reactor control systems, military equipment etc.). You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application for which it is not intended. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for which the product is not intended by Renesas Electronics.

- 6. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the se of Renesas Electronics products beyond such specified ranges
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult please evaluate the safety of the final products or systems manufactured by you.
- 8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assume no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations
- 9. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You should not use Renesas Electronics products or technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. When exporting the Renesas Electronics products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations.
- 10. It is the responsibility of the buyer or distributor of Renesas Electronics products, who distributes, disposes of, or otherwise places the product with a third party, to notify such third party in advance of the contents and conditions set forth in this document, Renesas Electronics assumes no responsibility for any losses incurred by you or third parties as a result of unauthorized use of Renesas Electronics products.
- 11. This document may not be reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics
- 12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.
- (Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries
- (Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics

RENESAS

SALES OFFICES

Renesas Electronics Corporation

http://www.renesas.com

California Eastern Laboratories, Inc. 4590 Patrick Henry Drive, Santa Clara, California 95054, U.S.A Tel: +1-408-919-2500, Fax: +1-408-988-0279

Refer to "http://www.renesas.com/" for the latest and detailed information

Renesas Electronics Europe Limited Dukes Meadow, Milibard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 D üsseldorf, Germa Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd. 7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd. Unit 301, Tower A, Central Towers, 555 LanGao Rd., Putuo District, Shanghai, China Tei: +86-21-2226-0888, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tei + 852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.

80 Bendemeer Road, Unit #06-02 Hylfux Innovation Centre Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd. Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tei: +60-37955-9309, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea Tel: +82-2-558-3737. Fax: +82-2-558-5141