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M16C/65 群

串行 I/O 操作(时钟同步串行 I/O 模式下的接收)

1. 要点

在时钟同步串行 I/O 模式下接收数据,可以选择如表 1 中所列的各种功能。在表 1 中用符号"O"表示本篇资料所选的项目,图 1 是串行 I/O 的工作时序图。本篇资料的参考例程是使用 UARTO 在时钟同步模式下接收数据的例子。

2. 说明

本篇资料,适用于 M16C/65 群单片机。

本篇应用说明也适用于 M16C 族中与上面所述的群具有相同 SFR(特殊功能寄存器)定义的产品。关于产品功能的改进,请参看手册中的相关信息。在使用本篇应用说明的程序前,需进行详细的评价。



3. 选定功能

表 1. 选定功能

设定项目		设定内容	设定项目	设定内容	
传送时钟源		内部时钟	连续接收模式	0	禁止
		(f1SIO/f2SIO/f8SIO/f32SIO)			
	0	外部时钟(CLKi 引脚)			允许
RTS 功能	0	RTS 功能允许 输出传送时钟		0	不选择
		RTS功能禁止	多个引脚(注 1)		选择
CLK 极性	0	在传送时钟的上升沿输入接收 数据	数据逻辑选择功 能	0	不反转
		在传送时钟的下降沿输入接收 数据			反转
传送时钟	0	LSB 先	CTS / RTS 引脚独	0	复用引脚
		MSB 先	立 (注2)		独立

注 1: 只能在 UART1 使用内部时钟时选择。

注 2: CTS0 /RTS0 独立功能是 CTS0 与 RTS0 引脚功能不复用,从 P6_0 引脚输出 RTS0 ,从 P6_4 引脚输出 CTS0 的功能。当选择这个功能时,不能使用 UART1 的 CTS /RTS 功能,请将 CTS /RTS 禁止位设置为"1"。

4. 串行 I/O 的操作

- (1) 将虚设数据设定到 UARTi 发送缓冲寄存器,并且将接收允许位和发送允许位均置为"1",进入数据接收准备状态。此时,如果 RTSi 引脚的输出电平为"L",将已成为可接收的状态通知发送方(发送方 IC 在检查到 RTS 输出变为"L"电平后,输出传送时钟)。
- (2) 与传送时钟的第一个上升沿同步,将 RxDi 引脚的输入信号保存到 UARTi 接收寄存器的最高位。然后,与发送时钟的上升沿同步通过右移 UARTi 接收数据的内容来读取数据。
- (3) 当 UARTi 接收寄存器中有 1 个字节的数据时,UARTi 接收寄存器中的内容被传送到 UARTi 接收缓冲寄存器,发送时钟停止为"H"电平。此时,接收完成标志位和 UARTi 接收中断请求位为"1"。
 - (4) 当 UARTi 接收缓冲寄存器的低字节被读取时,接收完成标志位将被置为"0"。



使用 UARTi 在时钟同步 I/O 模式下接收数据的工作时序图如下所示:

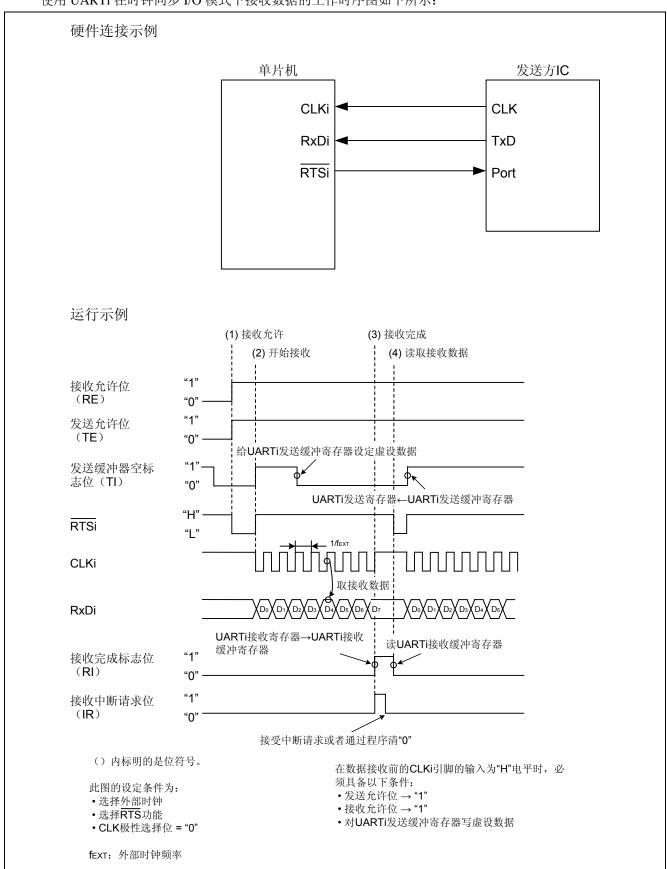
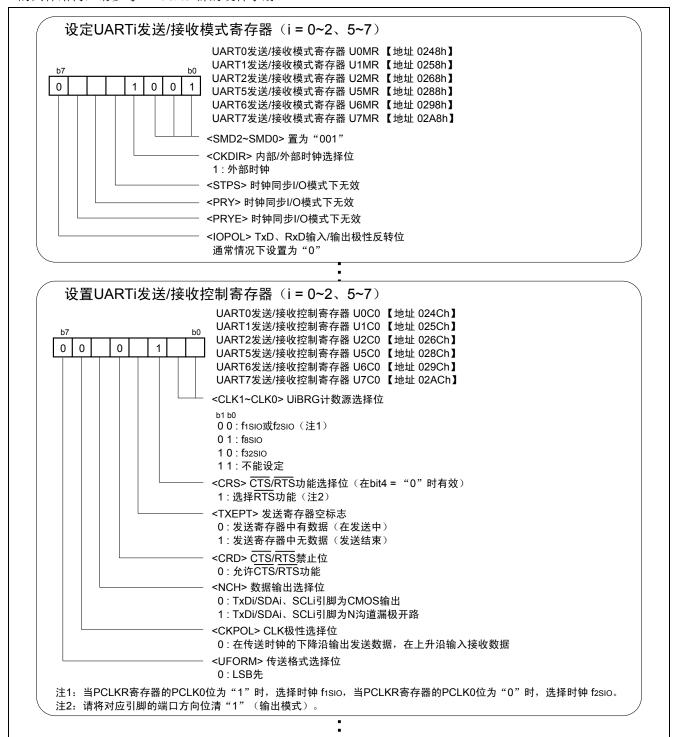


图 1. 使用 UARTi 在时钟同步 I/O 模式下接收数据的工作时序图

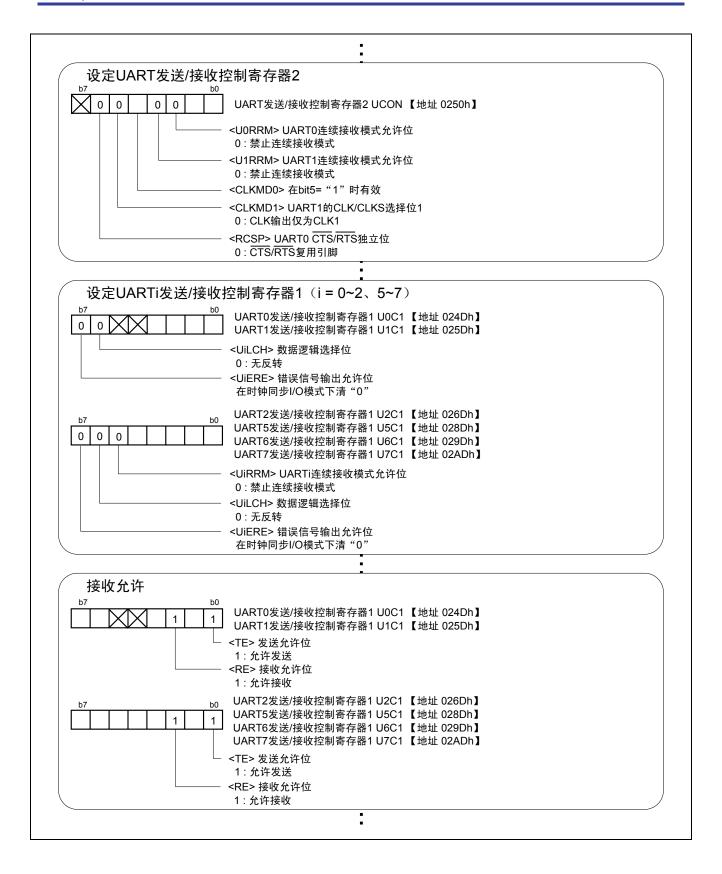


5. 寄存器设置

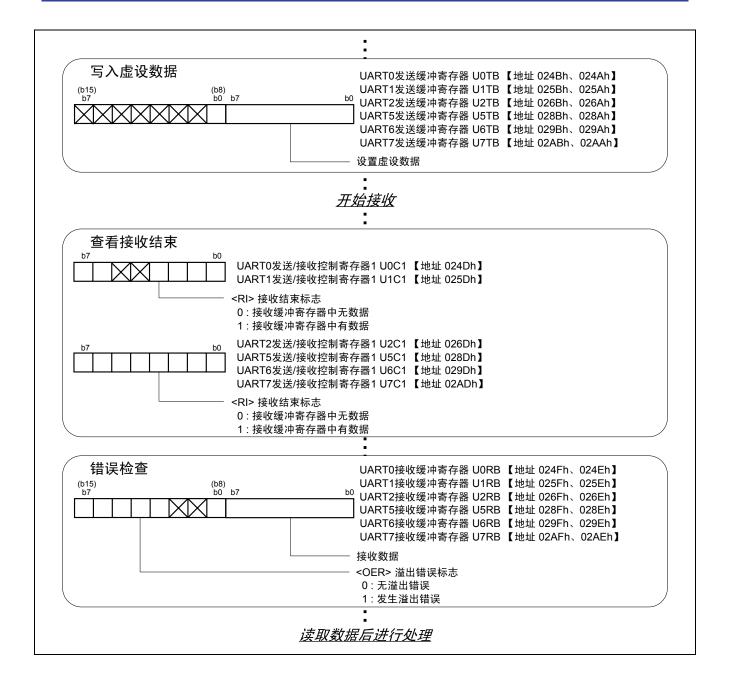
为了能实现定义在"4. 串行 I/O 的操作"的功能,下列寄存器必须按步骤顺序进行设置。对于每个寄存器的具体结构,请参考 M16C/65 群的硬件手册。













6. 参考文献

数据手册

M16C/65 群硬件手册 (最新版本请从瑞萨科技网页上取得)

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