

R32C/100 系列

串行接口操作（时钟异步串行接口模式下的接收）

1. 要点

本篇资料，介绍了使用时钟异步串行接口模式（UART 模式）以任意的通信速率、数据格式接收数据的设定方法和使用例子。

2. 说明

本篇资料，适用于 R32C/111 群单片机。

本篇应用说明也适用于 R32C/100 系列中与上面所述 R32C/111 群具有相同 SFR（特殊功能寄存器）定义的产品。关于产品功能的改进，请参看手册中的相关信息。在使用本篇应用说明的程序前，需进行详细的评价。

3. 概要

本篇资料，介绍了使用时钟异步串行接口模式在通信速率为 9600bps（XIN = 16MHz、PLL 时钟 = 100MHz、实际的通信速率 = 9586bps）的条件下接收数据的情况。

本篇资料中，时钟异步串行接口模式接收数据的设定条件如表 3.1 所示。

表 3.1 时钟异步串行接口模式接收数据的设定条件

项目	设定条件
通信速率	9600bps
传送数据长度	8 位
校验	奇校验
停止位	1 个停止位
传送时钟	内部时钟
发送控制	RTS 功能
传送格式	LSB 先发送

通信速率的计算公式如下所示。

$$\text{实际的通信速率} = \frac{\text{UiBRG寄存器 (i=0~6) 的计数源}}{16 \times (\text{UiBRG寄存器设定值} + 1)}$$

通信速率的设定示例如表 3.2 所示。

表 3.2 通信速率的设定示例

目标通信速率 (bps)	UiBRG 的计数源	PLL 时钟：96MHz		PLL 时钟：100MHz		PLL 时钟：120MHz		PLL 时钟：128MHz	
		外围功能时钟：24MHz		外围功能时钟：25MHz		外围功能时钟：30MHz		外围功能时钟：32MHz	
		UiBRG 的设定值	实际通信速率 (bps)	UiBRG 的设定值	实际通信速率 (bps)	UiBRG 的设定值	实际通信速率 (bps)	UiBRG 的设定值	实际通信速率 (bps)
1200	f8	155(9Bh)	1202	162(A2h)	1198	194(C2h)	1202	207(CFh)	1202
2400	f8	77(4Dh)	2404	80(50h)	2411	97(61h)	2392	103(67h)	2404
4800	f8	38(26h)	4808	40(28h)	4764	48(30h)	4783	51(33h)	4808
9600	f1	155(9Bh)	9615	162(A2h)	9586	194(C2h)	9615	207(CFh)	9615
14400	f1	103(67h)	14423	108(6Ch)	14335	129(81h)	14423	138(8Ah)	14388
19200	f1	77(4Dh)	19231	80(50h)	19290	97(61h)	19133	103(67h)	19231
28800	f1	51(33h)	28846	53(35h)	28935	64(40h)	28846	68(44h)	28986
31250	f1	47(2Fh)	31250	49(31h)	31250	59(3Bh)	31250	63(3Fh)	31250
38400	f1	38(26h)	38462	40(28h)	38109	48(30h)	38265	51(33h)	38462
51200	f1	28(1Ch)	51724	30(1Eh)	50403	36(24h)	50676	38(26h)	51282

这里为了控制接收使用了 $\overline{\text{RTS}}$ 。在 R32C/111 中，因为要输出 $\overline{\text{RTS}}$ ，所以需要设定 $\overline{\text{RTS}}$ 引脚所对应的方向位和功能选择寄存器。

$\overline{\text{RTS}}$ 引脚和端口方向位以及功能选择寄存器的设定如表 3.3 所示。

表 3.3 $\overline{\text{RTS}}$ 引脚和端口方向位以及功能选择寄存器的设定

$\overline{\text{RTS}}$ 引脚	端口	端口方向位	设定值	功能选择寄存器	设定值
$\overline{\text{RTS0}}$	P6_0	PD6_0	1	P6_0S	03h
$\overline{\text{RTS1}}$	P6_4	PD6_4	1	P6_4S	03h
$\overline{\text{RTS2}}$	P7_3	PD7_3	1	P7_3S	03h
$\overline{\text{RTS3}}$	P4_0	PD4_0	1	P4_0S	03h
$\overline{\text{RTS4}}$	P9_4	PD9_4 (注 1)	1	P9_4S (注 1)	03h
$\overline{\text{RTS5}}$	P8_1	PD8_1	1	P8_1S	03h
$\overline{\text{RTS6}}$	P4_4	PD4_4	1	P4_4S	03h

注 1: 请在 PRC2 位设为“1”（允许改写）之后的下一条指令修改这些寄存器。另外，请不要在 PRC2 位设“1”和改写指令之间插入中断或 DMA 传送。

3.1 时钟异步串行接口模式接收数据的说明

- (1) 将 UiC1 ($i = 0 \sim 6$) 寄存器的 RE 位设置为“1”（允许接收）后，进入接收等待状态。此时， $\overline{\text{RTSi}}$ 引脚的输出电平为“L”，将已成为可接收的状态通知发送方。
- (2) 当接收数据的开始位从 RXDi 引脚输入时， $\overline{\text{RTSi}}$ 引脚输出“H”电平，接下来按照数据位（LSB） $\rightarrow \dots$ 数据位（MSB）、校验位、停止位的顺序一位一位地接收。
- (3) 当 UARTi 接收寄存器接收了一个字节的数据后， UARTi 接收寄存器的内容被传送到 UiRB 寄存器中。此时， UiC1 寄存器的 RI 位变为“1”（ UiRB 寄存器中有数据），表示接收完成。另外， SiRIC 寄存器的 IR 位变为“1”（有中断请求）。
- (4) 一旦读取 UiRB 寄存器的低位字节，RI 位就变为“0”（ UiRB 寄存器中无数据）。此时， $\overline{\text{RTSi}}$ 引脚输出电平为“L”。

连接示例如图 3.1 所示，动作时序图如图 3.2 所示。

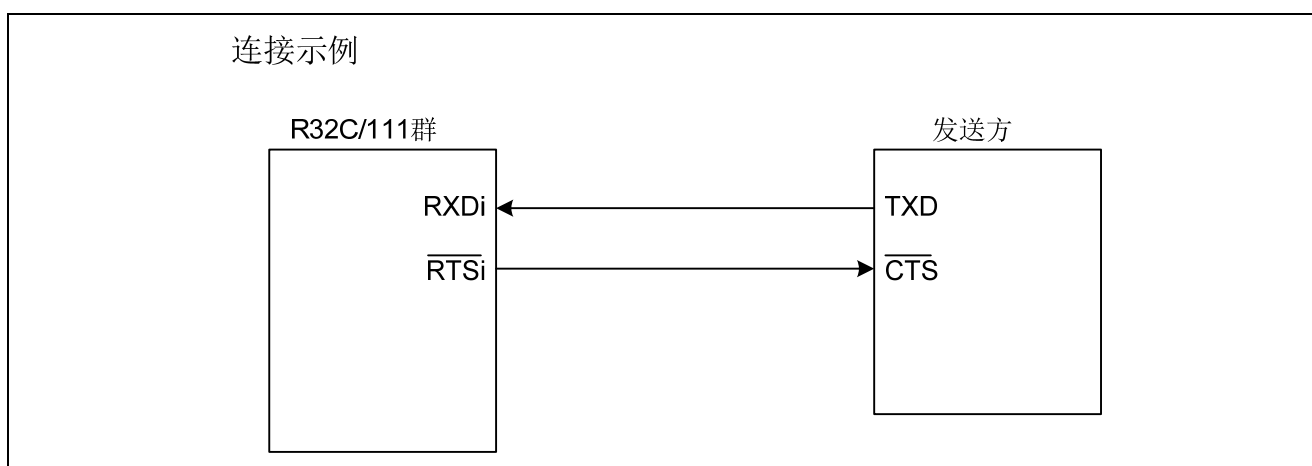


图 3.1 接收数据的连接示例

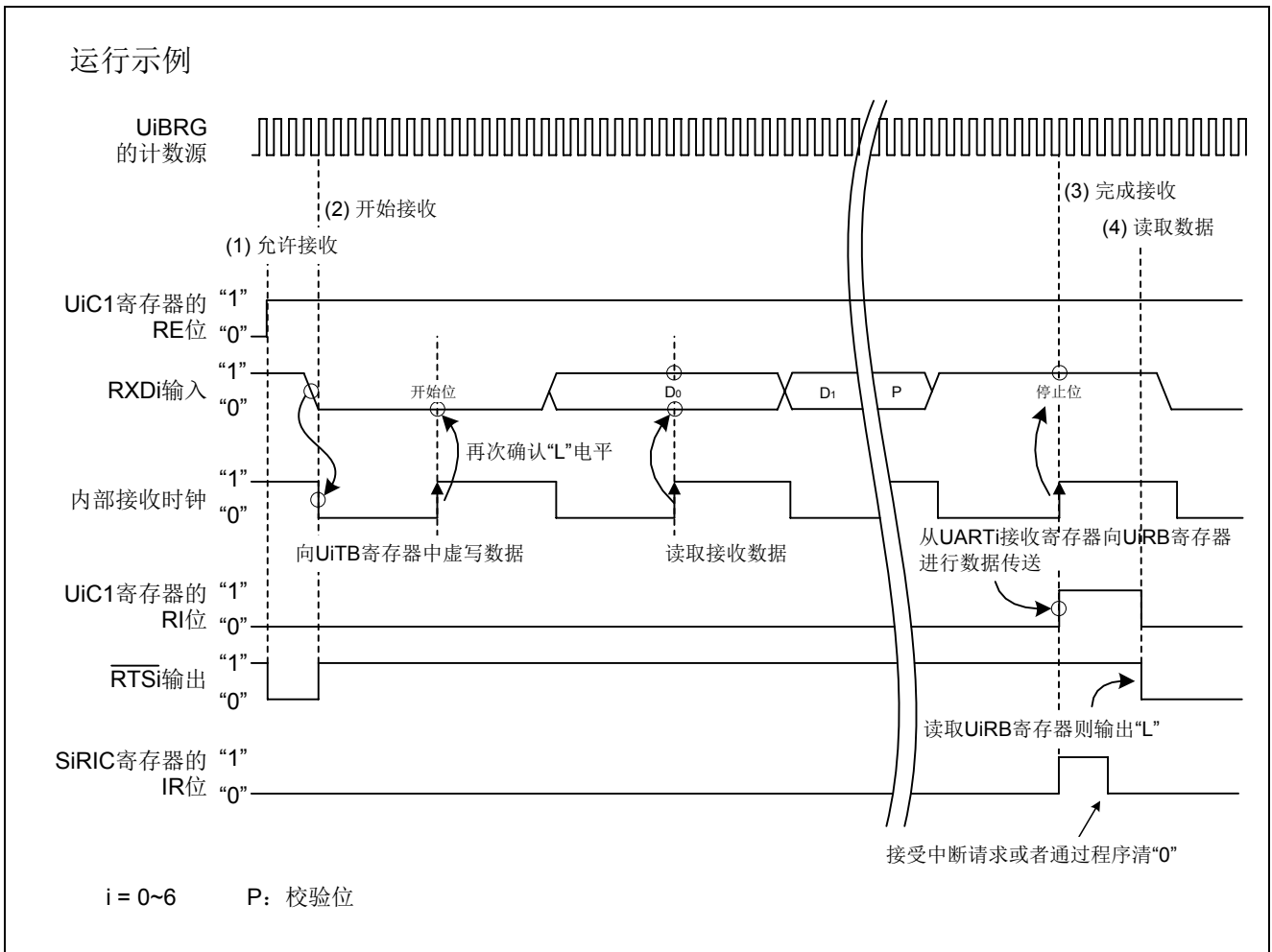


图 3.2 接收动作时序图

3.2 设定方法

本节描述了“3.1 时钟异步串行接口模式接收数据的说明”中的设定步骤及设定值。关于各寄存器的详细信息请参照硬件手册。

这里，UARTi (i=0~6) 的初始化设定后，进入等待接收状态。参考程序中，当程序检测出 UARTi 接收中断的中断请求位变为“1”（有中断请求），保存接收数据。

主函数处理的流程图如图 3.3 所示，UARTi 初始化设置处理的流程图及寄存器设置如图 3.4 所示。

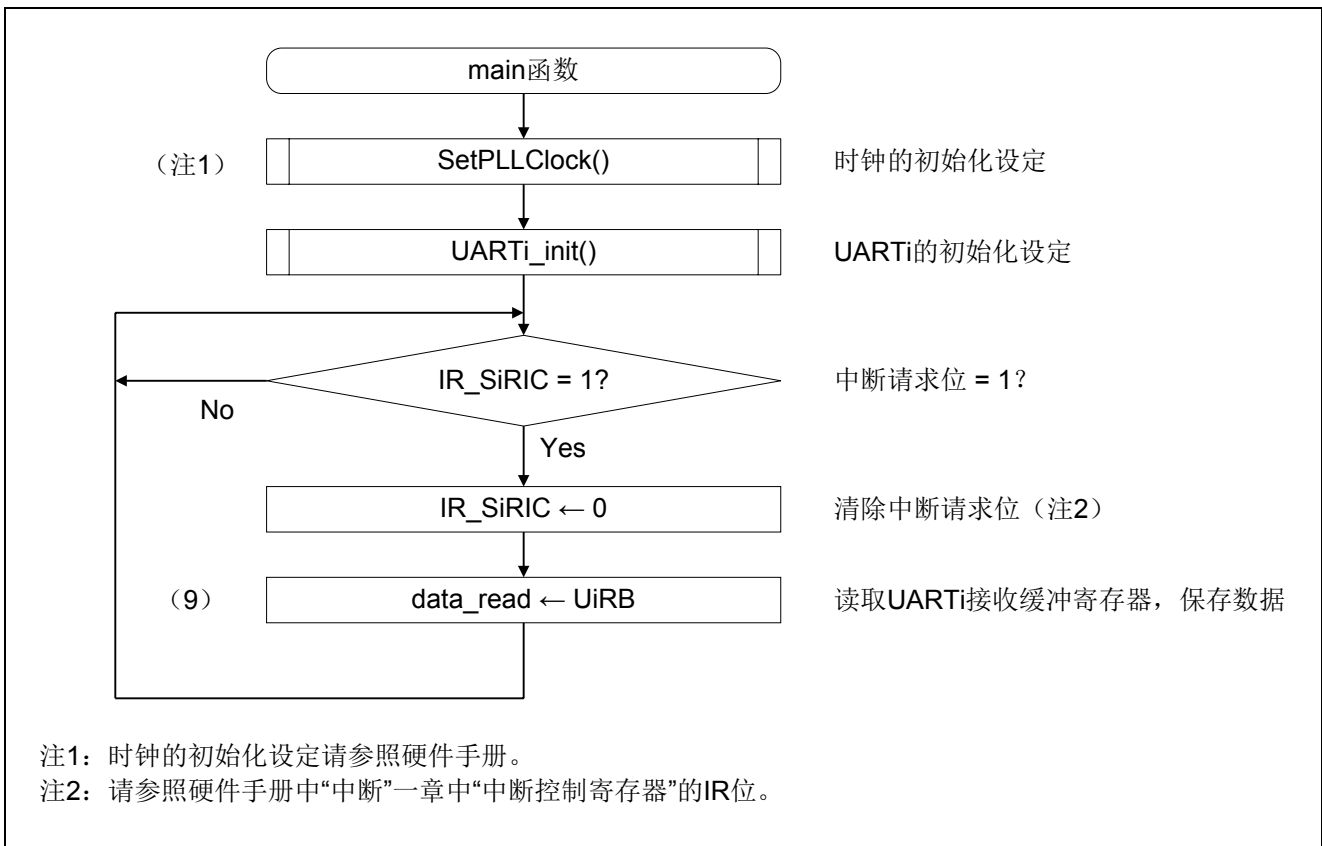


图 3.3 主函数的流程图 (i=0~6)

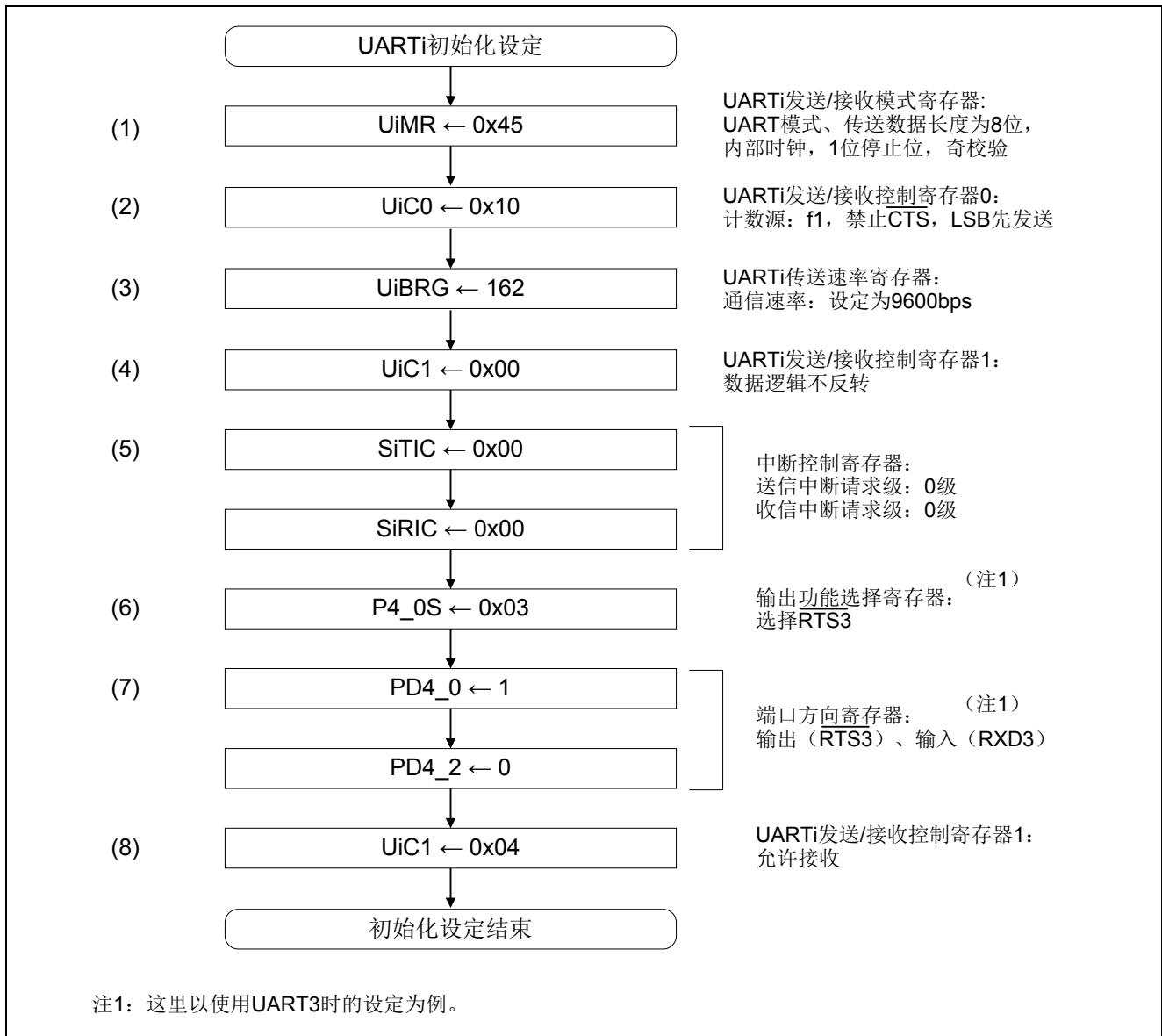
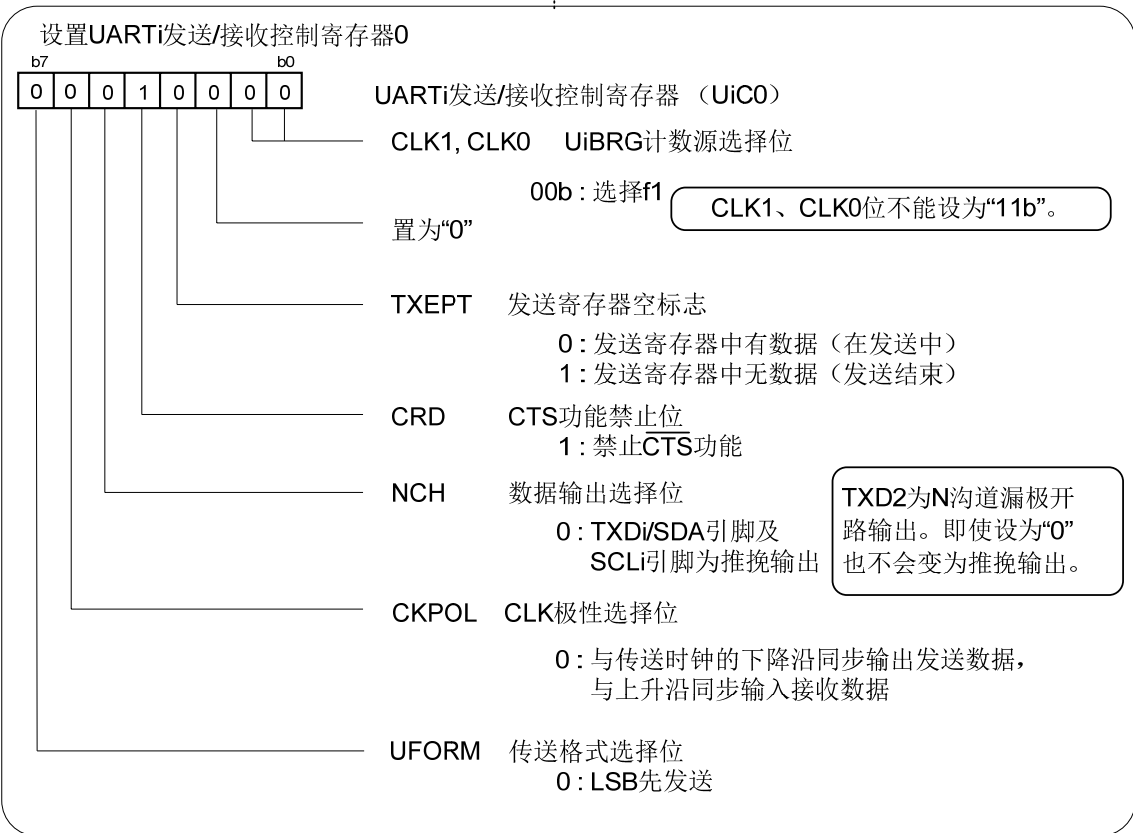
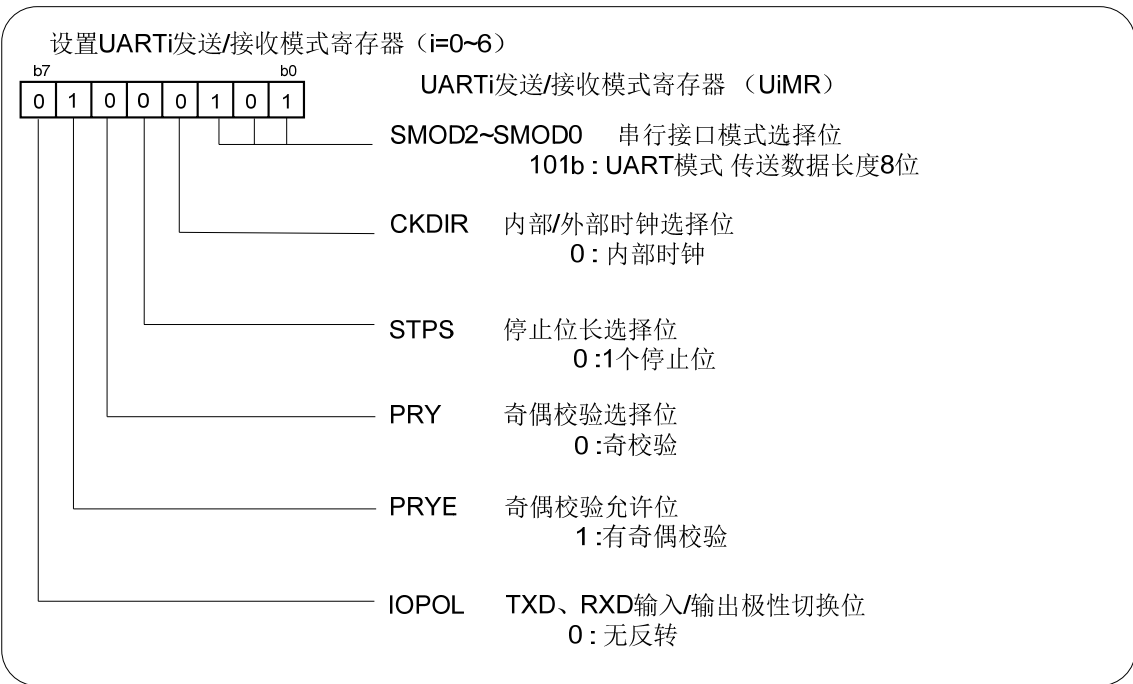
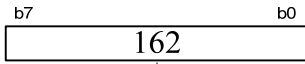


图 3.4 UARTi 初始化设定处理的流程图 (i = 0~6)

3.3 详细设定步骤



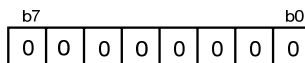
设置UARTi传送速度寄存器（i=0~6）



UARTi传送速度寄存器（UiBRG）

设定值为n时，UiBRG将计数源设为n+1分频
162 : 163分频

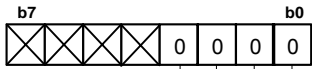
设置UARTi发送/接收控制寄存器1



UARTi发送/接收控制寄存器（UiC1）

- TE 发送允许位
0 : 禁止发送
- TI 发送缓冲器的空标志
0 : UiTB寄存器有数据
1 : UiTB寄存器无数据
- RE 接收允许位
0 : 禁止接收
- RI 接收结束标志
0 : UiRB寄存器无数据
1 : UiRB寄存器有数据
- UiIRS UARTi发送中断源选择位
0 : UiTB寄存器空 (TI=1)
- UiRRM UARTi连续接收模式允许位
0 : 禁止连续接收模式
- UiLCH 数据逻辑选择位
0 : 无反转
- 置为“0”

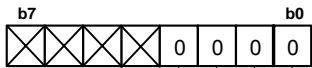
设置中断控制寄存器（i=0~6）



UARTi发送中断控制寄存器（SiTIC）

ILVL2~ILVL0 中断请求级选择位
000b：0级（禁止中断）

IR 中断请求位
0：无中断请求



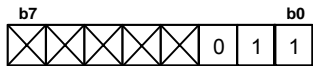
UARTi接收中断控制寄存器（SiRIC）

ILVL2~ILVL0 中断请求级选择位
000b：0级（禁止中断）

IR 中断请求位
0：无中断请求

设置功能选择寄存器

这里以使用UART3时的设定为例。



端口P4_0功能选择寄存器（P4_0S）

PSEL2~PSEL0 端口P4_0输出功能选择位
011b：RTS3输出

设置端口方向寄存器

这里以使用UART3时的设定为例。

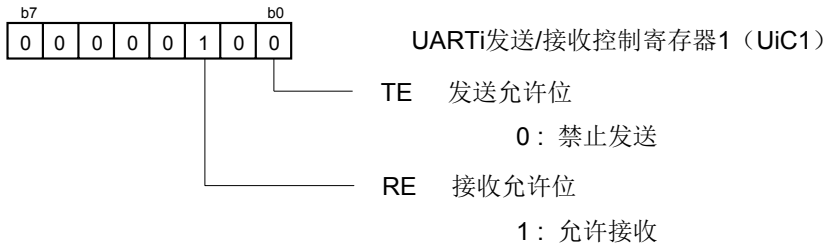


端口P4方向寄存器（PD4）

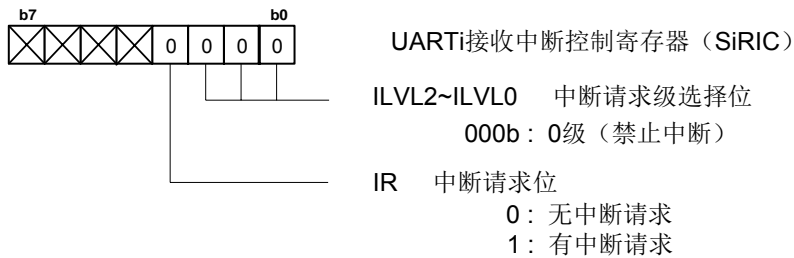
PD4_0 端口P4_0方向位
1：输出

PD4_2 端口P4_2方向位
0：输入

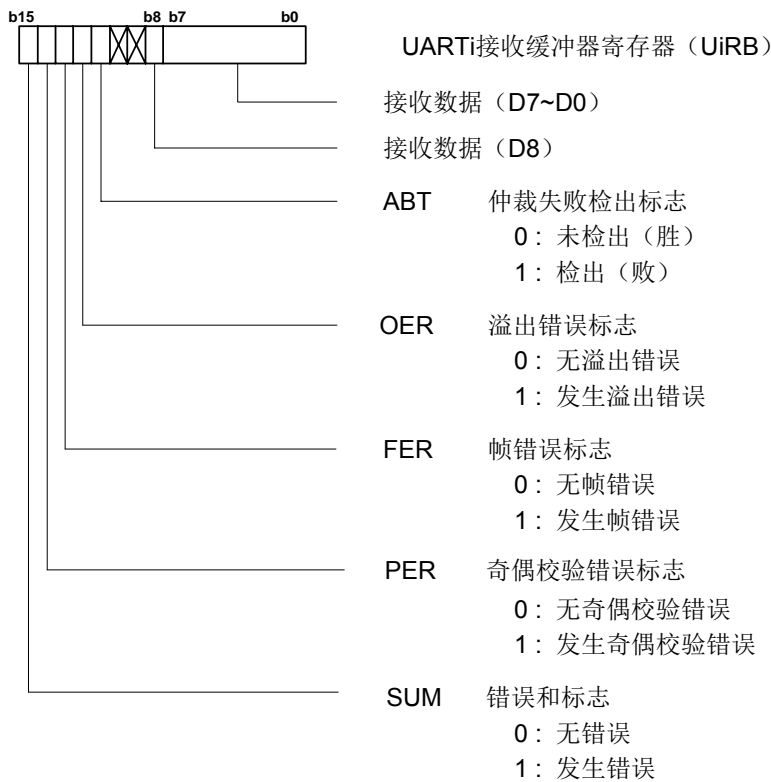
设置UARTi发送/接收控制寄存器1 (i=0~6)



中断请求位的确认及中断控制寄存器的设置



读接收数据并检查错误



4. 参考文献

硬件手册

R32C/111 群硬件手册

（最新版本请从瑞萨电子网站上取得）

技术更新/技术信息

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C 编译器手册

R32C/100 系列用的 C 编译器包 V.1.02 C 编译器用户手册 Rev.1.00

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修订记录	R32C/100 系列 应用说明
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Rev.	发行日	修改内容	
		页	要点
1.00	2011.12.16	—	初版发行

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同样，使用内部上电复位功能对产品进行复位时，从通电到达到复位产生的一定电压的期间，不能保证引脚的状态。

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