

[Revisions to documents]

R20TS0241EJ0100

Rev.1.00

Dec. 16, 2017

**Renesas Starter Kit+ for RX65N-2MB**

**User's Manual Rev.1.00, Circuit Diagram Rev.1.00**

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## Outline

This document is to notify you of modifications to the Renesas Starter Kit+ for RX65N-2MB User's Manual (document number: R20UT3888EG0100) and circuit diagram (document number: R20UT3887EG0100).

## 1. Applicable Documents

➤ User's Manual

Title: Renesas Starter Kit+ for RX65N-2MB User's Manual

Document number: R20UT3888EG0100

Revision: Rev.1.00

➤ Circuit diagram

Title: Renesas Starter Kit+ for RX65N-2MB CPU Board Schematics

Document number: R20UT3887EG0100

Revision: Rev.1.00

## 2. Errata

There was an error in the shipment status of the DIP switch SW4.4 (pin 4 of SW4) on the CPU board.

We are going to correct the error as follows.

Error: SW4.4:OFF

Correct: SW4.4:ON

Refer to the following description for details of the part to be revised.

## 2.1 User's Manual (Document number: R20UT3888EG0100)

Table 6-8, Table 6-9, Table 6-10, Table 6-15, Table 6-16, Table 6-22, Table 6-26, and Table 6-30 in Chapter 6 are to be revised as shown in Figure 1. Text in blue and bold type in the tables represents the shipment status of the CPU board, and the part to be revised is framed in red.

(Error)

Signal name	MCU		MCU Peripheral Selection			Destination Selection		
	Pin	Port	Signal	Fit	DNF	Interface /Function	Fit	DNF
PA7	106	PA7	OnTFT-R4	SW4.4:ON	SW4.3:OFF	ONTFT1.9	MR2	-
			SDRAM-A7	-	<b>SW4.4:OFF</b>	U12.31	-	-
			JA3-A7	-	SW4.3:OFF	JA3.8	-	-

(Correct)

Signal name	MCU		MCU Peripheral Selection			Destination Selection		
	Pin	Port	Signal	Fit	DNF	Interface /Function	Fit	DNF
PA7	106	PA7	OnTFT-R4	<b>SW4.4:ON</b>	SW4.3:OFF	ONTFT1.9	MR2	-
			SDRAM-A7	-	SW4.4:OFF, SW4.3:OFF	U12.31	-	-
			JA3-A7	-	SW4.3:OFF	JA3.8	-	-

\* In this example, TFT is enabled in port PA7 rather than SDRAM/JA3.

Figure 1. Corrections to User's Manual SW4.4 (Example: Excerpt from Table 6-9)

## 2.2 Circuit Diagram (Document number: R20UT3887EG0100)

The default setting of Pin4 of SW4 in "MCU & Emulator Mode and Bus Switch Setting" in page 8 is to be revised as shown in Figure 2. The part to be revised is framed in red.

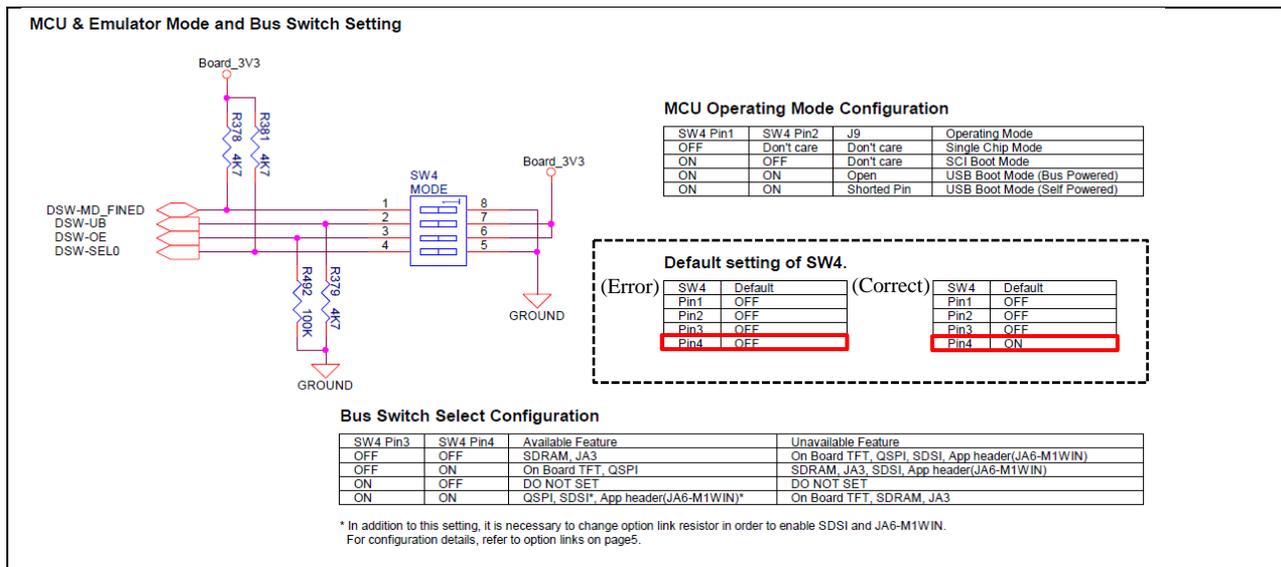


Figure 2. Corrections to SW4.4 in Circuit Diagram

### 3. Obtaining the Improved Edition of the Document

To obtain the document, access the information site for Renesas Starter Kit+ for RX65N-2MB (<https://www.renesas.com/rskrx65n-2mb>) and follow the steps below.

- (1) Click on "Documentation".
- (2) Click on "Document Type".
- (3) Select the [User's Manual: Development Tools] check box.
- (4) Click on [Filter].
- (5) Click on the following title to download the document.  
The document will be available from January 31, 2018.

- Title: Renesas Starter Kit+ for RX65N-2MB User's Manual  
Document number: R20UT3888EG0200
- Title: Renesas Starter Kit+ for RX65N-2MB CPU Board Schematics  
Document number: R20UT3887EG0200

We do not plan to fix the user's manual and circuit diagram in the installers attached to the product.

**Revision History**

Rev.	Date	Description	
		Page	Summary
1.00	Dec. 16, 2017	-	First edition issued

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061 Japan  
 Renesas Electronics Corporation

■Inquiry

<https://www.renesas.com/contact/>

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