

[Notes]

R20TS1053EJ0100

Rev.1.00

Oct. 22, 2024

## RX Family Board Support Package Module

### Using Firmware Integration Technology Rev.7.50

#### Outline

When using the product in the title, note the following points.

1. Selecting the IWDTC as the LPT clock source in the RX261 and RX260 groups.

#### 1. [Note 1]Selecting the IWDTC as the LPT clock source

##### 1.1 Applicable Products

- (1) Board Support Package Module Using Firmware Integration Technology (BSP FIT Module)

The applicable revision and document are as follows.

Table 1.1 BSP FIT Module applicable product

Revision	Document number
Rev.7.50	R01AN1685EJ0750

##### 1.2 Applicable Devices

- RX261 and RX260 groups

##### 1.3 Details

There is an error in the contents of the initial setting processing regarding the LPT.

When the IWDTC is selected as the clock source for the low-power timer (BSP\_CFG\_LPT\_CLOCK\_SOURCE), the IWDTC Count Stop Control Register (IWDTCSTPR) setting process is not executed. Therefore, if you use it in combination with the LPT module(R01AN2571) or the LPT code generation software to set the LPT registers and enable the LPT to return from a low power consumption mode by the LPT, and then execute the WAIT instruction, the IWDTC will stop counting when the system transition to a low power consumption mode. When the IWDTC stops, the LPT count operation also stops, making it impossible to return from the low power consumption mode by the LPT.

##### 1.4 Conditions

When the following conditions are all met:

- BSP FIT module rev.7.50 is used with RX260 and RX261.
- Select IWDTC as the LPT clock source  
(BSP\_CFG\_LPT\_CLOCK\_SOURCE = 1)
- Set the registers for LPT in combination with the LPT module(R01AN2571) or the LPT code generation software to enable to return from a low power consumption mode by the LPT.
- Execute the WAIT instruction.

##### 1.5 Workaround

Please refer to the 1.6 Schedule for Fixing the Problem.

### 1.6 Schedule for Fixing the Problem

The problem is fixed in BSP Rev.7.51 (document number: R01AN1685EJ0751).

Update your product to BSP Rev.7.51.

## Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Oct.22.24	-	First edition issued

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