[Notes] PG-FP6, PG-FP5 Flash Memory Programmer

Outline

When using the PG-FP6, PG-FP5 flash memory programmer, note the following point.

- 1. RX630 group
- 1. RX630 Group
- 1.1 Applicable Products
 - Programming GUI for PG-FP6 (FP6 Terminal) V1.00.00, V1.01.00, V1.01.01, V1.02.00, V1.02.01, V1.03.00, V1.03.01, and V1.03.02
 - Parameter file for PG-FP5 (Parameter file for RX630 group: PR5-R5F5630) V1.01

1.2 Applicable Devices

RX Family RX630 group: R5F5630A and R5F5630B (176 or higher pin-count)

1.3 Details

The applicable devices cannot be used because the following errors occur when executing commands.

- Console window of Programming GUI for PG-FP6, PG-FP5 ERROR(E021): Connection or Synchronisation failed.
- Message display of the PG-FP6 main unit ERROR:021 Inv. Sig code
- Message display of the PG-FP5 main unit ERROR:021 Inv. device code

1.4 Workaround

Download and use the parameter file for R5F5630E from the following URL.

- Parameter file for R5F5630E
 - R5F5630E_L.pr5 (for little endian)
 - R5F5630E_B.pr5 (for big endian)
- Programming GUI for PG-FP6 (FP6 Terminal) V1.03.02 <u>https://www.renesas.com/pg-fp6#downloads</u>
- Parameter file for PG-FP5 (Parameter file for RX630 group: PR5-R5F5630) V1.01 <u>https://www.renesas.com/pg-fp5#downloads</u>



1.5 Schedule for Fixing the Problem

- This problem will be fixed in the Programming GUI for PG-FP6 (FP6 Terminal) V1.03.03. (Scheduled to be released in October 2019.)
- This problem will be fixed in the Parameter file for PG-FP5 (Parameter file for RX630 group: PR5-R5F5630) V1.02 (Scheduled to be released in October 2019.)



Revision History

		Description	
Rev.	Date	Page	Summary
1.00	Sep.16.19	-	First edition issued

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