

[Notes]

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Rev.1.00

Mar.16, 2020

e<sup>2</sup> studio Simulator for RL78/G14,

CS+ Simulator for RL78/G14

## Outline

When using the products in the title, note the following point.

1. When using timer RD for controlling the event count period for timer RJ in event counter mode

## 1. When Using Timer RD for Controlling the Event Count Period for Timer RJ in Event Counter Mode

### 1.1 Applicable Products

- e<sup>2</sup> studio Simulator for RL78/G14 V1.00.00 (e<sup>2</sup> studio V7.0.0) or later
- CS+ Simulator for RL78/G14 V1.00.00 (CS+ for CC V7.00.00) or later

### 1.2 Applicable Devices

RL78 family: RL78/G14 group

### 1.3 Details and Conditions

When using timer RJ in event counter mode, if the settings of the related registers meet the following (1) to (3), the simulator operates differently from device specifications.

- (1) Timer RJ mode register 0 (TRJMR0)

Timer RJ operating mode selection: Event counter mode

- (2) Timer RJ I/O control register 0 (TRJIOC0)

TRJIO count control: Event is counted during polarity period (high-level period or low-level period) specified for timer output signal

- (3) Timer RJ event pin select register 0 (TRJISR0)

Timer output signal selection: "TRDIOD1" or "TRDIOC1" to select timer RD

#### ■ Simulator's operations that differ from device specifications

- When "an event is counted during the high-level period" is selected as polarity selection, the TRJ0 counter does not count, even if the output signal of timer RD is in the high-level period.
- When "an event is counted during the low-level period" is selected as polarity selection, the TRJ0 counter counts, even if the output signal of timer RD is in the high-level period.

### 1.4 Workaround

There is no workaround.

## 1.5 Solution

This problem will be fixed in the next version update in July 2020.

**Revision History**

Rev.	Date	Description	
		Page	Summary
1.00	Mar.16.20	-	First edition issued

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