[Notes] CS+ Integrated Development Environment

R20TS0343EJ0100 Rev.1.00 Sep. 01, 2018

Outline

When using the CS+ integrated development environment, note the following point.

1. Verify specification options of CC-RL

1. Verify Specification Options of CC-RL

1.1 Applicable Products

CS+ for CC Common Program V7.00.00

CC-RL V1.07.00

1.2 Details

If you run a build when the [Check that output address is in internal ROM and data flash] is set to "Yes (-CHECK_OUTPUT_ROM_AREA)" in the Hex Output Options settings, the error message: [E0562006: Option "check_output_rom_area" is ineffective without option "device"] will be output and a build error will occur.

1.3 Conditions

The error will occur when the following condition is met.

The [Check that output address is in internal ROM and data flash] under [Verify] in the [Hex Output Options] settings is set to "Yes (-CHECK_OUTPUT_ROM_AREA)".

1.4 Workaround

To avoid this problem, **specify the device file name with the full path using the -DEVICE option** for [Other additional options] under [Others] in the [Hex Output Options] settings.

Setting example: When using a device file name for R5F104PG

Specify the following file name to where indicated by the red rectangle.

-DEVICE="C:\Program Files (x86)\Renesas Electronics\CS+\CC\Device\RL78\Devicefile\DR5F104PG.DVF"

	Property ×					
1	CC-RL Property					
> > >	Output File Hex Format CRC Operation					
×	Verify Checks that output address is in Yes(-CHECK_OUTPUT_ROM_AREA) Message					
ř	Others Other additional options -DEVICE="C:\Program Files (x86)\Renesas Electronics\CS+\CC\Device\RL78\Devicefile\DR5F104PG.DVF"					
Other additional options						
	Common Options 🖌 Compile Options 🙏 Assemble Options 🖌 Link Options 👌 Hex Output Options 🔏 I/O Header File Generation Options 🦯 🔷 💌					

The device file names and path information used in the project can be found in the microcontroller information of the microcontroller node displayed in the CS+ project tree.

1. Right-click on the Micro	ocontroller node.	
	Property	▼ X
2 0 2 2 	R5F104PG Property	+ - ٩
R5F104PG (Microcontroller)	File Information File name Absolute solute	DR5F104PG.DVF CVProme File (190) Researce Floring (CS-) CC) Device (PI 70) Device file (DR5E104PC, DVF
Change Microcontroller	Absolute path Microcontroller Informat Notes	C:\Program Files (x86)\Renesas Electronics\CS+\CC\Device\RL78\Devicefile\DR5F104PG.DVF ion
RL78 Simulato (Debug Tool) Program Analy r (Analyze Tool) File	File name This is the file name of this de Microcontroller Informat	
2. Select [Property] fi	rom the context menu	.]

1.5 Schedule for Fixing the Problem

This problem will be fixed in the CS+ for CC common program V8.01.00. This program will be available in January 2019.

Revision History

		Description		
Rev.	Date	Page	Summary	
1.00	Sep. 01, 2018	-	First edition issued	

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