[Notes]
CS+ Integrated Development Environment

Outline

When using the CS+ integrated development environment, note the following point.

1. Verify specification options of CC-RL

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1.1 Applicable Products

CS+ for CC Common Program V7.00.00

CC-RL V1.07.00

1.2 Details

If you run a build when the [Check that output address is in internal ROM and data flash] is set to "Yes (-CHECK_OUTPUT_ROM_AREA)" in the Hex Output Options settings, the error message: [E0562006: Option "check_output_rom_area" is ineffective without option "device"] will be output and a build error will occur.

1.3 Conditions

The error will occur when the following condition is met.

The [Check that output address is in internal ROM and data flash] under [Verify] in the [Hex Output Options] settings is set to "Yes (-CHECK_OUTPUT_ROM_AREA)".
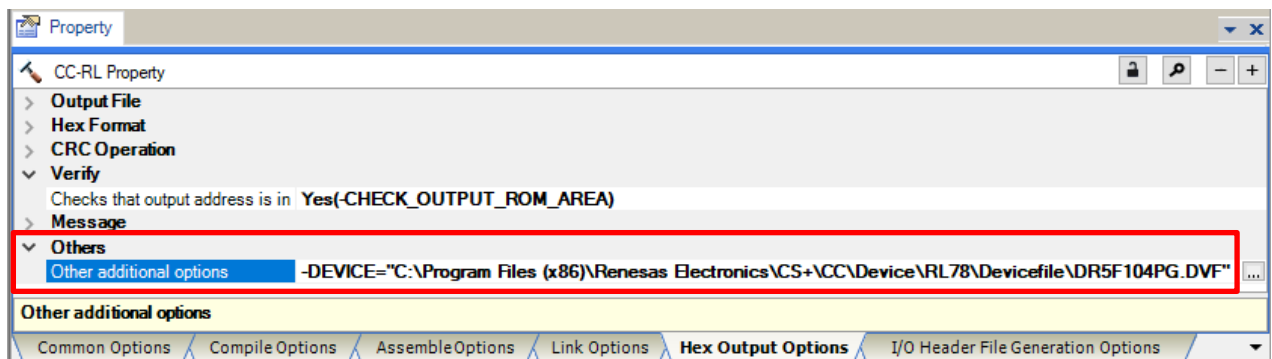
1.4 Workaround

To avoid this problem, **specify the device file name with the full path using the -DEVICE option** for [Other additional options] under [Others] in the [Hex Output Options] settings.
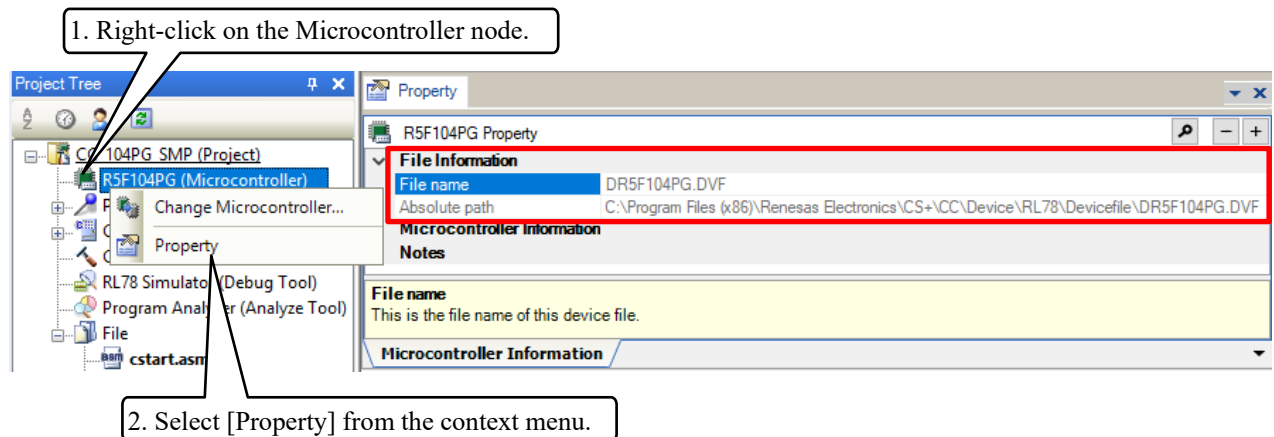
Setting example: When using a device file name for R5F104PG
Specify the following file name to where indicated by the red rectangle.
-DEVICE="C:¥Program Files (x86)¥Renesas Electronics¥CS+¥CC¥Device¥RL78¥Devicefile¥DR5F104PG.DVF"

The device file names and path information used in the project can be found in the microcontroller information of the microcontroller node displayed in the CS+ project tree.

1. Right-click on the Microcontroller node.

2. Select [Property] from the context menu.

1.5   Schedule for Fixing the Problem

This problem will be fixed in the CS+ for CC common program V8.01.00. This program will be available in January 2019.

Revision History

Rev.	Date	Description	
		Page	**Summary**
1.00	Sep. 01, 2018	-	First edition issued

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■Inquiry

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