[Notes]
CS+ Code Generator for RL78 (CS+ for CC),
CS+ Code Generator for RL78 (CS+ for CA,CX),
e^2 studio Code Generator Plug-in,
AP4 Coding Assistance Tool for RL78

R20TS0545EJ0100
Rev.1.00
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Overview

When using the products in the title, note the following points.

1. Callback function setting of CSI and UART
2. Operation that cancels pin function assignment of CSI and UART

1. Callback Function Setting of CSI and UART

1.1 Applicable Products

Products	RL78/I1E	RL78/G11
CS+ Code Generator for RL78 (CS+ for CC)	V2.13.00 (CS+ for CC V5.00.00) or later	V2.12.00 (CS+ for CC V4.01.00) or later
CS+ Code Generator for RL78 (CS+ for CA,CX)	V2.13.00 (CS+ for CA,CX V4.00.00) or later	V2.12.00 (CS+ for CA,CX V3.03.00) or later
Code Generator Plug-in	V2.6.0 (e^2 studio V5.3.0) or later	V2.5.0 (e^2 studio V5.2.0) or later
AP4 for RL78	V1.12.00 or later	V1.11.00 or later

1.2 Applicable Devices

RL78 family: RL78/I1E and RL78/G11(*1) groups

(*1): R5F1054A(16pin), R5F1056A(20pin), R5F1057A(24pin), and R5F1058A(25pin)

1.3 Details

When [Transmit function] is selected in the channel settings, incorrect callback functions might be enabled in CSI or UART, resulting in generation of unintended callback functions.

- RL78/I1E group

  This problem occurs when [Transmit function] is selected in serial array unit [CSI01] or [UART1] with interrupt [INTP2] selected.

- RL78/G11 group

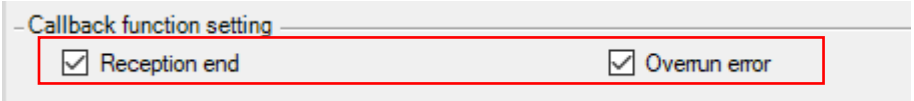
  This problem occurs when the transmission/reception function is switched in serial array unit [CSI11] or [UART1].

  In the case of 5F1056A (20pin), R5F1057A (24pin), and R5F1058A (25pin), the problem occurs when [Pin assignment setting] is set as follows.

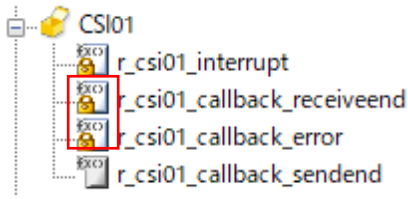
PIOR register	Function	Port setting
PIOR21, PIOR20	RxD0	P31
PIOR21, PIOR20	TxD0	P30

Example: For RL78/I1E group

When [Transmit function] is selected in the channel settings, the [Transmission end] callback function should be enabled in the [CSI01] tab. However, the [Reception end] and [Overrun error] callback functions are enabled as shown in the image below.

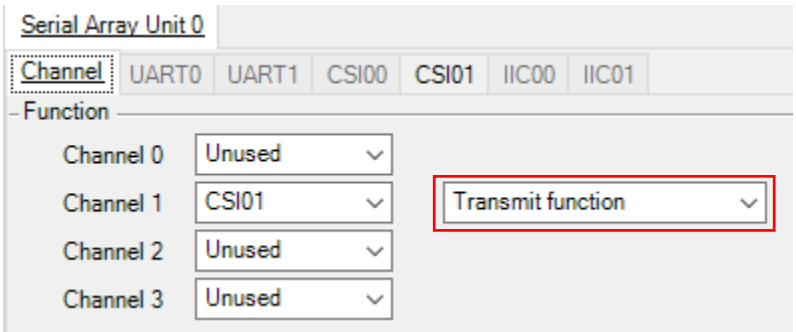


Additionally, if you open a code preview of [CSI01] in this state, you will see incorrect callback functions locked as shown below.



1.4 Workaround

After setting the channels of the serial array unit, do not change the function indicated by the red frame below unless it becomes necessary to do so. If you do need to change it, make the change, then set the channel settings back to “Unused”, and set the channels again.



1.5 Schedule for Fixing the Problem

This problem will be fixed in a later version.

2. Operation That Cancels Pin Function Assignment of CSI and UART

2.1 Applicable Products

- CS+ Code Generator for RL78 V2.13.00 (CS+ for CC V5.00.00) or later
- CS+ Code Generator for RL78 V2.13.00 (CS+ for CA,CX V4.00.00) or later
- Code Generator plug-in V2.6.0 (e^2 studio V5.3.0) or later
- AP4 for RL78 V1.12.00 or later

2.2 Applicable Devices

RL78 family: RL78/I1E group

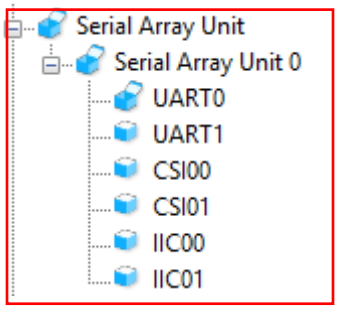
2.3 Details

If you open an item in the [Serial Array Unit] folder again after setting CSI or UART, the pin function assignment of the serial array unit is canceled, and the pin conflict judgment does not function correctly.

For example, if you set UART0 and open [Serial Array Unit] in the [Pin Function] tab in [Device List View], a pin function is assigned as follows.

Pin Function	Available Assignment	Search Available Assignment	Pin Number	Pin Direction
RXD0	P14/SI00/RXD0/SDA00/TI02/TO02/INTP5/TOOLRXD		C2 (P14)	In
RXD1	-		Not assigned	-

If you open the item in the [Serial Array Unit] folder in the peripheral function tree, the pin function assignment is canceled.



Pin Function	Available Assignment	Search Available Assignment	Pin Number	Pin Direction
RXD0	-		Not assigned	-
RXD1	-		Not assigned	-

2.4 Workaround

After setting CSI or UART, do no open any item in the [Serial Array Unit] folder.

If you open an item in the [Serial Array Unit] folder and the pin function assignment is deleted, set the channel settings back to “Unused”, then set the serial array unit again.

2.5 Schedule for Fixing the Problem

This problem will be fixed in a later version.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Feb.01.20	-	First edition issued

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