

RENESAS TOOL NEWS on May 1, 2004: RSO-E7\_1-040501D

## A Note on Using the Tiny/SLP E7 Emulator Software --On Single-Step Program Execution--

Please take note of the following problem in using the Tiny/SLP E7 Emulator Software:

- On single-step program execution

### 1. Versions Concerned

Tiny/SLP E7 Emulator Software V.2.0.01, V.2.0.02, V.2.1.00 and V.2.3.01

### 2. Description

When the instructions listed in the table below exist in the program of an MCU belonging to the R8C/Tiny series, the problems in the right column arise if these instructions are step-executed.

Instruction	Problem
PUSHC FLG (Save the flag register)	The IPL (processor interrupt priority level) of the flag register restored by POPC FLG goes to 0.
LDC src,FLG (Load src data to the flag register)	The I flag of the flag register may not be cleared.
STC FLG,dest (Store the contents of the flag register to dest)	Bit 6 of the flag register is always set to 0 and bit 1 always to 1.

### 3. Workaround

Don't perform the following setting and execution for the above instructions:

- Setting hardware and software breakpoints

Executing step-in, step-over, and step-out operations

#### 4. **Schedule of Fixing the Problem**

We plan to fix this problem in our next release of the products.

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