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A Note on Using the Simulator Debugger for the SuperH RISC Engine; and for the H8S Families and the H8/300 Series of MCUs --On Setting Access to Memory Resources--

Please take note of the following problem in using the simulator debugger for the SuperH RISC engine; and the one for the H8S families and H8/300 series of MCUs:

- On setting access to memory resources
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1. Products and Versions Concerned

- Simulator debugger V.9.04.00 for the SuperH RISC engine family
(this is included with the C/C++ compiler package V.9.00.Release 00 through Release 02 for the SuperH RISC engine family)
- Simulator debugger V.5.03.00 for the H8S family and H8/300 series
(this is included with the C/C++ compiler package V.6.01.Release 00 for the H8, H8S, and H8SX families)

To check for the version number of your simulator debugger, follow these steps:

- (1) In the High-performance Embedded Workshop, open the Tool menu and select the Administration command. The Tool Administration dialog box appears.
- (2) Select the name of your simulator debugger in the Registered Components list and click the Properties button. The Properties dialog box opens.
- (3) Click the Information tab, and you see the version number of yours.

2. Description

When "Read" is selected as the type of access to a memory resource and a user program is loaded into any area in this memory resource, the type of access to the area where the user program has been loaded is changed to "Read/Write."

3. **Workaround**

After loading the program, change the type of access to the memory resource by performing the following steps:

- (1) In the High-performance Embedded Workshop, open the Option menu and select the Simulator -> Memory Resources command to open the Simulator System dialog box.
- (2) Select the memory resource to which you want to change the type of access, click the Change button, and then open the Set Memory Resource dialog box.
- (3) Select the type you want to use out of the Attribute drop down list box and click the OK button.
- (4) Also click the OK button in the Simulator System dialog box.

4. **Schedule of Fixing the Problem**

We plan to fix this problem in the release of the simulator debugger V.9.05.00 for the SuperH RISC engine family and the simulator debugger V.5.04.00 for the H8S family and H8/300 series.

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