

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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MESC TECHNICAL NEWS

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Note on Executing 32-Bit Operation Instructions in 7900 Series MCUs

1. Affected Devices

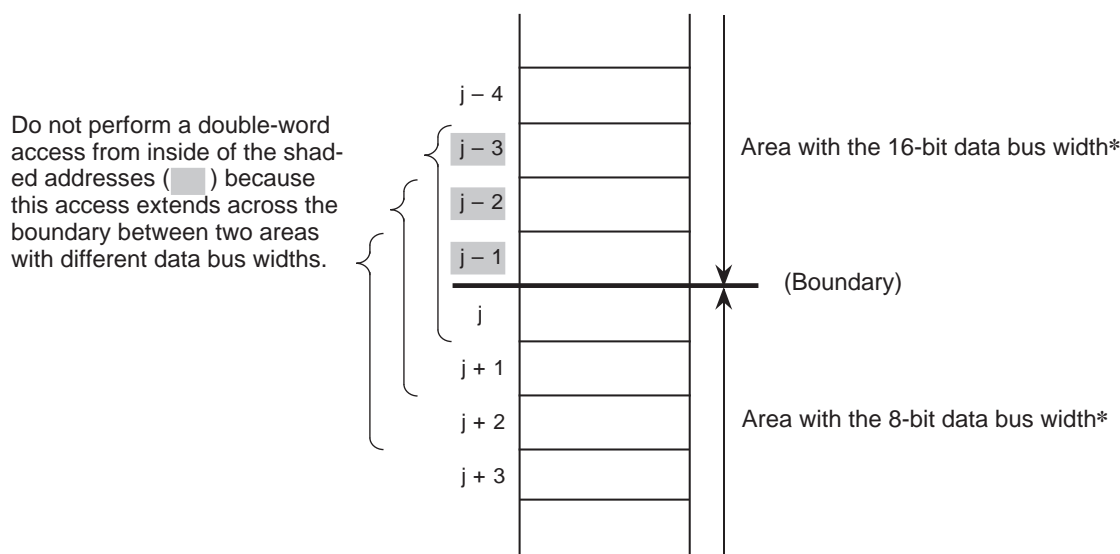
All of the 7900 Series MCUs

2. Affected Instructions

ADCD, ADDD, ADDMD, ANDMD, CMPD, CMPMD, EORMD, LDAD, ORAMD, SBCD, STAD, SUBD, SUBMD

3. Caution

When performing memory access with one of the 32-bit operation instructions shown above, make sure that this access does not extend across the boundary between two areas with different data bus widths; that is, when an area with the 16-bit data bus width (including an internal area) is immediately followed by another area with the 8-bit one, do not perform a double-word (32-bit) access from inside of three bytes preceding the boundary.



* : If an area with the 8-bit data bus width is allocated to the low-order addresses and an area with the 16-bit one is allocated to the high-order addresses, the above caution is also applied.

4. Symptoms

A double-word access that extends across the boundary between two areas with different data bus widths may result in the following symptoms:

- (1) Incorrect accesses before and after the boundary may occur.
- (2) The CPU may run away.