

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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# RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan  
Renesas Technology Corp.

Product Category	MPU&MCU	Document No.	TN-H8*-A345A/E	Rev.	1.00
Title	Specification change of port C in the H8/36077 Group		Information Category	Technical Notification	
Applicable Product	H8/36077 Group	Lot No.	Reference Document	H8/36077 Group Hardware Manual (REJ09B0216-0100 Rev.1.00)	
		All lots			

We would like to inform you of the following changes in the specification of port C in the H8/36077 Group manual. Please note the changes before use of the product.

## 1. Specification Change of Port C

When the OSC1 pin (PC0) is used for input of the external clock, the specification of the OSC2 (PC2) pin is changed as follows.

[Before Change]

PC0/OSC1 pin — External clock input

PC1/OSC2 pin — General port

[After Change]

PC0/OSC1 pin — External clock input

PC1/OSC2 pin — (Open)\*

Note: \* This pin cannot be used as a general port pin.

## 2. Changes in the Hardware Manual

### 5.2.4 Clock Control/Status Register (CKCSR)

[Before change]

Bit	Bit Name	Initial Value	R/W	Description																				
7	PMRC1	0	R/W	Port C Function Select 1 and 2																				
6	PMRC0	0	R/W	<table border="1"> <thead> <tr> <th>PMRC1</th> <th>PMRC0</th> <th>PC1</th> <th>PC0</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>I/O</td> <td>I/O</td> </tr> <tr> <td>1</td> <td>0</td> <td>CLKOUT</td> <td>I/O</td> </tr> <tr> <td>0</td> <td>1</td> <td>I/O</td> <td>OSC1 (external clock input)</td> </tr> <tr> <td>1</td> <td>1</td> <td>OSC2</td> <td>OSC1</td> </tr> </tbody> </table>	PMRC1	PMRC0	PC1	PC0	0	0	I/O	I/O	1	0	CLKOUT	I/O	0	1	I/O	OSC1 (external clock input)	1	1	OSC2	OSC1
				PMRC1	PMRC0	PC1	PC0																	
				0	0	I/O	I/O																	
				1	0	CLKOUT	I/O																	
0	1	I/O	OSC1 (external clock input)																					
1	1	OSC2	OSC1																					

[After change]

Bit	Bit Name	Initial Value	R/W	Description																				
7	PMRC1	0	R/W	Port C Function Select 1 and 0																				
6	PMRC0	0	R/W	<table border="1"> <thead> <tr> <th>PMRC1</th> <th>PMRC0</th> <th>PC1</th> <th>PC2</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>I/O</td> <td>I/O</td> </tr> <tr> <td>1</td> <td>0</td> <td>CLKOUT</td> <td>I/O</td> </tr> <tr> <td>0</td> <td>1</td> <td>(Open)</td> <td>OSC1 (external clock input)</td> </tr> <tr> <td>1</td> <td>1</td> <td>OSC2</td> <td>OSC1</td> </tr> </tbody> </table>	PMRC1	PMRC0	PC1	PC2	0	0	I/O	I/O	1	0	CLKOUT	I/O	0	1	(Open)	OSC1 (external clock input)	1	1	OSC2	OSC1
				PMRC1	PMRC0	PC1	PC2																	
				0	0	I/O	I/O																	
				1	0	CLKOUT	I/O																	
				0	1	(Open)	OSC1 (external clock input)																	
1	1	OSC2	OSC1																					

5.5.3 Inputting External Clock

[Before change]

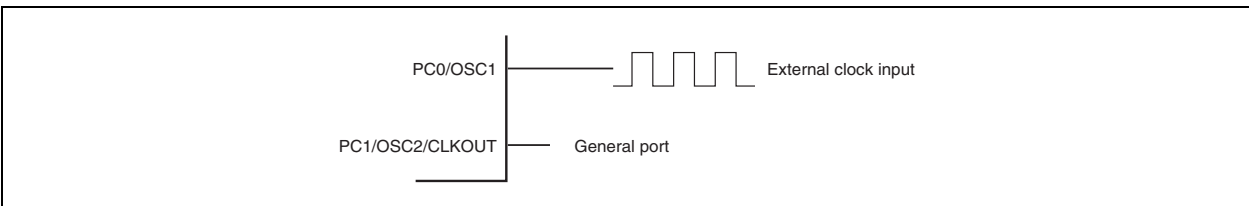


Figure 15.14 Example of External Clock Input

[After change]

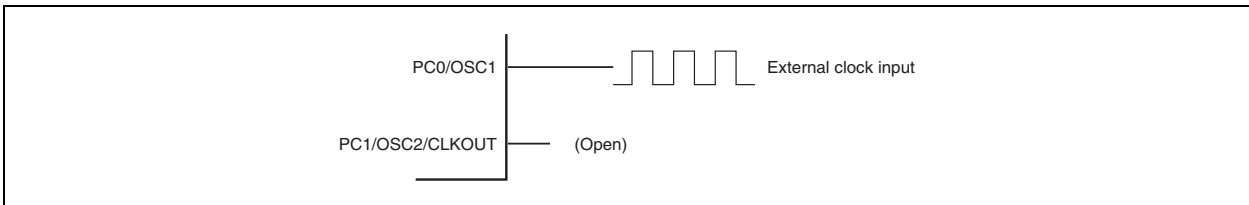


Figure 15.14 Example of External Clock Input

9.9.3 Pin Functions

[Before change]

Register	CKCSR		PCRC	Pin Function
	PMRC1	PMRC0	PCRC1	
Setting value	0	X	0	PC1 input pin
			1	PC1 output pin
	1	0	X	CLKOUT output pin
			1	X

[After change]

Register		CKCSR		PCRC	Pin Function
Bit Name	PMRC1	PMRC0	PCRC1		
Setting value	0	0	0	PC1 input pin	
			1	PC1 output pin	
		1	X	(Open)	
	1	0	X	CLKOUT output pin	
		1	X	OSC2 oscillation pin	

3. Time of Application

(1) Changes of description in the hardware manual

The changes will be reflected from the next version of the manual.

(2) Specification changes of the product

The changes of the specification are scheduled to be effective from the shipments of January 2007.