RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A0070A/E	Rev.	1.00
Title	Specification Change for Pin Assignment of RZ/G1E		Information Category	Technical Notification		
Applicable Product	RZ/G Series RZ/G1E	Lot No.	Reference Document	RZ/G1E1 User's Manual: Hardware Rev.1.00 (R01UH0544EJ0100)		
		All lots				

There is a following specification change about the RZ/G1E.

[Summary]

   Specification change for Pin Assignment.

   QSPI=78MHz should be removed in Boot Device Selection.

   Because the setting of "QSPI = 78 MHz" is not currently used in boot device setting.

[Products]

   RZ/G1E

[Note]

   This document includes specification change.

[Specification Change]

1.   QSPI=78MHz should be removed in Boot Device Selection.

Because the setting of "QSPI = 78 MHz" is not currently used in boot device setting.

[Correction]

  (Gray highlighted parts (abcd) are corrected.)

1.   Section 3. Pin Assignment

Section 3.3 Mode Pin Settings - Boot Device Selection

Current (from):

MD3	MD2	MD1	Boot Device Selection
0	0	0	area 0 boot (boot from external MaskROM)
0	1	0	QSPI (48.75 MHz 16 KB transfer)
0	0	1	Reserved
0	1	1	Reserved
1	0	0	QSPI (39 MHz 16 KB transfer)
1	0	1	QSPI (78 MHz 16 KB transfer)
1	1	0	QSPI (39 MHz 4 KB transfer)
1	1	1	Reserved

Correction (to):

MD3	MD2	MD1	Boot Device Selection
0	0	0	area 0 boot (boot from external MaskROM)
0	1	0	QSPI (48.75 MHz 16 KB transfer)
0	0	1	Reserved
0	1	1	Reserved
1	0	0	QSPI (39 MHz 16 KB transfer)
1	0	1	**Reserved**
1	1	0	QSPI (39 MHz 4 KB transfer)
1	1	1	Reserved

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