HITACHI MICROCOMPUTER TECHNICAL UPDATE

DATE	11 January 2001	No.	TN-SH7-290A/E
THEME	SH7729,SH7709A usage notice about IRL,IRLS interrupt		
CLASSIFICATION	☐ Spec change ☐ Supplement of Documents	■ Limitation on Use	
PRODUCTNAME			Lot No.etc.
REFERENCE DOCUMENTS	SH7729 Hardware manual SH7709A Hardware manual	Rev.	EffectiveDate Eternity
			From

HD6417729 and HD6417709A have followed usage notice.

1. Problem

When the following conditions are all satisfied, an unexpected interrupt may happen after IRL or IRLS level changes. At this time, INTEVT and INTEVT2 hold the previous value.

- BL bit in SR register: BL=1
- Interrupt mask bit in SR register: I[3:0]<15
- Internal clock frequency: Iø>120MHz

2. Work around

Either (1) or (2) is work around.

(1) Please change IRL or IRLS level while BL=1

(2) Please change IRL or IRLS level while I[3:0]=15