

2. work around

Work around of (A) and (B)

- Please don't use PCMCIA and DMAC at the same time.
- When you use PCMCIA and DMAC at the same time, please set A5(6)TEH[2:0] to 3'b000.

Work around of (A)

- Please set AM bit in CHCRn to the setting that DACK is asserted in PCMCIA cycle.

Work around of (B)

- Please fix DACK to high during PCMCIA bus cycle.

3. Notes

Even if the DACK is negated incorrectly, other signals for DMA are generated correctly.

Even if the DACK is asserted incorrectly, DMA bus cycle never be generated while the incorrect DACK signal is asserted.