

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A633A/E	Rev.	1.00
Title	SH7618/SH7619, Setting for PFC		Information Category	Technical Notification		
Applicable Product	HD6417618ABG100(V),HD6417618ABGN100(V), HD6417618ABGW100(V),HD6417618RBG100(V), HD6417618RBGN100(V),HD6417618RBGW100(V), R4S76190B125BGV,R4S76190N125BGV, R4S76190W125BGV,R4S76190D125BGV, R4S76190B125BG,R4S76190N125BG, R4S76190W125BG,R4S76190D125BG	Lot No.	Reference Document	SH-2 SH7618 Group Hardware Manual (REJ09B0131-0600 Rev.6.00) SH-2 SH7619 Group Hardware Manual (REJ09B0237-0500 Rev.5.00)		
		All lots				

We would like to inform valued customers of the restriction in using SH7618/SH7619 Group as follows.

-Note-

1. Restriction in Using

In the use of the pins listed in the following table, please set the data registers and also pin function controller (i.e. in the use of the output functions EXCEPT the function 1, please DO NOT change the initial value (=0) in the data registers of that pins).

Table 1. Pins restricted in using in SH7618/SH7619

Product	Function 1 (Related Module)	Function 2 (Related Module)	Function 3 (Related Module)	Function 4 (Related Module)
SH7618	PD2 input/output (port)	IRQ2 input (INTC)	TxD1 output (SCIF)	-
	PD4 input/output (port)	IRQ4 input (INTC)	SCK1 input/output (SCIF)	-
	PD5 input/output (port)	IRQ5 input (INTC)	TxD2 output (SCIF)	-
SH7619	PD2 input/output (port)	IRQ2 input (INTC)	TxD1 output (SCIF)	DREQ0 input (DMAC)
	PD4 input/output (port)	IRQ4 input (INTC)	SCK1 input/output (SCIF)	DACK0 output (DMAC)
	PD5 input/output (port)	IRQ5 input (INTC)	TxD2 input/output (SCIF)	DREQ1 input (DMAC)

2. Details of Restriction

For the logical specs of the output functions of the pins listed in the above table (i.e. logical sum of the value of the data register), when the data register of the pins is set to '1', the output of the pins will be FIXED to '1' (=High). For the initial value of that data register is '0', it DOES NOT cause any problems in the use of NOT writing any data at all after power-on-reset. In addition, the output is fixed to '1' (=High) in the use of writing '1' to the data register, it must be safe with the sets that have already worked without any problems UNLESS change the value of PFC, whereas input functions do work safety even in the function 1.

*This notification is only for SH7618/SH7619.