

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HITACHI MICROCOMPUTER TECHNICAL UPDATE

DATE	12 December 2000	No.	TN-SH7-281A/E
THEME	SH3/SH3DSP usage notice ADJ bit of RTC		
CLASSIFICATION	<input type="checkbox"/> Spec. change <input checked="" type="checkbox"/> Limitation on Use <input type="checkbox"/> Supplement of Documents		
PRODUCT NAME	HD6417708, HD6417708S, HD6417708R, HD6417718R, HD6417707, HD6417709, HD6417709A, HD6417729, HD6417729R, HD6417709S		
REFERENCE DOCUMENTS	SH7708 series Hardware manual, SH7707 Hardware manual, SH7709 Hardware manual, SH7709A Hardware manual, SH7729 Hardware manual, SH7729R Hardware manual	Effective Date	Eternity
		From	

HD6417708, HD6417708S, HD6417708R, HD6417718R, HD6417707, HD6417709, HD6417709A, HD6417729, HD6417729R and HD6417709S have followed usage notice.

1. Condition

The maximum time from 1) to 2) is 91.6 us (when 32.768kHz clock oscillator is connected to EXTAL2 pin).

- 1) Setting 1 to RCR2.ADJ.
- 2) The second counter register (RSECCNT) read value has 30 second adjustment.

The second counter itself has done the 30 second adjustment at the same time when RCR2.ADJ bit is set to 1, so the counting of RTC will not have any problem.

2. Note on usage

If it is needed to read adjusted second counter value, please read the second counter register after 91.6us from when adjust bit is set.