

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A030A/E	Rev.	1.00
Title	RZ/G1M User's Manual: Hardware Corrections		Information Category	Technical Notification		
Applicable Product	RZ/G Series, RZ/G1M	Lot No.	Reference Document	RZ/G1M User's Manual: Hardware Rev.1.00 Sep. 30, 2016 (R01UH0626EJ0100)		
		All lots				

There are following document corrections about the RZ/G1M user's manual: hardware.

[Summary]

RZ/G1M User's manual: hardware corrections (manual errata).

[Products]

RZ/G1M

[Note]

There is no specification change (document correction only).

[Corrections]

1. Section 1. Overview

1) Figure 1.1 R-Car M2-W System Configuration, Sub-system bus connection (page 1-2):

(Gray parts (abcd) are corrected in figure 1.1.)

Current (from): SDHI, MMCIF and TSIF modules are connected to the AXI-bus.

Correction (to): SDHI, MMCIF and TSIF modules are connected to the APB.

2) Section1.4 Power Supply Voltage and Temperature Range, MMC, STAT, PCI Express, USB3.0 power supply voltage (typ.)

(page 1-22):

Current (from): MMC, SATA, PCI Express, USB3.0 are all in 1.8 V.

Correction (to): MMC is removed from 1.8 V because MMC is only supported at 3.3 V, and SATA, PCI Express, USB3.0 are moved to 1.03V.

3) Section1.4 Power Supply Voltage and Temperature Range, DDR3_GPIO power supply voltage (typ.) (page 1-25):

Current (from): - (missing)

Correction (to): DDR3_GPIO is added to 1.8 V.

2. Section 3. Pin Assignment

Section 3.1 Top View (Left) and 3.2 Top View (Right), Pins name (pages 3-1 and 3-2):

(Gray parts (abcd) are corrected or newly added.)

Pin Location	Pin Name		
	Current (from)	Correction (to)	
Top View (Left)	F16	VDD_CPGPLL0	VDD_CPGPLL1
	F15	VSS_CPGPLL0	VSS_CPGPLL1
	H15	VDD_CPGPLL1	VDD_CPGPLL2
	G15	VSS_CPGPLL1	VSS_CPGPLL2
Top View (Right)	L25	VDD_CPGPLL2	VDD_CPGPLL0
	M25	VSS_CPGPLL2	VSS_CPGPLL0

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