

Microcomputer Technical Information

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1. Affected product

V850 Series real-time OS RX850 Pro

2. Details of correction

See the attachment.

3. Document revision history

V850 Series Real-time OS RX850 Pro Correction of User's Manual

Document Number	Issued on	Description
SBG-DT-03-0240-E (latest version)	August 14, 2003	Newly created.

1. Correction of Technical User’s Manual

1.1 Scheduler

The descriptions in **4.4 Scheduler** (page 58) include errors. Substitute Table 4-2 and Figure 4-4 shown below for those in the user’s manual.

Table 4-2. Structure of Task Context

Information	Offset Value	Size	Description
ctx_sts	0x0	0x4	Task context status Context status (Bit position: Meaning) 0 to 7 : Task-specific psw value 8 to 10 : Context status 0x0: Dispatch by system call 0x2: On creation of task 0x4: Pre-empt by interrupt 11 to 31 : RFU
ctx_retd	0x4	0x4	Return value from system call
ctx_stacd	0x4	0x4	Task start code
ctx_pc	0x8	0x4	RFU
ctx_gp	0xc	0x4	Task-specific global data pointer
ctx_tp	0x10	0x4	Task-specific text pointer
ctx_r20	0x14	0x4	Variable register
ctx_r21	0x18	0x4	
ctx_r22	0x1c	0x4	
ctx_r23	0x20	0x4	
ctx_r24	0x24	0x4	
ctx_r25	0x28	0x4	
ctx_r26	0x2c	0x4	
ctx_r27	0x30	0x4	
ctx_r28	0x34	0x4	
ctx_r29	0x38	0x4	
ctx_r30	0x3c	0x4	Task-specific ep
ctx_ctpc	0x40	0x4	CTPC (V850E compatible version only)
ctx_ctpsw	0x44	0x4	CTPSW (V850E compatible version only)

Figure 4-4. Relationship Between Task Management Block (t_tsk) and Task Context

