

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A122B/E	Rev.	2.00
Title	RX64M Group Modified List of Pin and Pin Functions (177-Pin TFLGA, 176-Pin LFBGA) (4/7)		Information Category	Technical Notification		
Applicable Product	RX64M Group	Lot No.	Reference Document	RX64M Group User's Manual: Hardware Rev 1.1 (R01UH0377EJ0110)		
		All				

This document describes modifications in Table 1.5 List of Pin and Pin Functions (177-Pin TFLGA, 176-Pin LFBGA) (4/7).

These contents are included in the previous TU (TN-RX\*-A122A/E, Rev1.00), however, they were missed to be corrected in the RX64M Group User's Manual: Hardware Rev1.1. We only mention the missed part in this version as the other contents had already been reflected.

## 【Before correction】

Pin Number 177-Pin TFLGA 176-Pin LFBGA	Power Supply Clock System Control	I/O Port	Bus EXDMAC SDRAMC	Timer (MTU, GPT, TPU, TMR, PPG, RTC, CMTW, POE, CAC)	Communication (ETHERC, SClg, SCIh, RSPI, RIIC, CAN, USB, SSI)	Memory Interface Camera Interface (QSPI, SDHI, MMCIF, PDC)	Interrupt	S12ADC, R12DA
M5	VCC_USB	P12	WR3#/BC3#	MTIC5U/TMCI1	RXD2/SMISO2/ SSCL2/ SCL0[FM+]		IRQ2	
M6	AVCC_USBA							
M7	USBA_RREF	P11		MTIC5V/TMCI3	SCK2/ USBA_VBUS/ USBA_VBUSEN		IRQ1	
M8	VCC_USBA	P10	ALE	MTIC5W/TMRI3	USBA_OVRCURA		IRQ0	
R8		P11		MTIC5V/TMCI3	SCK2/ USBA_VBUS/ USBA_VBUSEN			

【After correction】

Pin Number 177-Pin TFLGA 176-Pin LFBGA	Power Supply Clock System Control	I/O Port	Bus EXDMAC SDRAMC	Timer (MTU, GPT, TPU, TMR, PPG, RTC, CMTW, POE, CAC)	Communication (ETHERC, SC1g, SCIh, RSPI, RIIC, CAN, USB, SSI)	Memory Interface Camera Interface (QSPI, SDHI, MMCIF, PDC)	Interrupt	S12ADC, R12DA
M5	VCC_USB							
M6	AVCC_USBA							
M7	USBA_RREF							
M8	VCC_USBA							
R8		P11		MTIC5V/TMCI3	SCK2/ USBA_VBUS/ USBA_VBUSEN		IRQ1	

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