

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-R8C-A019A/E	Rev.	1.00
Title	Revisions for Input Threshold Control Register 0 (VLT0) Specifications		Information Category	Technical Notification		
Applicable Product	R8C/LA6A Group R8C/LA8A Group	Lot No.	Reference Document			

Input threshold control register 0 (VLT0) specifications in the R8C/LA6A Group and R8C/LA8A Group User's Manual: Hardware Rev.1.02 (R01UH0051EJ0102) have been revised described below. Corresponding descriptions of the user's manual will be revised and issued as Rev.1.03.

7.5.16 Input Threshold Control Register 0 (VLT0)

Address 01F5h

Bit	b7	b6	b5	b4	b3	b2	b1	b0
Symbol	VLT07	VLT06	VLT05	VLT04	VLT03	VLT02	VLT01	VLT00
After Reset	0	0	0	0	0	0	0	0

Bit	Symbol	Bit Name	Function	R/W
b0	VLT00	P00, P01, P02, P03, P04, P05, P06	^{b1 b0} 0 0: 0.50 × VCC 0 1: 0.35 × VCC 1 0: 0.70 × VCC 1 1: Do not set.	R/W
b1	VLT01	input level select bit		R/W
b2	VLT02	P07, P10, P11, P12, P13, P14, P15,	^{b3 b2} 0 0: 0.50 × VCC 0 1: 0.35 × VCC 1 0: 0.70 × VCC 1 1: Do not set.	R/W
b3	VLT03	P16, P17 input level select bit		R/W
b4	VLT04	P20, P21, P22, P23, P24, P25, P26,	^{b5 b4} 0 0: 0.50 × VCC 0 1: 0.35 × VCC 1 0: 0.70 × VCC 1 1: Do not set.	R/W
b5	VLT05	P27, P36 input level select bit		R/W
b6	VLT06	P30, P31, P32, P33, P34, P35, P37	^{b7 b6} 0 0: 0.50 × VCC 0 1: 0.35 × VCC 1 0: 0.70 × VCC 1 1: Do not set.	R/W
b7	VLT07	input level select bit		R/W

The VLT0 register selects the voltage level of the input threshold values for ports P0 to P3. Bits VLT00 to VLT07 are used to select the input threshold values among three voltage levels (0.35 VCC, 0.50 VCC, and 0.70 VCC).