

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

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RENESAS TECHNICAL UPD

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|--------------------|---|----------|----------------------|--|------|------|
| Product Category | User Development Environment | | Document No. | TN-CSX-055A/EA | Rev. | 1.00 |
| Title | Release Information of H8S, H8/300 Series C/C++ Compiler Package Ver.6.0.00 | | Information Category | Specification Change | | |
| Applicable Product | PS008CAS6-MWR PS008CAS6-SLR PS008CAS6-H7R | Lot No. | Reference Document | H8S, H8/300 Series C/C++ Compiler Assembler Optimizing Linkage Editor User's Manual REJ10B0058-0100H Rev.1.0 | | |
| | | All lots | | | | |

H8S, H8/300 Series C/C++ compiler package is updated in Ver.6.0.00.
Refer to the attached document, PS008CAS6-031007E, for details.

Attached: PS008CAS6-031007E
H8S, H8/300 Series C/C++ Compiler Ver.6.0.00 Updates

H8S,H8/300 Series C/C++ Compiler Package Ver. 6.0.00 Updates

The contents of updates in this package are shown below.
The item 1 and 2 hold true only for PC version.

1 High-performance Embedded Workshop (Ver. 3.0 -> Ver. 3.0.01)

1.1 Fixed HEW Network database message box on Windows(R) Me

On Windows(R) ME, HEW is invoked and HEW will not show HEW Network database message box.

1.2 Adding and Modifying the Data Generated by the Project Generator

Project generation of the following CPUs has been newly added:

H8XS/1650, H8/36014F, H8/36024F, H8/36037F, H8/36057F, H8/3694F,
H8/38000, H8/38001, H8/38002F, H8/38004F

The I/O definition files (iodefine.h) of the following CPUs have been modified:

H8/3687

2 H8S,H8/300 Series simulator/debugger(Ver. 5.0.00 -> Ver. 5.1.00)

2.1 Supporting the H8SX Simulator

The H8SX simulator is supported.

Debugging the H8SX will be enabled.

3 Compiler (Ver. 4.0.04 -> Ver. 6.0.00)

3.1 Support for New CPU

Creation of an object file with a CPU type of H8SX is supported.

3.2 Support for 2-byte Pointer (only in H8SX)

The `__ptr16` keyword or option `ptr16` can be used to specify use of a 2-byte pointer.

They are valid in H8SX advanced mode or H8SX maximum mode.

3.3 Assembly Capability (only in H8SX)

The `__asm` keyword can be used to allow the assembly language to be used in a C/C++ source program.

4 Assembler (Ver. 4.1 -> Ver. 6.0.01)

4.1 Support for the New CPU

Creation of an object file with a CPU type of H8SX is supported.

5 Optimizing linkage editor (Ver. 7.1.07 -> Ver. 8.0.00)

5.1 Support for New CPU

Input of an object file with a CPU type of H8SX is supported.

5.2 Specifying Memory Size Used

The memory option specifies the used size of internal memory.

6 Standard Library Generator (Ver. 1.0.02 -> Ver. 2.0.00)

6.1 Reentrant library

If the reent option is specified to the standard library generator, a reentrant library is created.