

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU	Document No.	TN-RA*-A0056A/E	Rev.	1.00
Title	RA4W1 Group, correction of Wake Up Interrupt Enable Register (WUPEN)		Information Category	Technical Notification	
Applicable Product	RA4W1 Group	Lot No.	Reference Document	RA4W1 Group User's Manual Hardware Rev.1.00	
		All			

The description of Wake Up Interrupt Enable Register (WUPEN) is corrected.

## [Before]

### 14.2.9 Wake Up Interrupt Enable Register (WUPEN)

Address(es): ICU.WUPEN 4000 61A0h

	b31	b30	b29	b28	b27	b26	b25	b24	b23	b22	b21	b20	b19	b18	b17	b16
	IIC0WUPEN	AGT1CBWUPEN	AGT1CAWUPEN	AGT1UDWUPEN	USBFSWUPEN	—	RTCPRDWUPEN	RTCALMWUPEN	ACMPLPOWUPEN	—	—	VBATTWUPEN	—	LVD1WUPEN	KEYWUPEN	IWDTWUPEN
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
	IRQWUPEN[15:0]															
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Bit	Symbol	Bit name	Description	R/W
b15 to b0	IRQWUPEN[15:0]	IRQ Interrupt Software Standby Returns Enable	0: Disable Software standby returns by IRQ interrupt 1: Enable Software standby returns by IRQ interrupt.	R/W
b16	IWDTWUPEN	IWDT Interrupt Software Standby Returns Enable	0: Disable Software standby returns by IWDT interrupt. 1: Enable Software standby returns by IWDT interrupt.	R/W

The bits in this register control whether the associated interrupt can wake up the CPU from Software Standby mode.

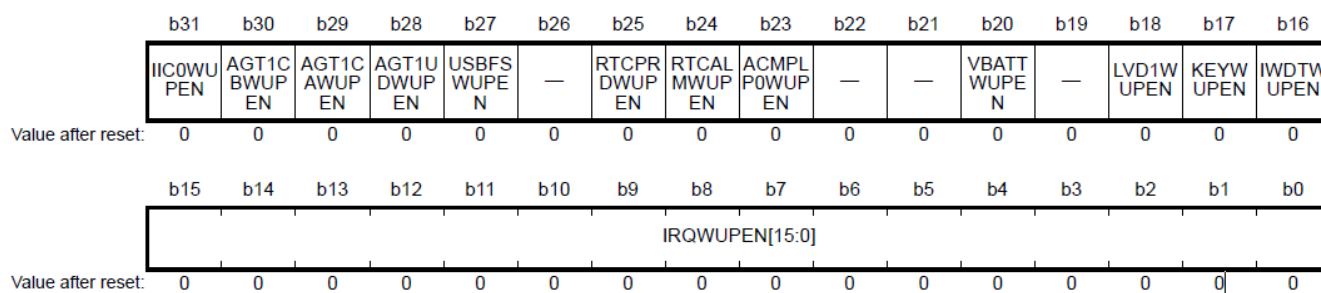
### IRQWUPEN[15:0] bits (IRQ Interrupt Software Standby Returns Enable)

The IRQWUPEN[15:0] bits enable the use of IRQn interrupts to cancel Software Standby mode. Only 0 to 4, 6, 7, 9, 11, 14, 15 bits can be used.

[After]

### 14.2.9 Wake Up Interrupt Enable Register (WUPEN)

Address(es): ICU.WUPEN 4000 61A0h



Bit	Symbol	Bit name	Description	R/W
b15 to b0	IRQWUPEN[15:0]	IRQ Interrupt Software Standby Returns Enable	0: Disable Software standby returns by IRQ interrupt 1: Enable Software standby returns by IRQ interrupt.	R/W
b16	IWDTWUPEN	IWDT Interrupt Software Standby Returns Enable	0: Disable Software standby returns by IWDT interrupt. 1: Enable Software standby returns by IWDT interrupt.	R/W

The bits in this register control whether the associated interrupt can wake up the CPU from Software Standby mode.

#### IRQWUPEN[15:0] bits (IRQ Interrupt Software Standby Returns Enable)

The IRQWUPEN[15:0] bits enable the use of IRQn interrupts to cancel Software Standby mode. Only 0 to 4, 6 to 9, 11, 14, 15 bits can be used.