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Concerned Products:	Customer Notification	Date: May 25th, 1999
		NEC-Electronics (Europe) GmbH EAD –Technical Product Support
IE-789177-NS-EM1	Bug Report	Source Doc: SBG-T-1593 SBG-T-1632
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	May 11th, 99	Doc. No.: TPS-LE-B-ST177
1 st revision	: May 25 th , 99	Doc. No.: TPS-LE-B-ST177-1

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(A) BUG LIST

Bug No.	Outline	IE-789177-NS-EM1 Control code: A	IE-789177-NS-EM1 Control code: B	IE-789177-NS-EM1 Control code: C
1	A/D conversion time		✓	✓
2	Input with low voltage at port 5		✓	✓
3	TM8 PWM mode bug		✓	✓
4	μPD78917xY devices supported			✓

- ✓: No problem
- : Bug (will be corrected by next version upgrade)
- : Bug (restriction, not corrected by version upgrade)

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(B) BUG DESCRIPTION

Bug No.	Outline	Description
1	A/D conversion time	<u>Detail:</u> A/D conversion time differs from the value set

Bug No.	Outline	Description
2	Input with low voltage at port 5	<u>Detail:</u> There is a possibility that the input value of port 5 will not be read correctly during low voltage operation (Vdd 3.5V or lower)

Bug No.	Outline	Description
3	8-bit PWM timer (TM80/TM81) restrictions	<u>Detail:</u> To use these timers in interval timer mode, rewrite the value of the compare registers (CR80/CR81) in a state where the timer operation is inhibited. Rewriting the value of a compare register (CRxx) in a state where the timer operation is permitted may generate coincidence signals immediately. (In the case that interrupts are permitted, interrupt requests will occur.)

Bug No.	Outline	Description
4	μPD78917xY devices supported	<u>Detail:</u> μPD78917xY devices are supported from Control Code: C onwards. Y → SMB (System Management Bus)