

Exclusively for design purposes; no part of this may be disclosed to a third party without the consent of NEC Electronics (Europe) GmbH.

<b>Concerned Products:</b>	<b>Customer Notification</b>	Date: 14 March, 2001
		NEC-Electronics (Europe) GmbH EAD -Technical Product Support
<i>IE-784038-R-EM1</i>  <i>I/O-Emulation board for the <math>\mu</math>PD78403x devices</i>	<b>Bug Report</b> <b>IE-784038-R-EM1</b>	Source Doc: SUD-T-5146-1-E
		Author: A. Twardy
Date of initial issue :	Mar. 14 <sup>th</sup> 2001	Doc. No.: TPS-LE-B-4T006-0

**Table of contents**

- (A) Bug list
- (B) Bug description
- (C) History list

Exclusively for design purposes; no part of this may be disclosed to a third party without the consent of NEC Electronics (Europe) GmbH.

**(A) BUG LIST**

Bug No.	Outline	IE-784038-R-EM1					
		Rev./ Control code <sup>NOTE1</sup>					
		A	B	C			
		RANK of the peripheral EVA-CHIP					
		V 1.0	V 2.0 K	V 4.0 MM or P			
1	78402x subseries is not supported any longer	✓	☛	☛			
2	Value of timer register	☞	☞	✓			
3	Synchronisation with compare register	☞	☞	✓			
4	Start bit may be missing during UART transmission	☞	☞	✓			
5	Oscillation stabilisation time after releasing IDLE mode	☞	☞	✓			

- ✓ : No problem
- ☞ : Bug (will be corrected by version upgrade)
- ☛ : Bug (restriction, not corrected by version upgrade)

NOTE 1 : The control code is the **second letter** from the left of the 10 digit serial code on the warranty that comes with this product when it is purchased (version that have not been upgraded). For upgraded versions, an upgrade label is affixed to the product and the final letter of the label (i.e. the X in the V-UP LEVEL X) indicates the control code

Exclusively for design purposes; no part of this may be disclosed to a third party without the consent of NEC Electronics (Europe) GmbH.

**(B) BUG DESCRIPTION**

Bug No.	Outline	Description
1	78402x subseries is not supported any longer	<p><u>Details</u> In contrast to the R-Type I/O emulation board IE-784038-R-EM1 (control code A) this version does not support the 78402x subseries any longer.</p> <p><u>Workaround</u> There is no workaround.</p>
2	Value of timer register	<p><u>Details</u> The read value of timer register may be illegal.</p> <p><u>Workaround</u> There is no workaround.</p>
3	Synchronisation with compare register	<p><u>Details</u> Interruption in synchronisation with compare register may not be performed correctly.</p> <p><u>Workaround</u> There is no workaround.</p>
4	Start bit may be missing during UART transmission	<p><u>Details</u> Start bit may be missing during UART transmission.</p> <p><u>Workaround</u> There is no workaround.</p>
5	Oscillation stabilisation time after releasing IDLE mode	<p><u>Details</u> Oscillation stabilisation time set by oscillation stabilisation time setting register may have to be waited after releasing IDLE mode.</p> <p><u>Workaround</u> There is no workaround.</p>

Exclusively for design purposes; no part of this may be disclosed to a third party without the consent of NEC Electronics (Europe) GmbH.

**(C) History list**

No.	Outline	Description
1	First release of this document	1.1 No changes related to the initial release of this document