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Concerned Products:	Customer Notification	Date: 7 March, 2001
		NEC-Electronics (Europe) GmbH EAD -Technical Product Support
<i>IE-784038-NS-EM1</i> <i>I/O-Emulation board for the μPD78403x devices</i>	Bug Report IE-784038-NS-EM1	Source Doc: SBG-T-0607 SUD-T-5147-1-E
		Author: A. Twardy
Date of initial issue :	Oct. 5 th 1999	Doc. No.: TPS-LE-B-4T003-0
1 st revision :	Mar. 7 th 2001	Doc. No.: TPS-LE-B-4T003-1

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(A) BUG LIST

Bug No.	Outline	IE-784038-NS-EM1					
		Rev./ Control code ^{NOTE1}					
		A	B	C			
		RANK of the peripheral EVA-CHIP					
		V 1.0	V 2.0 K	V 4.0 MM or P			
1	78402x subseries is not supported any longer	✓	☛	☛			
2	Value of timer register	☞	☞	✓			
3	Synchronisation with compare register	☞	☞	✓			
4	Start bit may be missing during UART transmission	☞	☞	✓			
5	Oscillation stabilisation time after releasing IDLE mode	☞	☞	✓			

- ✓ : No problem
- ☞ : Bug (will be corrected by version upgrade)
- ☛ : Bug (restriction, not corrected by version upgrade)

NOTE 1 : The control code is the **second letter** from the left of the 10 digit serial code on the warranty that comes with this product when it is purchased (version that have not been upgraded). For upgraded versions, an upgrade label is affixed to the product and the final letter of the label (i.e. the X in the V-UP LEVEL X) indicates the control code

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(B) BUG DESCRIPTION

Bug No.	Outline	Description
1	78402x subseries is not supported any longer	<p><u>Details</u> In contrast to the R-Type I/O emulation board IE-784038-R-EM1 for the 78403x subseries the IE-784038-NS-EM1 does not support the 78402x subseries any longer.</p> <p><u>Workaround</u> There is no workaround. If the 78402x subseries should be emulated, please use R-Tool environment.</p>
2	Value of timer register	<p><u>Details</u> The read value of timer register may be illegal.</p> <p><u>Workaround</u> There is no workaround.</p>
3	Synchronisation with compare register	<p><u>Details</u> Interruption in synchronisation with compare register may not be performed correctly.</p> <p><u>Workaround</u> There is no workaround.</p>
4	Start bit may be missing during UART transmission	<p><u>Details</u> Start bit may be missing during UART transmission.</p> <p><u>Workaround</u> There is no workaround.</p>
5	Oscillation stabilisation time after releasing IDLE mode	<p><u>Details</u> Oscillation stabilisation time set by oscillation stabilisation time setting register may have to be waited after releasing IDLE mode.</p> <p><u>Workaround</u> There is no workaround.</p>

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(C) History list

No.	Outline	Description
1	Updates from Doc.-No.: TPS-LE-B-4T003-0 to Doc.-No.: TPS-LE-B-4T003-1	1.1 Addition of the restrictions described under point 2. 1.2 Addition of the restrictions described under point 3. 1.3 Addition of the restrictions described under point 4. 1.4 Addition of the restrictions described under point 5. 1.5 Addition of the History list related to point (C). 1.6 Adapting of the first page "cover page".