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Concerned Products: IE-78K0-NS-P01	Customer Notification		Date: April 16 th , 1998
			NEC-Electronics (Europe) GmbH EAD – Technical Product Support
	Bug Report		Source Doc: SBG-T-0601, SBG-T-0968, SBG-T-1545, SBG-T-1550
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1 st revision	: Apr. 16 th , 99	Doc. No.:	TPS-LE-B-0T009-1
2 nd revision	:	Doc. No.:	TPS-LE

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(A) BUG LIST (1/2)

Bug No.	Outline	IE-78K0-NS-P01				
		V1.A Code "A"	V1.B Code "B"	V1.C Code "C"	V1.D Code "D"	
1	Problem on debugger start-up	☞	✓	✓	✓	
2	Problem on restarting the debugger	☞	✓	✓	✓	
3	Low voltage operation	☞	✓	✓	✓	
4	Reading SFR	☹	☞	✓	✓	
5	Wait pin	☹	☹	☹	☞	
6	FIP Display RAM	☹	✓	✓	✓	
7	Serial Buffer RAM	☹	☞	✓	✓	
8	Low voltage operation with IE-78001-R-A	☹	☹	☞	✓	

✓: No problem

☞: Bug (will be corrected by next version upgrade)

☹: Bug (restriction, not corrected by version upgrade)

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(B) BUG DESCRIPTION

Bug No.	Outline	Description
1	Problem on debugger start-up	When starting the debugger ID78K0 and port 4 is connected to VSS a "Receive timeout" error occurs.
2	Problem on restarting the debugger	After terminating the debugger ID78K0, the restart of the debugger causes a "Receive timeout" error Workaround: After terminating the debugger ID78K0, turn the power supply of before restarting it.
3	Low voltage operation	When the hardware trigger start function of the AD-converter is active, the signal output level is fixed to 5V. This function can not be used at a low voltage operation
4	Reading SFR	The SFR's at address FF1xh may not be read correctly
5	Wait pin	The Wait pin can not be used as port pin when the external memory access is activated.
6	FIP Display RAM	The read values from the FIP display are incorrect when the IE-78K0-NS-P01 is connected to the IE-780233-NS-EM4.
7	Serial Buffer RAM	The read values from the serial buffer RAM are incorrect when the IE-78K0-NS-P01 is connected to the IE-780233-NS-EM4.
8	Low voltage operation with IE-78001-R-A	The low voltage operation is not possible in association with the IE-78001-R-A.