

Exclusively for design purposes; no part of this may be disclosed to a third party without the consent of NEC Electronics (Europe).

Concerned Products:	Customer Notification	Date: May 27 th 97
<i>IE-703017-MC-EM1</i>		NEC-Electronics (Europe) GmbH EAD –Technical Product Support
	Bug Report	Source Doc: HW-021298 HW-052698
		Author: H.-W. Hoeft
Date of initial issue: Jan 14 th 97		Doc. No.: TPS-HE-B-2711

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(A) BUG LIST

Bug No.		Outline	EM1 board	
			1.10	2.21
1		PORT,PM,PU,PMC Read Data	☞	✓
2		Supply voltage	☞	☞

- ✓: No problem
- ☞: Bug (corrected by version upgrade)
- : Bug (restriction, not corrected by version upgrade)

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(B) BUG DESCRIPTION

Bug No.	Outline	Discription																												
1	PORT,PM,PU,PMC Read Data	<p>When ICE read the following internal I/O, ICE could not read the unused bits that normally 0 is read .</p> <table border="0" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 15%;"></th> <th style="width: 15%;"></th> <th style="width: 20%; text-align: center;">real chip read value</th> <th style="width: 50%; text-align: center;">ICE read value</th> </tr> </thead> <tbody> <tr> <td>P11</td> <td>bit5-7</td> <td style="text-align: center;">000aaaaa</td> <td style="text-align: center;">xxxxaaaa</td> </tr> <tr> <td>PM11</td> <td>bit4-7</td> <td style="text-align: center;">0001aaaa</td> <td style="text-align: center;">xxxxaaaa</td> </tr> <tr> <td>PU11</td> <td>bit4-7</td> <td style="text-align: center;">0000aaaa</td> <td style="text-align: center;">xxxxaaaa</td> </tr> <tr> <td>P12</td> <td>bit1-7</td> <td style="text-align: center;">0000000a</td> <td style="text-align: center;">xxxxxxxxa</td> </tr> <tr> <td>PM12</td> <td>bit1-7</td> <td style="text-align: center;">0000000a</td> <td style="text-align: center;">xxxxxxxxa</td> </tr> <tr> <td>PMC12</td> <td>bit1-7</td> <td style="text-align: center;">0000000a</td> <td style="text-align: center;">xxxxxxxxa</td> </tr> </tbody> </table> <p>x:unkown a: right data</p>			real chip read value	ICE read value	P11	bit5-7	000aaaaa	xxxxaaaa	PM11	bit4-7	0001aaaa	xxxxaaaa	PU11	bit4-7	0000aaaa	xxxxaaaa	P12	bit1-7	0000000a	xxxxxxxxa	PM12	bit1-7	0000000a	xxxxxxxxa	PMC12	bit1-7	0000000a	xxxxxxxxa
		real chip read value	ICE read value																											
P11	bit5-7	000aaaaa	xxxxaaaa																											
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P12	bit1-7	0000000a	xxxxxxxxa																											
PM12	bit1-7	0000000a	xxxxxxxxa																											
PMC12	bit1-7	0000000a	xxxxxxxxa																											
2	Supply voltage	<p><u>Details</u></p> <p>This ICE works correctly when Vdd and BVdd are at 3.0V- 3.6V, instead of specified 2.7V-3.6V .</p>																												