

## Customer Notification

# QB-V850EIX3

## In-Circuit Emulator

## Operating Precautions

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### Target Device:

**V850E/IF3**

**V850E/IG3**

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**(A) Table of Operating Precautions**

No.	Outline	μPD70F3451, μPD70F3452 μPD70F3453 μPD70F3454	
		Rev.	
		Rank <sup>Note</sup>	
1	Interrupts Input INTP14, INTP15, INTP16 may not be acknowledged (Specification Change Notice)		X

✓: Not applicable

X: Applicable

**Note:** The rank is indicated by the letter appearing at the 5th position from the left in the lot number, marked on each product.

**(B) Description of Operating Precautions**

<p>No. 1</p>	<p>Interrupts Input INTP14, INTP15, INTP16 may not be acknowledged (Specification Change Notice)</p>
<p><u>Details</u></p> <p>If digital noise elimination is disabled for the following pins (signals), interrupts input from these pins may not be acknowledged on the microcontroller side.</p> <ul style="list-style-type: none"> <li>• INTP14, INTP15, INTP16</li> <li>• Signals which are input via the ANI05, ANI015, ANI016 or ANI017 pin and output from a comparator.</li> </ul> <p>Register settings to which the restriction applies:</p> <ul style="list-style-type: none"> <li>• Bit 7 of INTNFC14, INTNFC15 or INTNFC16 register (corresponding to INTP14, INTP15, INTP16) = 0</li> <li>• Bit 7 of CMPNFCnL = 0 (n = 0 or 1)</li> <li>• Bit 7 of CMPNFCnF = 0 (n = 0 or 1)</li> </ul> <p>The Hi-Z signal output from the comparator, resulted from the input from the ANI05, ANI015, ANI016 or ANI017 is acknowledged normally. This restriction does not apply to other interrupts signals.</p> <p><u>Workaround</u></p> <p>In order to achieve an signal acknowledgement enable digital noise elimination for the pins or signals in question by setting following register:</p> <ul style="list-style-type: none"> <li>• Set bit 7 of the INTNFC14, INTNFC15 or INTNFC16 register = 1</li> <li>• Set bit 7 of CMPNFCnL = 1 (n = 0 or 1)</li> <li>• Set bit 7 of CMPNFCnF = 1 (n = 0 or 1)</li> </ul> <p>At the same time, select an appropriate sampling clock for the digital noise elimination.</p>	

**(C) Valid Specification**

<b>Item</b>	<b>Date published</b>	<b>Document No.</b>	<b>Document Title</b>
1	August 2007	U18279EJV0UD00	V850E/IF3, V850E/IG3 Hardware (User's Manual)
2	February 2004	U14559EJ3V1UM00	V850E1 Architecture (User's Manual)

**(D) Revision History**

<b>Item</b>	<b>Date published</b>	<b>Document No.</b>	<b>Comment</b>
1	May. 13, 2008	TPS-HE-B-4280	First release.

**Operating Precautions for QB-V850Ix3**

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<b>(A)</b>	<b>Table of Operating Precautions . . . . .</b>	<b>3</b>
<b>(B)</b>	<b>Description of Operating Precautions . . . . .</b>	<b>4</b>
<b>(C)</b>	<b>Valid Specification . . . . .</b>	<b>5</b>
<b>(D)</b>	<b>Revision History . . . . .</b>	<b>6</b>

