

Customer Notification

Applilet3 for V850ES/Jx3-L

Device Driver Configurator

Operating Precautions

AP3-V850ESJX3L-EE

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A) Table of Operating Precautions for AP3-V850ESJX3L-EE

Table A-1 Operating Precautions for AP3-V850ESJX3L-EE

No.	Outline	Version	AP3-V850ESJX3L-EE			
			V1.20	V2.01	V2.02	V2.10
A1	Missing API Reference		×	✓	✓	✓
A2	Invalid value for CPU clock (fCPU) selectable		×	×	✓	✓
A3	Wrong calculation of compare match values		×	×	✓	✓
A4	CSIB interrupt mask name in SendData() is wrong		-	-	×	×
A5	GUI settings of A/D module not loaded correctly		-	-	×	×
A6	With unchecked error callback in UARTC GUI wrong code is generated		-	-	×	×
A7	Unused modules not erased from project file		-	-	×	×

- ✓: Not applicable
- ×: Applicable
- : Not checked

B) Description of Operating Precautions for AP3-V850ESJX3L-EE

Table B-1 No. A1 Missing API Reference

<p><u>Details</u></p> <p>The online help consisting of User Guide and API reference is not available.</p> <p><u>Workaround</u></p> <p>To get a list of the possible API functions, please generate an output report. The output report includes two files generated in the project folder:</p> <ul style="list-style-type: none">- function.html -> summary of all API functions (used and unused functions of the current application)- macro.html -> summary of all preprocessor symbols

Table B-2 No. A2 Invalid value for CPU clock (fCPU) selectable

<p><u>Details</u></p> <p>Due to wrong calculation of main clock fxx in PLL mode it is possible to select an invalid CPU clock (fCPU) value. This behavior only applies to the V850ES/Jx3-L devices uPD70F3792 and uPD70F3793</p> <p><u>Workaround</u></p> <p>Please edit generated source files manually.</p>

Table B-3 No. A3 Wrong calculation of compare match values

<p><u>Details</u></p> <p>Due to wrong calculation of main clock fxx in PLL mode the setup of a timer (e.g. Timer M) with an arbitrary interval will generate an incorrect compare match value. This behavior only applies to the V850ES/Jx3-L devices uPD70F3792 and uPD70F3793.</p> <p><u>Workaround</u></p> <p>Please edit generated source files manually.</p>

Table B-4 No. A4 CSIB interrupt mask name in SendData() is wrong

<p><u>Details</u></p> <p>When CSIB is used as single transmission mode, the interrupt mask name in XXX_SendData() is wrong. In XXX_SendData(), interrupt is masked to secure pointer which is used into the interrupt function MD_INTCBnR/MD_INTCFnR.</p> <p><u>Workaround</u></p> <p>Please edit generated source files manually.</p> <p>[CSIB] wrong name: CBnTMK correct name: CBnRMK [CSIF] wrong name: CFnTMK correct name: CFnRMK</p>
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Table B-5 No. A5 GUI settings of A/D module not loaded correctly

<p><u>Details</u></p> <p>In A/D module, the GUI setting of "Conversion time mode" and "Conversion time" are not set correctly when project file is loaded.</p> <p><u>Workaround</u></p> <p>Please check and edit "Conversion time mode" and "Conversion time" after loading an Applilet project file.</p>

Table B-6 No. A6 With unchecked error callback in UARTC GUI wrong code is generated

<p><u>Details</u></p> <p>When error callback is unchecked at receive mode of UARTC GUI, generated code of MD_INTUCnR() is wrong.</p> <p><u>Workaround</u></p> <p>Please remove the following code lines from __interrupt void MD_INTUCnR(void) manually.</p> <pre> ----- __interrupt void MD_INTUCnR(void) { : else <-- should be deleted { { ((USHORT) rx_data_8); <-- } <-- } : } ----- </pre>

Table B-7 No. A7 Unused modules not erased from project file

Details

If a peripheral is disabled after it was used and code was generated at least once, the regarding source files are not excluded from the project.

Workaround

Manually exclude / erase the source files in the IAR / GHS project manager.

C) Supported Devices

ROM / RAM	V850ES/JC3-L		V850ES/JE3-L	V850ES/JF3-L	V850ES/JG3-L
	40pin	48pin	64pin	80pin	100 / 121 pin
512 / 40 Kbyte	-	-	-	-	70F3796 70F3793
384 / 40 Kbyte	-	-	-	-	70F3795
384 / 32 Kbyte	-	-	-	-	70F3792
256 / 40 Kbyte	-	-	-	-	70F3794
256 / 16 Kbyte	70F3838	70F3839	70F3840	70F3736	70F3738
128 / 8 Kbyte	70F3800	70F3804	70F3808	70F3735	70F3737
64 / 8 Kbyte	70F3799	70F3803	70F3807	-	-
32 / 8 Kbyte	70F3798	70F3802	70F3806	-	-
16 / 8 Kbyte	70F3797	70F3801	70F3805	-	-

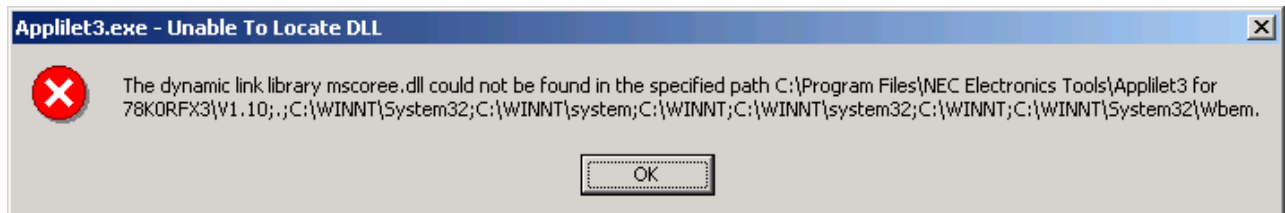
Supported
Not supported

D) Supported Operating Systems

Applilet supports Windows XP, Windows Vista and Windows 7.

For operation the Microsoft .NET Framework Version 2.0 Redistributable Package is necessary.

If the Microsoft .NET Framework Version 2.0 Redistributable Package is not installed, the following Windows error message will occur during application start:



The Microsoft .NET Framework Version 2.0 Redistributable Package can be downloaded from the Microsoft Corporation WEB Site: <http://www.microsoft.com/downloads>

Download Package: .NET Framework Version 2.0 Redistributable Package (x86)

Please use .NET Framework version 2.0 Redistributable package with latest patches.

E) Supported Development Environments

Renesas Electronics Development Tools	
Product Name	Version
Project Manager PM+	V6.31 or later
CA850	V3.40 or later
ID850QB	V3.60 or later
Device File Package DF70F3738	V2.00 or later

IAR Systems Development Tools	
Product Name	Version
Embedded Workbench for V850 EWW850	V3.50a or later
Device File Package DF-V850ES-Jx3-EE	V1.10 or later

Green Hills Development Tools	
Product Name	Version
GHS MULTI	V4.2.3 or later
Device File Package DF-V850ES-Jx3-EE	V1.10 or later

F) Valid Specification

Item	Date published	Document No.	Document Title
1	May 2009	U18952EJ3V0UD00	V850ES/JF3-L User's Manual
2	December 2009	U18953EJ5V0UD00	V850ES/JG3-L User's Manual
3	July 2010	R01UH0018EJ0001	V850ES/JC3-L and V850ES/JE3-L User's Manual
4	June 2010	R01UH0001EJ0001	V850ES/JG3-L (on-chip USB controller) User's Manual

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H) Revision History

Item	Date published	Document No.	Comment
1	04-Feb-2009	U20146EE1V0IF00	First Release
2	20-May-2010	R20TU0006ED0200_ESJx3L-URN	General update
3	20-May-2010	R20TU0006ED0201_ESJx3L-URN	Name changes
4	27-July-2010	R20TU0006ED0202_ESJx3L-URN	Items A2 and A3 added
5	05-Jan-2011	R20TU0006ED0203_ESJx3L-URN	Items A4 , A5 , A7 and A6 added

