

Customer Notification

AP3-78K0RIX3-EE

Applilet3 for 78K0R/lx3

Operating Precautions

78K0R/IB3: μPD78F1201, 78F1203

78K0R/IC3: μPD78F1211, 78F1213, 78F1214, 78F1215

78K0R/ID3: μPD78F1223, 78F1224, 78F1225

78K0R/IE3: μPD78F1233, 78F1234, 78F1235

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A) Table of Operating Precautions for AP3-78K0RIX3-EE

Table A-1 Table description

No.	Outline	Version	AP3-78K0RIX3-EE		
			V1.00		
A1	Unused modules not erased from project file		x		
A2	Generated I ² C Start condition of SAU function may violate I ² C specification		x		
A3	Generated I ² C Stop condition of SAU function may violate I ² C specification		x		

- ✓: Not applicable
- x: Applicable
- : Not checked

B) Description of Operating Precautions for AP3-78K0RIX3-EE

Table B-1 No. A1 Unused modules not erased from project file

<p><u>Details</u></p> <p>If a peripheral is disabled after it was used and code was generated at least once, the regarding source files are not excluded from the project.</p> <p><u>Workaround</u></p> <p>Manually exclude / erase the source files from the IAR Embedded Workbench project.</p>

Table B-2 No. A2 Generated I²C Start condition function of SAU may violate I²C specification

<p><u>Details</u></p> <p>Applilet generated code to send the start condition (<code>void IIC10_StartCondition(void)</code>) of an I²C frame does not take care to wait for $t_{HD;STA}$, specified by the I²C specification. The specified wait times are 4μs for Normal and 0.6μs for Fast mode.</p> <p><u>Workaround</u></p> <p>Set up a wait loop between “clear IIC10 SDA” and “clear IIC10 SCL”, which meets the I²C specification, manually.</p>
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Table B-3 No. A3 Generated I²C Stop condition function of SAU may violate I²C specification

<p><u>Details</u></p> <p>Applilet generated code to send the stop condition (<code>void IIC10_StopCondition(void)</code>) of an I²C frame does not take care to wait for $t_{SU;STO}$, specified by the I²C specification. The specified wait times are 4μs for Normal and 0.6μs for Fast mode.</p> <p><u>Workaround</u></p> <p>Set up a wait loop between “set IIC10 SCL” and “set IIC10 SDA”, that meets the I²C specification, manually.</p>

C) Supported Devices

ROM [KB]	78K0R/IB3	78K0R/IC3			78K0R/ID3	78K0R/LH3
	30 pins	38 pins	44 pins	48 pins	52 pins	128 pins
64				μPD78F1215	μPD78F1225	μPD78F1235
48				μPD78F1214	μPD78F1224	μPD78F1234
32	μPD78F1203	μPD78F1213	μPD78F1213	μPD78F1213	μPD78F1223	μPD78F1233
16	μPD78F1201	μPD78F1211	μPD78F1211			

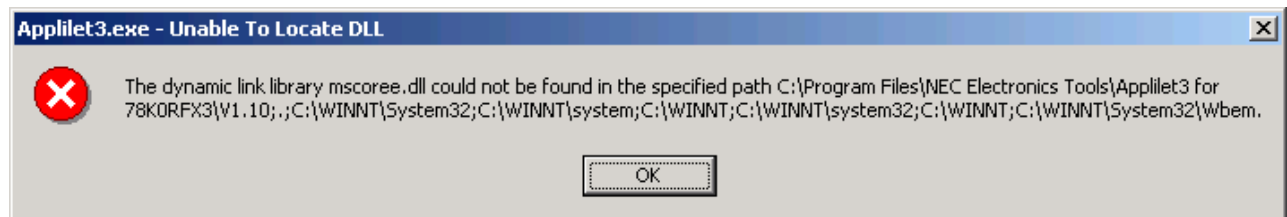
Supported
Not supported

D) Supported Operating Systems

Applilet supports Windows XP, Windows Vista and Windows 7.

For operation the Microsoft .NET Framework Version 2.0 Redistributable Package is necessary.

If the Microsoft .NET Framework Version 2.0 Redistributable Package is not installed, the following Windows error message will occur during application start:



The Microsoft .NET Framework Version 2.0 Redistributable Package can be downloaded from the Microsoft Corporation WEB Site:

<http://www.microsoft.com/downloads>

Download Package: .NET Framework Version 2.0 Redistributable Package (x86)

And, please use .NET Framework version 2.0 Redistributable package with latest patches.

E) Supported Development Environments

Renesas Electronics Development Tools	
Product Name	Version
Project Manager PM+	V6.30 or later
CC78K0	V4.00 or later
RA78K0	V4.01 or later
ID78K0R-QB	V3.10 or later
Device File Package DF78F0588	V2.00 or later

IAR Systems Development Tools	
Product Name	Version
Embedded Workbench for 78K EW78K	V4.62a or later

F) Valid Specification

Item	Date published	Document No.	Document Title
1	Nov 2009	U19678EJ1V1UD00	78K0R/Ix3 User's Manual

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H) Revision History

Item	Date published	Document No.	Comment
1	01-Feb-2011	R20TU0013ED0100_K0RIX3_URN	First release.
2	20-Jun-2011	R20TU0013ED0101_K0RIX3_URN	Item A2 and A3 added

