

Customer Notification

μ PD789852 SubseriesTM

8-bit Single-Chip Microcontrollers

Operating Precautions

μ PD789851

μ PD789852

μ PD78F9852

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(A) Table of Operating Precautions

| No. | Outline | μPD78985x | | | μPD78F9852 | |
|-----|--|----------------------|-----|--|------------|----|
| | | Rev. | all | | DS, ES | MP |
| | | Rank ^{Note} | | | | |
| 1 | DCAN High speed RX Loss and manipulated TX ID (Specification Change) | ✓ | | | X | ✓ |
| 2 | DCAN REDEF Function (Direction of use) | X | | | X | X |
| 3 | DCAN Extended Identifier (Direction of use) | X | | | X | X |
| | | | | | | |

✓ : Not applicable

X : applicable

Note: The rank is indicated by the letter appearing at the 5th position from the left in the lot number, marked on each product.

(B) Description of Operating Precautions

| | |
|-------|---|
| No. 1 | DCAN High speed RX Loss and manipulated TX ID (Specification Change) |
| | <p><u>Details</u> For detailed description, pls. refer to the document EACT-BR-5004-1.0.pdf or later.</p> |
| No. 2 | DCAN REDEF Function (Direction of use) |
| | <p><u>Details</u> Issue REDEF function only directly after 'bus idle' was detected. Use RXF and TXF bits in CAN Control Register CANC for this purpose and disable all interrupt during these operations. Alternatively the regular initialization mode can be used for re-configuration of the message buffer area or when REDEF was used to provide data consistency, this method needs to be replaced by the normal method using DN and MUC bit.</p> <p>For detailed description, pls. refer to the document EACT-BR-5006-1.0 or later.</p> |
| No. 3 | DCAN Extended Identifier (Direction of use) |
| | <p><u>Details</u> Pls. use Extended Identifiers only, if it can be guaranteed, that there are no two Extended Identifiers available on the CAN bus, which are identical within their Standard Identifier part. Otherwise the data contents of messages with same Standard-ID part, but differing within the Extended-ID part, can be mixed, may be lost or wrong stored, while error frames or stuff bit errors occur on the CAN bus within specific time slots.</p> <p>For detailed description pls. refer to the document EACTBR-5010-1.2 or later.</p> |

(C) Valid Specification

| Item | Date published | Document No. | Document Title |
|-------------|-----------------------|---------------------|---|
| 1 | March 2004 | U15814EJ2V0UD00 | μ PD789852 Subseries User's Manual |
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(D) Revision History

| Item | Date published | Document No. | Comment |
|-------------|-----------------------|---------------------|--|
| 1 | November 8, 2002 | TPS-LE-OP-9852 | 1 st Release |
| 2 | May 28, 2003 | TPS-LE-OP-9852-1 | 1 st Update DS, ES included in (A) Table of Operating Precautions |
| 3 | April 4, 2005 | TPS-LE-OP-9852-2 | 2 nd Update Item 3 added. |
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