NEC

Customer Notification

μPD789850 Subseries
μPD789850A Subseries™

8-bit Single-Chip Microcontrollers

Operating Precautions

μPD789850
μPD78F9850
μPD789850A
μPD78F9850A

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No.	Outline		µPD789850			µPD78F9850		
		Rev.	MP			MP		
		Rank Note	all			all		
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2	DCAN Change of DCAN Controller Control Register names (Specification Change)		✗			✗		
3	DCAN High speed RX Loss and manipulated TX ID (Specification Change)		✗			✗		
4	DCAN REDEF Function (Direction of use)		✗			✗		
5	Flash Programming (Specification Change)		No Flash			✗		
6	DCAN RXONLY Mode (Specification Change)		✗			✗		
7	DCAN Extended Identifier (Direction of use)		✗			✗		

✓ : Not applicable
✗ : applicable

Note: The rank is indicated by the letter appearing at the 5th position from the left in the lot number, marked on each product.

No.	Outline		µPD789850A			µPD78F9850A		
		Rev.	MP			MP		
		Rank Note	all			all		
1	16-bit Timer TM0 One-shot pulse output (Specification Change)		✗			✗		
2	DCAN Change of DCAN Controller Control Register names (Specification Change)		✗			✗		
4	DCAN REDEF Function (Direction of use)		✗			✗		
5	Flash Programming (Specification Change)		No Flash			✗		
7	DCAN Extended Identifier (Specification Change)		✗			✗		

✓ : Not applicable
✗ : applicable

Note: The rank is indicated by the letter appearing at the 5th position from the left in the lot number, marked on each product.

(B) Description of Operating Precautions

No. 1	16-bit Timer TM0 One-shot pulse output (Specification Change)
	Details The One-shot pulse output function of 16-bit Timer TM0 is deleted.

No. 2	DCAN Change of DCAN Controller Control Register names (Specification Change)

Details

Address	Old name	New Name
FFB1H	CANC0	CANC
FFB2H	TCR0	TCR
FFB3H	RMES0	RMES
FFB4H	REDEF0	REDEF
FFB4H.7 (bit)	DEFEN	DEF
FFB5H	CANES0	CANES
FFB6H	TEC0	TEC
FFB7H	REC0	REC
FFB8H	MCNT0	MCNT
FFB9H	BRPRS0	BRPRS
FFBAH	SYNC00	SYNC0
FFBBH	SYNC01	SYNC1
FFBCH	MASKC0	MASKC

No. 3	DCAN High speed RX Loss and manipulated TX ID (Specification Change)
	Details For detailed description, pls. refer to the document EACT-BR-5004-1.0.pdf or later.

No. 4	DCAN REDEF Function (Direction of use)
	Details Issue REDEF function only directly after 'bus idle' was detected. Use RXF and TXF bits in CAN Control Register CANC for this purpose and disable all interrupts during these operations. Alternatively the regular initialization mode can be used for re-configuration of the message buffer area or when REDEF was used to provide data consistency, this method needs to be replaced by the normal method using DN and MUC bit. For detailed description, pls. refer to the document EACT-BR-5006-1.0.pdf or later.

No. 5	Flash Programming (Specification Change)
	Details When manipulating (erasing, writing) the flash memory of the target product using a flash programmer, the below listed communication conditions cannot longer be used. Communication mode: UART Communication speed: 4800Bd Operating frequency of target device fx: 8MHz

No. 6	DCAN RXONLY Mode (Specification Change)
	Details The RYONLY Mode of the DCAN is deleted. For detailed description pls. refer to the document EACT-CN-5001-1.0.pdf or later.

No. 7	DCAN Extended Identifier (Direction of use)
	Details Pls. use Extended Identifiers only, if it can be guaranteed, that there are no two Extended Identifiers available on the CAN bus, which are identical within their Standard Identifier part. Otherwise, the data-contents of messages with same Standard ID-part, but differing within the Extended ID-part, can be mixed, may be lost or wrong stored, while error frames or stuff bit errors occur on the CAN-bus within specific time-slots. For detailed description pls. refer to the document EACT-BR-5010-1.2 or later.

(C) Valid Specification

Item	Date published	Document No.	Document Title
1	February 2003	U16532E or later	µPD789850A Subseries User's Manual
2	August 2002	U14403Eor later	µPD789850 Subseries User's Manual

(D) Revision History

Item	Date published	Document No.	Comment
1	December 13, 2002	TPS-LE-OP-9850A	1st Release
2	March 9, 2005	TPS-LE-OP-9850A-1	1st Update Merging of documents TPS-LE-OP-9850-1 and TPS-LE-OP-9850A µPD789850 Subseries: Revision of items 1 to 7 µPD789850A Subseries: Revision of items 1 to 7