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Concerned Products: IE-78001-R-A	Customer Notification		Date: February 28 th , 2000
			NEC-Electronics (Europe) GmbH EAD – Technical Product Support
	Bug Report		Source Doc: SBG-T-0423-2, SBB-T-9526, SBG-T-0569, SBG-T-1914, SBG-T-1913
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Date of initial issue:	Nov. 18th, 98	Doc. No.:	TPS-LE-B-0T001
1 st revision	: Jan. 29 th , 99	Doc. No.:	TPS-LE-B-0T001-1
2 nd revision	: Feb. 28 th , 00	Doc. No.:	TPS-LE-B-0T001-2

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(A) BUG LIST (1/2)

Bug No.	Outline	IE-78001-R-A chassis						
		Level "A"	Level "B"	Level "C"	Level "D"	Level "E"	Level "F"	Level "G"
10	Transistor TR1	☛	☛	☛	☞	✓	✓	✓
11	Fetch opcode of external memory	☛	☛	☛	☛	☞	✓	✓
12	Emulation Memory	☛	☛	☛	☛	☛	☞	✓

✓: No problem

☞: Bug (will be corrected by next version upgrade)

☛: Bug (restriction, not corrected by version upgrade)

Level "n": The level of the IE-78001-R-A chassis is given on a sign at the backside of the emulator chassis. It is defined as the second character of the serial number (as example: (EDnnnnnnn -> Level "D").

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BUG LIST (2/2)

Bug No.	Outline	IE-78001-R-BK					
		V1.00 Code "A"	V1.11 Code "B"	V1.22 Code "C"	V1.33 Code "D"	V1.43 Code "E"	V1.53 Code "F"
1	Time measurement function	☛	☛	☞	✓	✓	✓
2	Trace full break	☛	☛	☞	✓	✓	✓
3	Port trace	☛	☛	☞	✓	✓	✓
4	Serial buffer RAM	☛	☛	☞	✓	✓	✓
5	External Probe	☛	☛	☞	✓	✓	✓
6	FPGA data download	☛	☛	☞	✓	✓	✓
7	Port 6	☛	☛	✓	✓	✓	✓
8	Port mode PM3	☛	☛	✓	✓	✓	✓
9	Restart of ID78K0	☛	☛	☞	✓	✓	✓
11	Fetch opcode of external memory	☛	☛	☛	☞	✓	✓
12	Emulation Memory	☛	☛	☛	☛	☞	✓

- ✓: No problem
- ☞: Bug (will be corrected by next version upgrade)
- ☛: Bug (restriction, not corrected by version upgrade)

Level "n": The level of the IE-78001-R-BK chassis is given as the second character of the serial number (as example: (EEEnnnnnn -> Level "E").

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(B) BUG DESCRIPTION

Bug No.	Outline	Description
1	Time measurement function	The time measurement function of the emulator is not supported.
2	Trace full break	The trace full break function is not supported.
3	Port trace	The port trace function is not supported.
4	Serial buffer RAM	The read/write command to the serial buffer RAM is not supported.
5	External Probe	The external probe function is not supported with SL-type emulation boards.
6	FPGA data download	The automatic download of the FPGA data for the emulation boards is not supported.
7	Port 6	P60, P61, P62 and P63 do not work with the R-type emulation board. The output buffer is CMOS-type instead of open drain-type.
8	Port mode PM3	Port mode register PM3 can not be changed with the R-type emulation board.
9	Restart of ID78K0	The restart of the debugger ID78K0 is not possible with the automatic download of the FPGA data for the break board IE-78001-R-BK.
10	Transistor TR1	When the following emulation boards are used the power generation transistor on the break board IE-78001-R-BK may be damaged. The following phenomena can occur: - The interrupt flag is set correctly, but the wrong vector is executed - Interrupts do not occur - The values of P4, P5 and P6 are not input/output correctly. - The external bus access function does not operate normally. - The debugger can not be started. Low-voltage-compatible emulation boards where the bug occurs: IE-78014-R-EM-A, IE-78064-R-EM, IE-780308-R-EM, IE-78078-R-EM, IE-178018-R-EM
11	Fetch opcode of external memory	The fetch opcode from the external memory is not supported for the SL-/NS-type emulation boards.

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12	Emulation Memory	An illegal value may be displayed in the memory window when the IE-78001-R-A is used with the IE-78K0-NS-P04 and the necessary probe board. In fact that the value of the data is correct, it does not affect the program execution.
a		
b		An illegal, randomised data may be stored in the emulation memory after the downloaded of the application software when the IE-78001-R-BK is used in combination with the IE-78K0-NS-P04.
		The IE-78001-R-A (including the IE-78001-R-BK) operates correctly in other combinations.