

Customer Notification

IE-703178-MC-EM1

In-Circuit-Emulator Option Board

Operating Precautions

Target Device

V850E/CG3,CG4TM CarGate+/CarGate-3G

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(A) Table of Operating Precautions

No.	Outline	IE-703178-MC-EM1			
		Ctrl.-Code	A	B	C
		Date	10.12.03	21.05.04	27.04.05
		HW Version	V1.00	V1.10	V1.11
1	Not readable SFR's	Y	N	N	
2	Combined receive and error interrupts of serial interface not available	Y	N	N	
3					
4					
5					
6					

N : Not applicable

Y : Applicable

Note: The control code is the second letter from the left of the 10 digit serial number or in case of update the latest control code is mentioned on the version up sticker.

(B) Description of Operating Precautions

No. 1	Not readable SFR's
	<p><u>Details:</u> The following SFR's couldn't be read out: IMR* 0xFFFFF100..0xFFFFF10F *IC 0xFFFFF110..0xFFFFF184 ISPR 0xFFFFF1FA PSC 0xFFFFF1FE</p> <p><u>Workaround:</u> None</p>
No. 2	Combined receive and error interrupts of serial interface not available
	<p><u>Details:</u> The combined receive and error interrupts of CSIB0-2 and UART0-1 aren't available. These are: INTCB0R to INTCB2R, INTUA0R to INTUA1R and the dedicated ICR-register.</p> <p><u>Workaround:</u> Use of the dedicated receive and error interrupts for the serial interfaces: For receive: INTCB0RD to INTCB2RD and INTUA0RD to INTUA1RD. For error handling: INTCB0RE to INTCB2RE and INTUA0RE to INTUA1RE</p>
No.	
	<p><u>Details:</u> <u>Workaround:</u></p>

(C) Valid Specification

Item	Date published	Document No.	Document Title
1	April 2005	U17556EE1V0UM00	Preliminary User's Manual
2	May 2005	EEDT-OP-0020-3.1	This document

(D) Revision History

Item	Date published	Document No.	Comment
1	December 2003	EEDT-OP-0020-1.0	1st release
2	May 2004	EEDT-OP-0020-2.0	1st update
3	April 2005	EEDT-OP-0020-3.0	2nd update (Precautions 1,2,3 & 6 from V2.0 shifted to U17556EE1V0UM00)
4	May 2005	EEDT-OP-0020-3.1	3rd update (Update of Revision History)