

RENESAS TECHNICAL UPDATE

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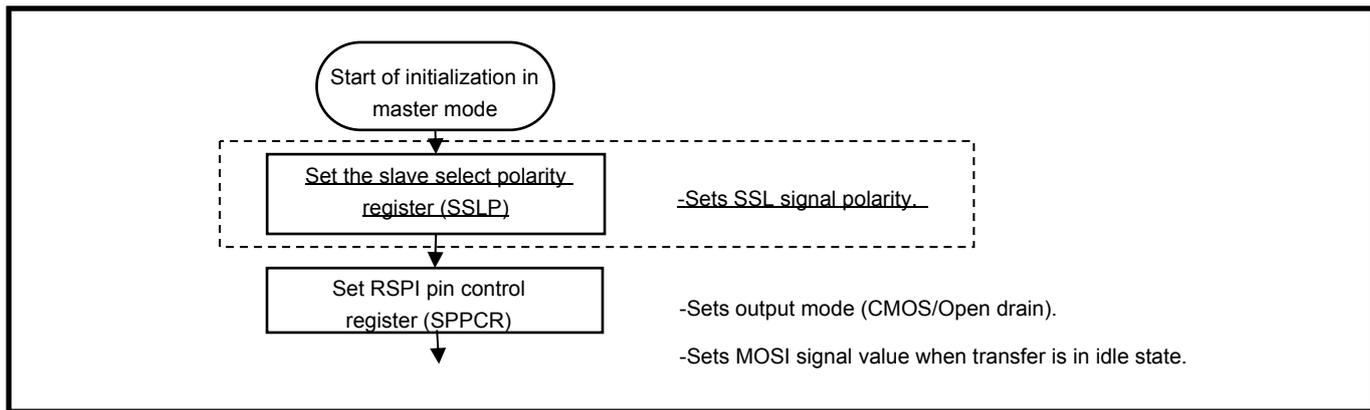
Product category	MPU & MCU	Document No.	TN-RX*-A094A/E	Rev.	1.00
Title	Notes on Initialization Flow of Serial Peripheral Interface (RSPI) in Master Mode of the RX600 Series and RX200 Series		Information category	Technical information	
Applicable products	RX62N Group, RX621 Group, RX62G Group, RX62T Group, RX630 Group, RX63N Group, RX631 Group, RX63T Group, RX210 Group, RX21A Group, RX220 Group	Lot No.	Reference information	RX62N Group, RX621 Group User's Manual: Hardware Rev.1.30 (R01UH0033JJ0130) RX62T Group, RX62G Group User's Manual: Hardware Rev.2.00 (R01UH0034JJ0200) RX630 Group User's Manual: Hardware Rev.1.50 (R01UH0040JJ0150) RX63N Group, RX631 Group User's Manual: Hardware Rev.1.70 (R01UH0041JJ0170) RX63T Group User's Manual: Hardware Rev.2.10 (R01UH0238JJ0210) RX210 Group User's Manual: Hardware Rev.1.50 (R01UH0037JJ0150) RX21A Group User's Manual: Hardware Rev.1.00 (R01UH0251JJ0100) RX220 Group User's Manual: Hardware Rev.1.10 (R01UH0292JJ0110)	
		All			

1. Notes

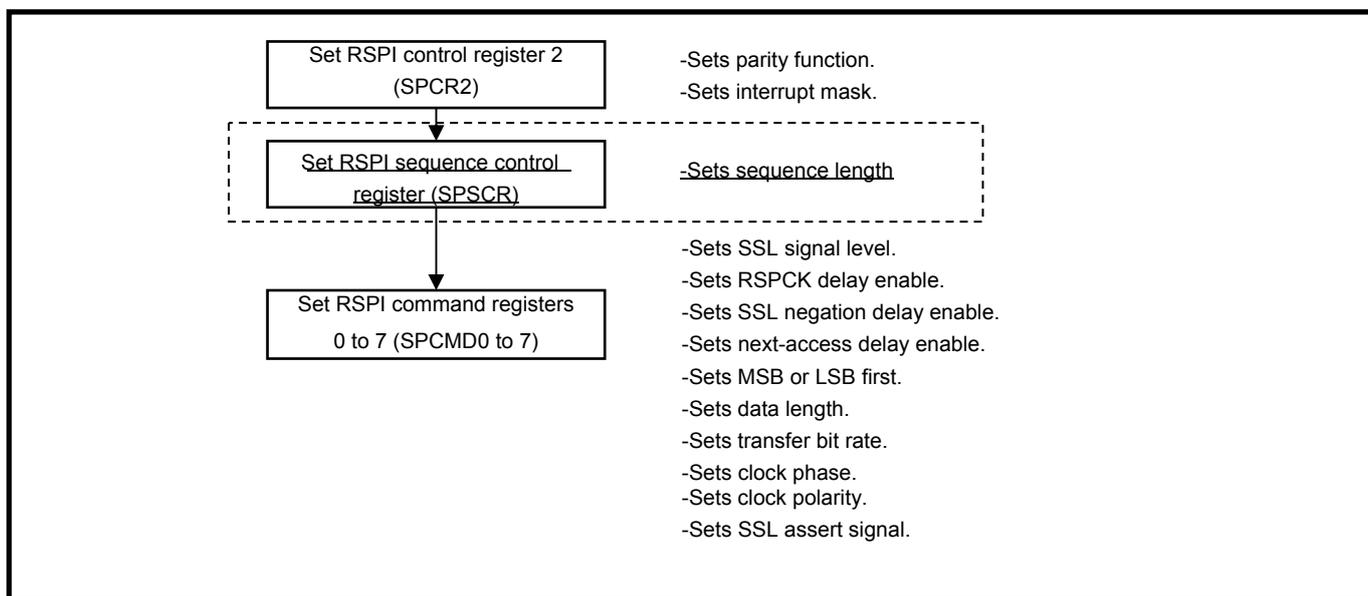
In the figure of "Example of Initialization Flowchart in Master Mode (SPI Operation)" in the "(8) Initialization Flowchart" of "Master Mode Operation" in "SPI Operation" of "Operation" section in the "Serial Peripheral Interface (RSPI)" chapter of the User's Manual: Hardware of the applicable products mentioned above, when the SSLn signal (n = 0 to 3) is active high, the corresponding bit of the RSPI Slave Select Polarity Register (SSLP register) must be set to 1. In addition, when the RSPI sequence length is any value other than "1", the sequence length must be set to the RSPI sequence control register (SPSCR).

2. Measures

With the settings mentioned above, add processing indicated with dotted lines in "Example setting of RSPI slave select polarity register (SSLP)" and "Example setting of RSPI sequence control register (SPSCR)" shown in the following page to the figure of "Example of Initialization Flowchart in Master Mode (SPI Operation)".



Example setting of the RSPI slave select polarity register (SSLP)



Example setting of the RSPI sequence control register (SPSCR)