

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A103A/E	Rev.	1.00
Title	Note on Using Port Output Enable 2 (POE2a) in the RX111 Group		Information Category	Technical Notification		
Applicable Product	RX111 Group	Lot No.	Reference Document	RX111 Group User's Manual: Hardware Rev.1.10 (R01UH0365EJ0110)		
		All				

1. Note

When the high-impedance control by detecting the input level is used, the pin may be placed in the high-impedance state immediately after switching the pin function from the general I/O port to POE0#, POE1#, POE2#, POE3#, or POE8#.

2. Measure

When using POE0#, POE1#, POE2#, POE3#, or POE8# for detecting the input level, set the pin function as a POE input pin by registers PMR and PmnPFS and clear flags ICSR1.POEkF and ICSR2.POE8F before starting the MTU (k = 0 to 3).